

CC6902

Single chip Hall effect current sensor

5 A/ 5 B/ 1 0 A/ 2 0 A/ 3 0 A series

Overview

CC6902 It is a high-performance single-ended output linear current sensor, which can be more effective for AC (AC) Or DC (DC) Current detection solutions are widely used in industrial, consumer and communications equipment.

CC6902 A high-precision, low-noise linear Hall circuit and a low-impedance main current wire are integrated inside. When the sampling current flows through the main current wire, the magnetic field generated by it induces a corresponding electrical signal on the Hall circuit, and the signal processing circuit outputs a voltage signal, making the product easier to use. Linear Hall circuit adopts advanced BiCMOS Process production, including high-sensitivity Hall sensor, Hall signal pre-amplifier, high-precision Hall temperature compensation unit, oscillator, dynamic offset cancellation circuit and amplifier output module. In the absence of a magnetic field, the static output is $\pm 0\%$ VCC.

At power supply voltage ± 5 V Under conditions, OUT allowable ± 0.5 ~ 4.5 V Varies linearly with the magnetic field, the linearity can reach $\pm 0.4\%$. CC6902 The internal integrated dynamic offset cancellation circuit enables IC The spirit Sensitivity is not subject to external pressure and IC The influence of package stress.

CC6902 provide SOP8 Package, operating temperature range- $40 \sim 125^{\circ}\text{C}$.

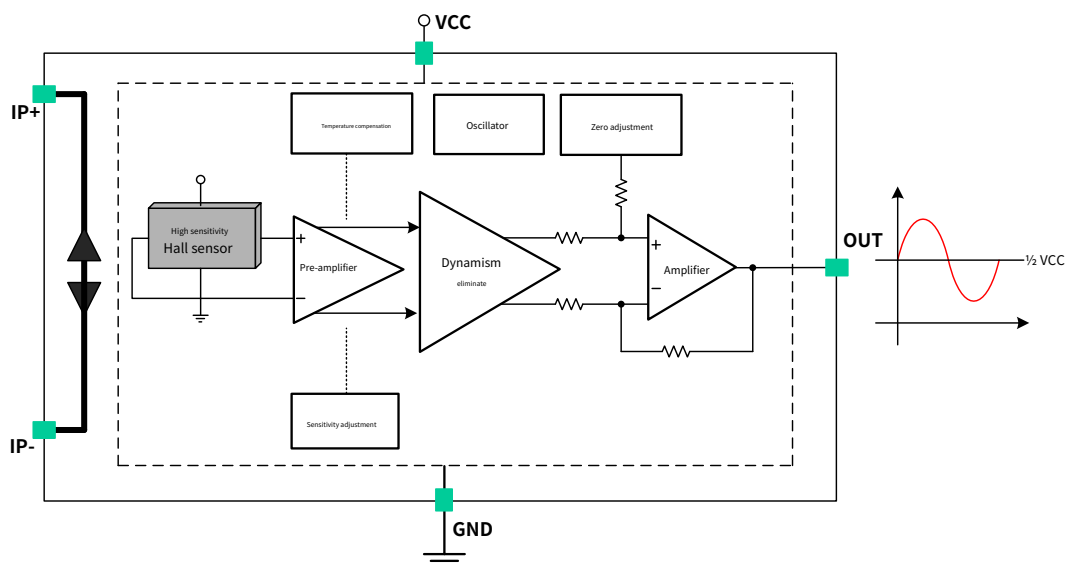
characteristic

- ◆ The static common mode output point is $\pm 0\%$ VCC
- ◆ Wide measuring range, 5 A/ 5 B/ 1 0 A/ 2 0 A/ 3 0 A 1 MHz Chopping frequency,
- ◆ high bandwidth, low noise, single-ended analog output wire pin to signal pin
- ◆ ± 2000 Vrms Safe isolation voltage and low power consumption
- ◆ Room temperature error $\pm 1\%$, Total temperature error $\pm 3\%$
- ◆ The temperature stability is good, and the internal use of the patented Hall signal amplifier circuit and temperature Degree compensation circuit
- ◆ Strong anti-interference ability
- ◆ Resistance to mechanical stress, magnetic parameters will not deviate due to external pressure
- ◆ ESD (HBM) 6000 V
- ◆ by UL 62368 -1:2014 test

application

- ◆ motor control
- ◆ Load monitoring system
- ◆ Switching power supply
- ◆ Overcurrent fault protection

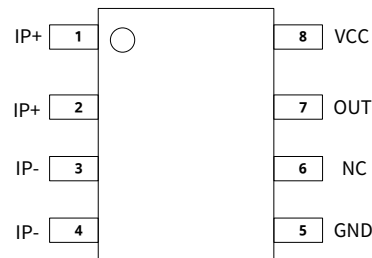
Functional block diagram



Ordering Information

product name	Sensitivity (mV/A)	Package outline	package
CC6902SO-05A	4 0 0	SOP8	Taping, 2 0 0 0 Piece/disk
CC6902SO-05B	1 8 5	SOP8	Taping, 2 0 0 0 Piece/disk
CC6902SO-10A	2 0 0	SOP8	Taping, 2 0 0 0 Piece/disk
CC6902SO-20A	1 0 0	SOP8	Taping, 2 0 0 0 Piece/disk
CC6902SO-30A	6 7	SOP8	Taping, 2 0 0 0 Piece/disk

Pin definition



SOP8 Encapsulation

name	Numbering	Features	name	Numbering	Features
IP+	1	Sampling current positive terminal	GND	5	Ground
IP+	2	Sampling current positive terminal	NC	6	Need to be suspended
IP-	3	Sampling current negative terminal	OUT	7	Signal output
IP-	4	Sampling current negative terminal	VCC	8	voltage

Limit parameters

parameter	symbol	Numerical value	unit
voltage	V _{CC}	7	V
The output voltage	V _{OUT}	-0.3~V _{CC} +0.3	V
Output source current	I _{OUT(SOURCE)}	4 0 0	uA
Output sink current	I _{OUT(SINK)}	3 0	mA
Universal insulation voltage	V _{ISO}	2 0 0 0	VAC
Working temperature	T _a	-40~125	°C
Maximum junction temperature	T _J	1 6 5	°C
Storage temperature	T _s	-55~150	°C
Magnetic field strength	B	Unlimited	mT
Electrostatic protection	ESD(HBM)	6 0 0 0	V
Transient inrush current at current sampling terminal	IP	1 pulse, 1 0 0 ms	1 0 0 A

Note: Do not exceed the maximum rating during application to prevent damage. Long-term operation at the maximum rating may affect the reliability of the device.

Isolation characteristics

parameter	symbol	Test conditions/notes	Numerical value	unit
Dielectric strength test voltage *	V _{ISO}	Type testing continues 1 min UL standard 6 2 3 6 8 -1:2014	2 5 0 0	V _{DC}
Dielectric strength test voltage *	V _{ISO}	Type testing continues 1 min	2 0 0 0	V _{RMS}
Basic insulation working voltage	V _{WFSI}	Basic insulation UL standard 6 2 3 6 8 -1:2014	4 2 0	V _{DC} or V _{PEAK}
			2 9 7	V _{RMS}
Electrical clearance		Input end to output end, the shortest distance	3 .8	mm

Note: * The core is not in progress 1 Minute test, only during type test.

Recommended working environment

parameter	symbol	Minimum	Max	unit
voltage	V _{CC}	4 .5	5 .5	V
Ambient temperature	T _a	-40	1 2 5	°C
DC current capacity	I _P	-30	3 0	A

Note: The actual current capacity of the chip should be determined according to the thermal resistance of the chip and the actual ambient temperature.

Working characteristics (Unless otherwise specified, V_{CC}= 5 V @ 2 5 °C)

parameter	symbol	condition	Minimum	Typical value	Max	unit
Electrical characteristics						
Supply voltage	V _{CC}	-	4 .5	-	5 .5	V
Quiescent Current	I _{CC}	OUT Hang in the air	-	5	8	mA
Output capacitive load	C _L		-	-	1	nF
Output resistance load	R _L		2 0	-	-	kΩ
Transmission delay time	t _D			1	1 .2	us
Rise Time	t _r		-	2	3 .6	us
System bandwidth	BW	-3dB	-	8 0	-	kHz
Linearity error	L _{inERR}		-	0 .4	1	%
Symmetry error	S _{ymERR}		-	0 .8	1 .5	%
Static output point	V _{OUT(Q)}		2 .48	2 .5	2 .52	V
PORTime	T _{POR}	Output from 0 To 9 0 %	-	1 0	-	us
Main current terminal resistance	R _P		-	1 .5	1 .8	mΩ
Junction to ambient thermal resistance	θ _{JA}	Copper foil is connected to 1 , 2 Feet and 3 , 4 Feet with an area of 1 5 0 0 mm ² , thickness 2 OZ	-	2 5	-	°C/W

5 Aseries

parameter	symbol	condition	Minimum	Typical value	Max	unit
Electrical characteristics						
Current range	I _P	-	-5	-	5	A
Sensitivity	Sens	Full current range	3 9 0	4 0 0	4 1 0	mV/A
Output noise	V _{NOISE(PP)}		-	5 0	-	mV
Zero current output temperature coefficient	ΔV _{OUT(Q)}		-	0 .26	-	mV/°C
Sensitivity temperature coefficient	ΔSens		-	0 .054	-	mV/A /°C
Total output error	E _{TOT}		-3.0	-	3 .0	%

5 Bseries

parameter	symbol	condition	Minimum	Typical value	Max	unit
Electrical characteristics						
Current range	I _P	-	-5	-	5	A
Sensitivity	Sens	Full current range	1 8 0	1 8 5	1 9 0	mV/A
Output noise	V _{NOISE(PP)}		-	3 0	-	mV
Zero current output temperature coefficient	ΔV _{OUT(Q)}		-	0 .29	-	mV/°C
Sensitivity temperature coefficient	ΔSens		-	0 .028	-	mV/A /°C
Total output error	E _{TOT}		-3.0	-	3 .0	%

1 0 Aseries

parameter	symbol	condition	Minimum	Typical value	Max	unit
Electrical characteristics						
Current range	I _P	-	-10	-	1 0	A
Sensitivity	Sens	Full current range	1 9 5	2 0 0	2 0 5	mV/A
Output noise	V _{NOISE(PP)}		-	3 0	-	mV
Zero current output temperature coefficient	ΔV _{OUT(Q)}		-	0 .30	-	mV/°C
Sensitivity temperature coefficient	ΔSens		-	0 .027	-	mV/A /°C
Total output error	E _{TOT}		-3.0	-	3 .0	%

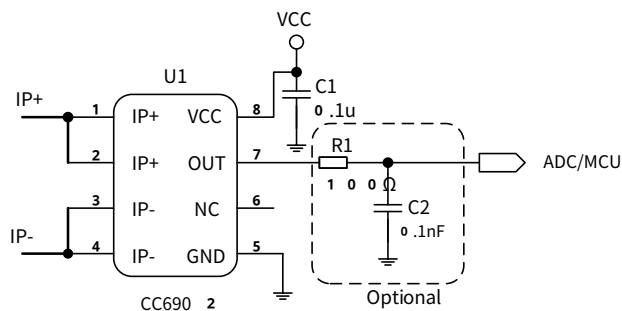
2 0 Aseries

parameter	symbol	condition	Minimum	Typical value	Max	unit
Electrical characteristics						
Current range	I _P	-	-20	-	2 0	A
Sensitivity	Sens	Full current range	9 5	1 0 0	1 0 5	mV/A
Output noise	V _{NOISE(PP)}		-	2 0	-	mV
Zero current output temperature coefficient	ΔV _{OUT(Q)}		-	0 .34	-	mV/°C
Sensitivity temperature coefficient	ΔSens		-	0 .017	-	mV/A /°C
Total output error	E _{TOT}		-3.0	-	3 .0	%

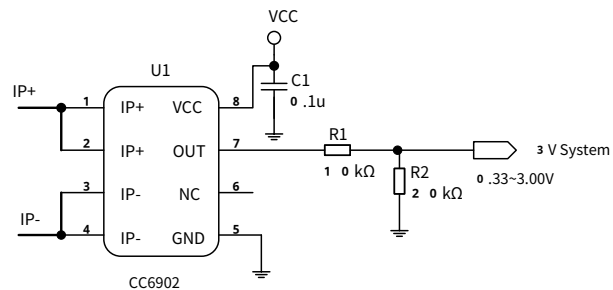
3 0 A series

parameter	symbol	condition	Minimum	Typical value	Max	unit
Electrical characteristics						
Current range	I_P	-	-30	-	30	A
Sensitivity	Sens	Full current range	64	67	70	mV/A
Output noise	$V_{NOISE(PP)}$		-	20	-	mV
Zero current output temperature coefficient	$\Delta V_{OUT(Q)}$		-	0.35	-	mV/°C
Sensitivity temperature coefficient	$\Delta Sens$		-	0.010	-	mV/A/°C
Total output error	E_{TOT}		-3.0	-	3.0	%

Typical application circuit



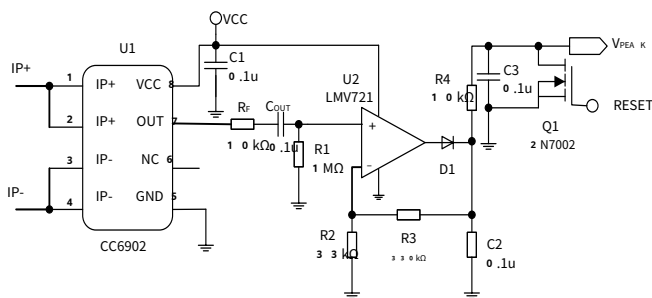
typical application



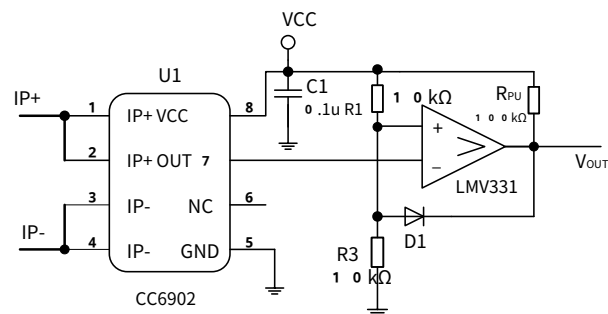
Signal attenuation circuit

Note: I_{OUT} < 0.3 mA, Drive capacity according to 0.25mA Calculation, sum of resistance

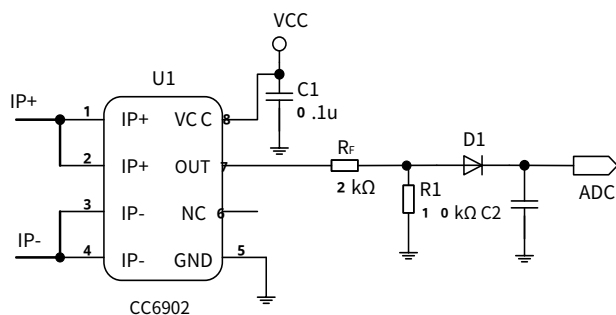
(R1+R2) Need to be greater than 20kΩ



Current peak monitoring application



Overcurrent fault detector



Rectified output, instead of current transformer application

Output characteristics

CC6902 Static output point ($I_P = 0$ A When) is $V_{CC} / 2$.

When the current increases, V_{OUT} increases until the saturation voltage of the output op amp ($V_{CC} - \text{Rail voltage}$); when the current decreases, V_{OUT} decreases until the saturation voltage of the output op amp (GND + Rail voltage). Core guarantee V_{OUT} in $0.5 \sim 4.5V$. In order to ensure the consistency of mass manufacturing, there is a certain margin in this range, but it is not recommended for customers to use this margin.

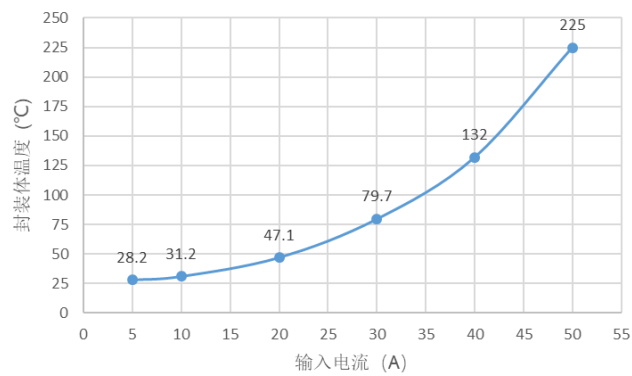
When the input current exceeds the range, V_{OUT} The output is close to the rail voltage of the power supply. When the input current does not exceed the withstand limit of the chip, the voltage will always be maintained and the input current will return to the range

After being within the range, V_{OUT} The output will return to normal without causing any damage to the chip.

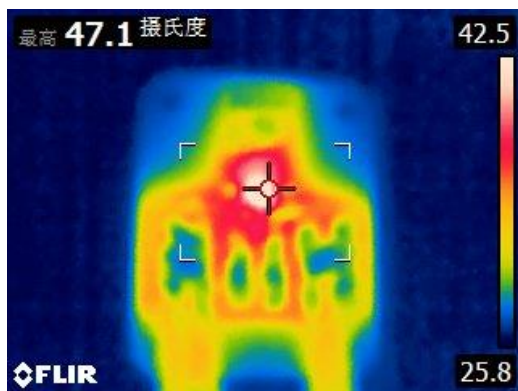
product name	Input Current	Sensitivity (mV/A)	Calculation formula (Note 1)
CC6902SO-05A	-5A ~ +5A	400	$V_{OUT} = V_{CC} / 2 + 0.400 \times I_P(A) \cdots \cdots (V)$
CC6902SO-05B	-5A ~ +5A	185	$V_{OUT} = V_{CC} / 2 + 0.185 \times I_P(A) \cdots \cdots (V)$
CC6902SO-10A	-10A ~ +10A	200	$V_{OUT} = V_{CC} / 2 + 0.200 \times I_P(A) \cdots \cdots (V)$
CC6902SO-20A	-20A ~ +20A	100	$V_{OUT} = V_{CC} / 2 + 0.100 \times I_P(A) \cdots \cdots (V)$
CC6902SO-30A	-30A ~ +30A	67	$V_{OUT} = V_{CC} / 2 + 0.067 \times I_P(A) \cdots \cdots (V)$

Note 1: This formula is only applicable to the calculation of DC current, when AC current is applied, please pay attention $I_{PK} < 1.414 \times I_{RMS}$. And pay attention to the positive or negative direction of the current.

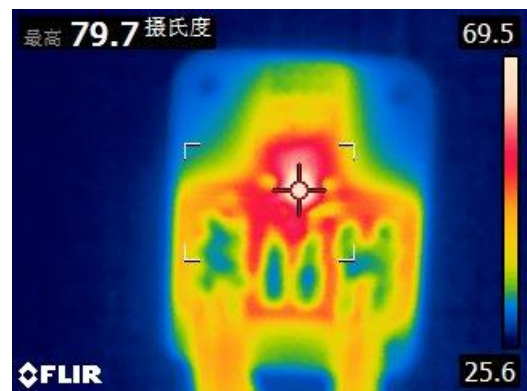
The relationship between package temperature and input current



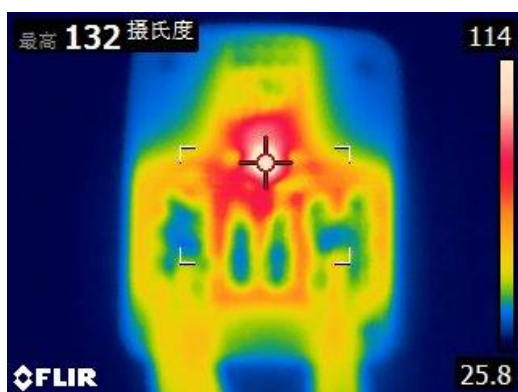
Input Current IP vs. Package temperature



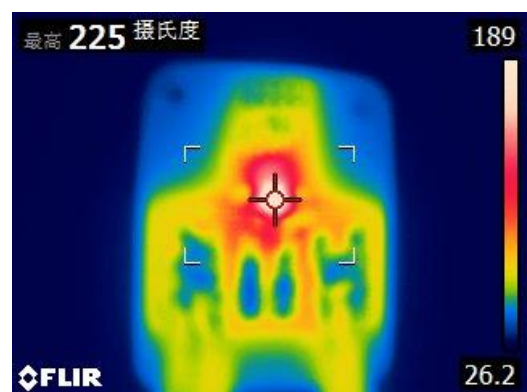
Package body thermal imaging diagram (input current 20 A)



Package body thermal imaging diagram (input current 30 A)

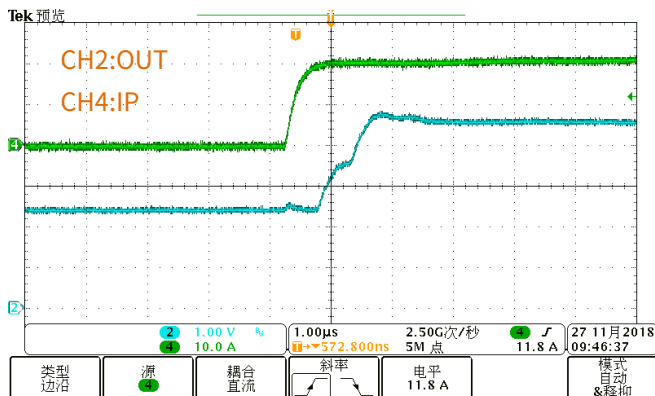


Package body thermal imaging diagram (input current 40 A)

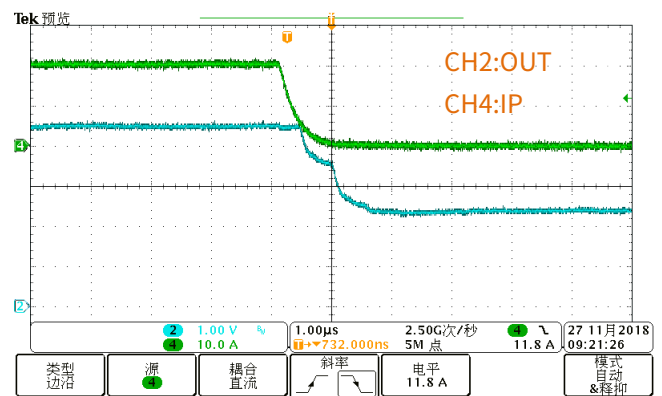


Package body thermal imaging diagram (input current 50 A)

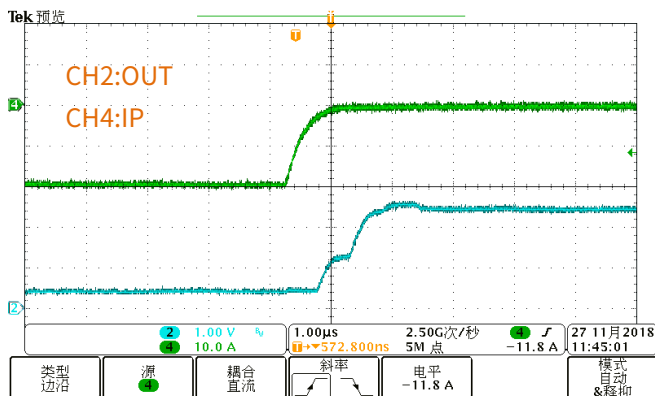
Curve & Wave (Unless otherwise specified, $V_{CC} = 5\text{ V}$ @ 25°C)



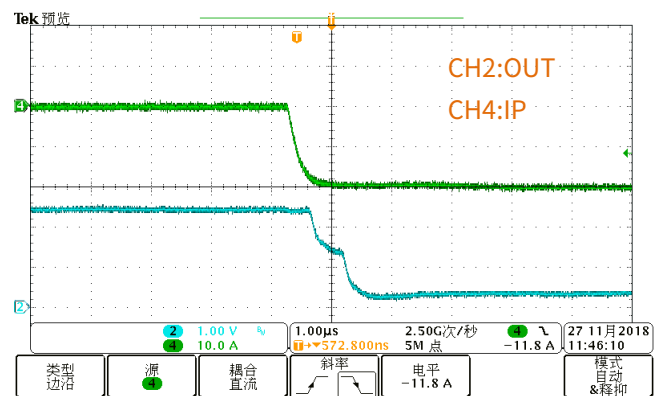
V_{out} vs. I_P (Forward current rising edge response) (20 A)



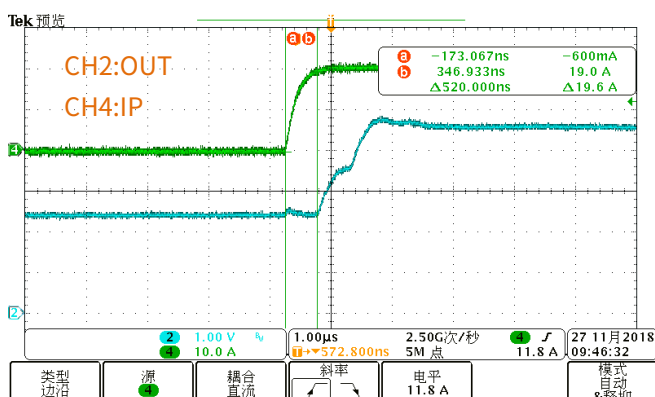
V_{out} vs. I_P (Forward current falling edge response) (20 A)



V_{out} vs. I_P (Response to rising edge of negative current) (20 A)

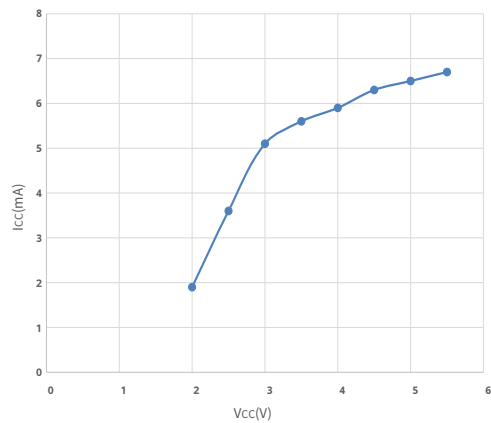


V_{out} vs. I_P (Response to the falling edge of negative current) (20 A)

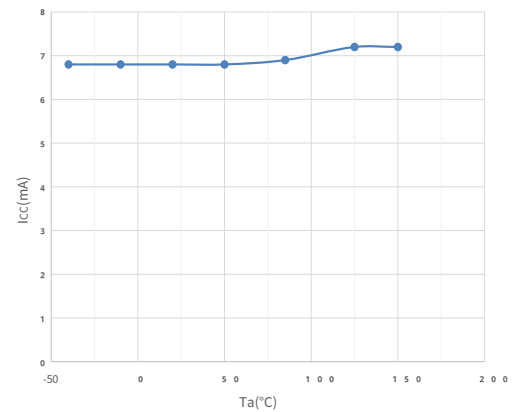


$t_{\text{Response time}}$ (20 A)

Quiescent Current

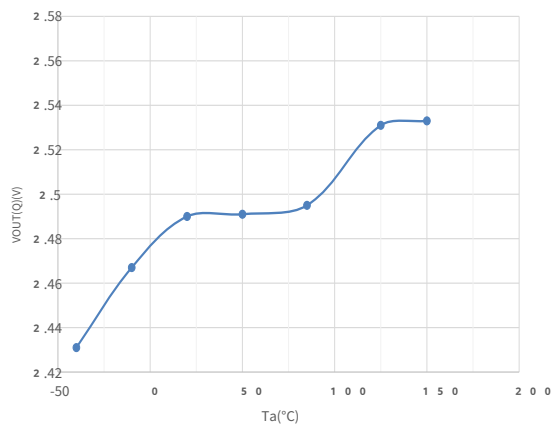


I_{cc} vs. V_{cc}

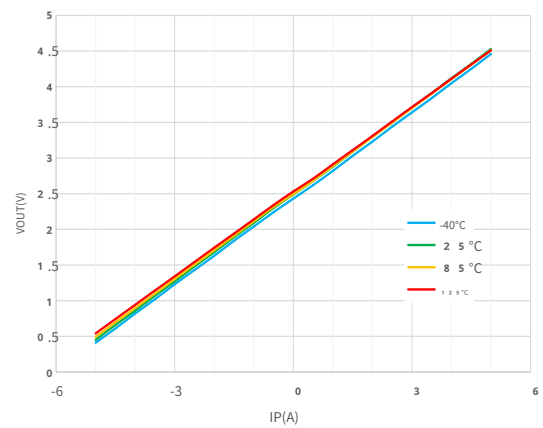


I_{cc} vs. T_a

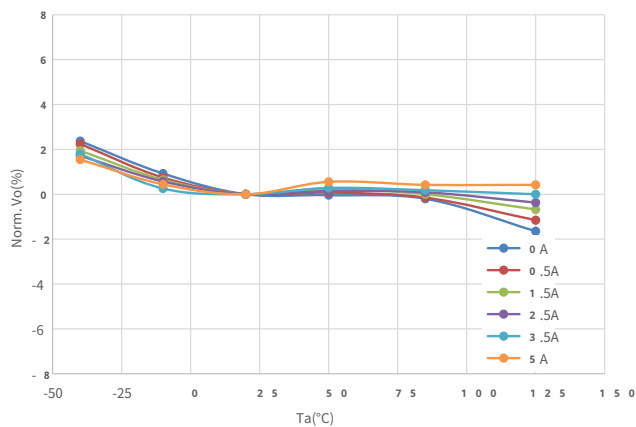
5 A series



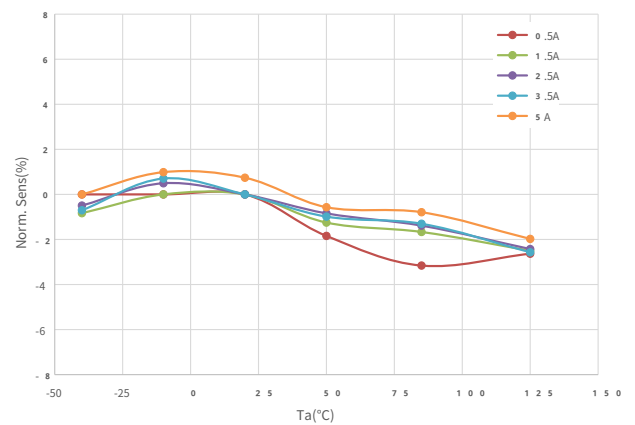
$V_{out(Q)}$ vs. T_a (5A)



V_{out} vs. I_P (5A)

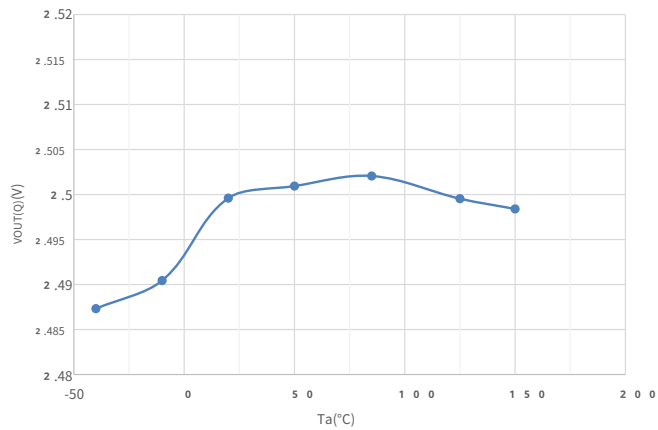


V_{out} error vs. T_a (5A)

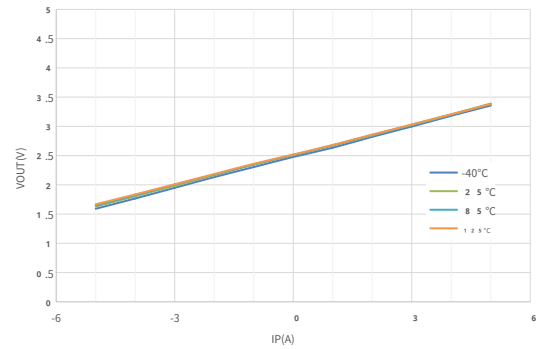


Sens error vs. T_a (5A)

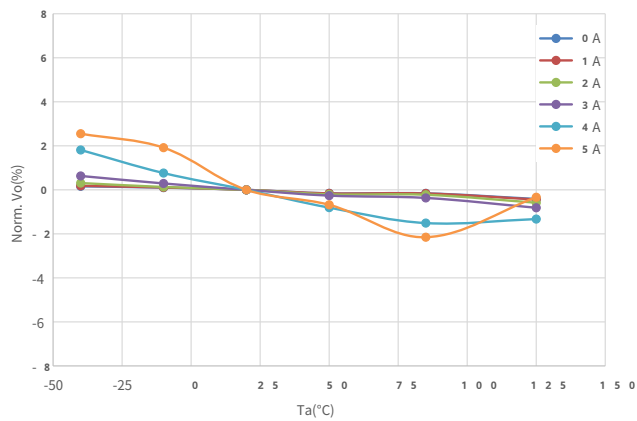
5 Bseries



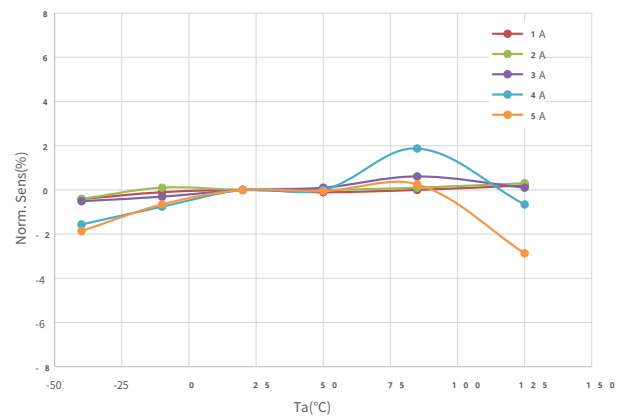
VOUT(Q) vs. Ta (5B)



VOUT vs. IP (5B)

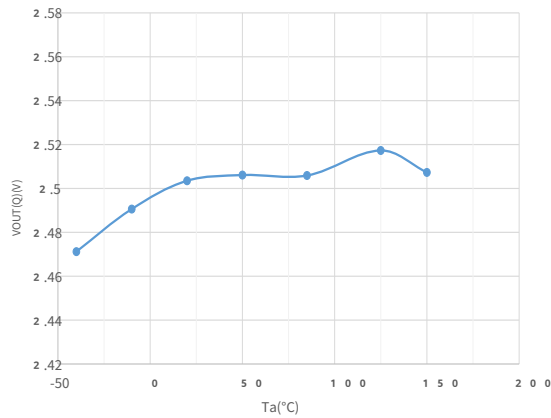


VOUTerror vs. Ta (5B)

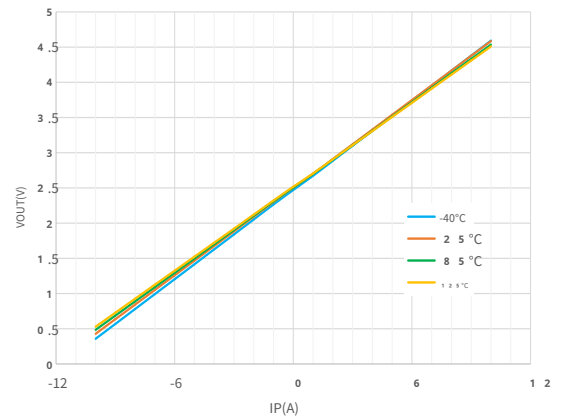


Sens error vs. Ta (5B)

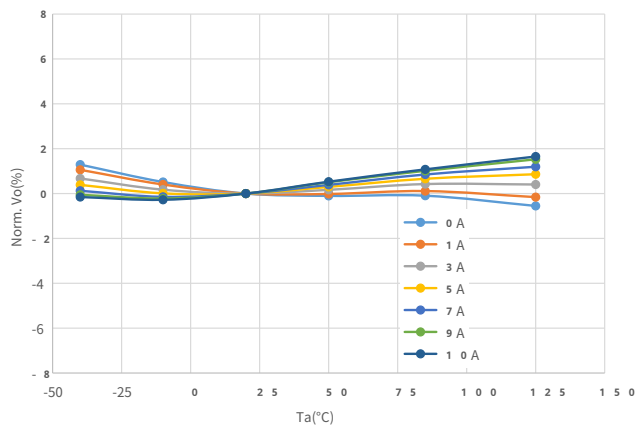
1 0 A series



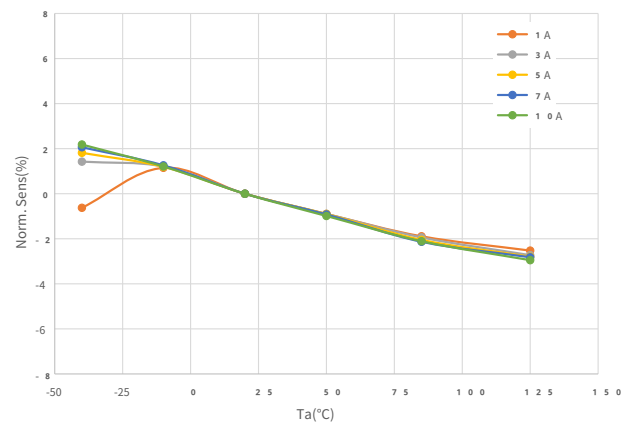
Vout(Q) vs. Ta (10A)



Vout vs. IP (10A)

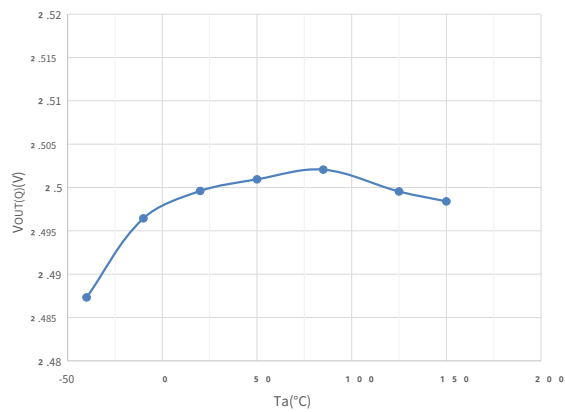


Vout error vs. Ta (10A)

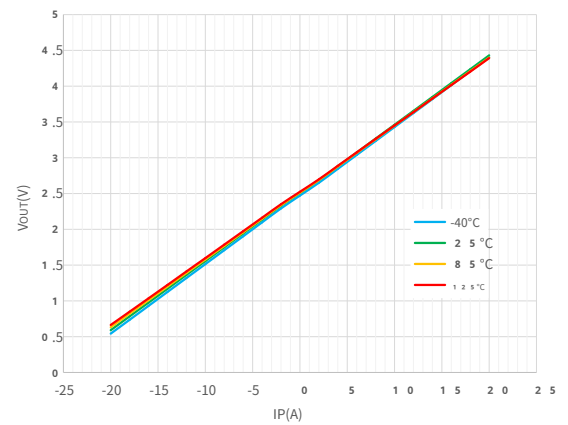


Sens error vs. Ta (10A)

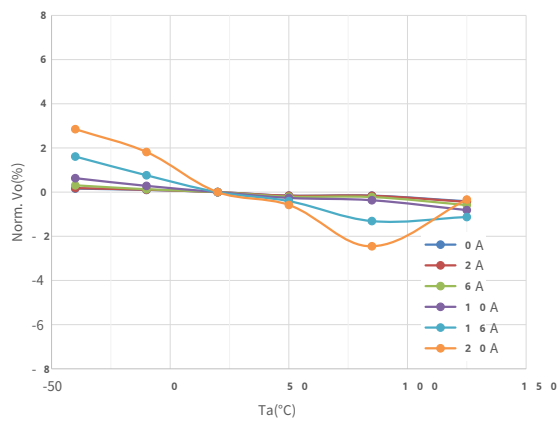
2.0 A series



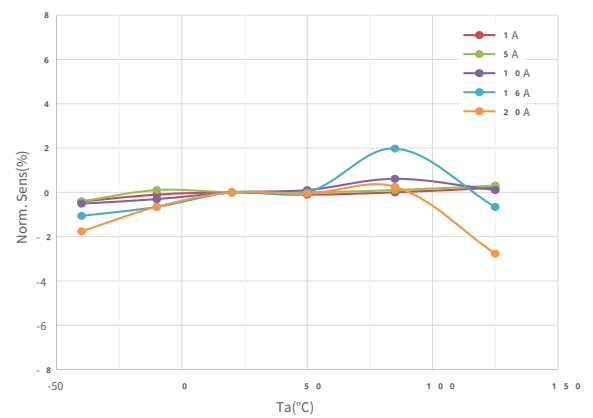
Vout(Q) vs. Ta (20A)



Vout vs. IP (20A)

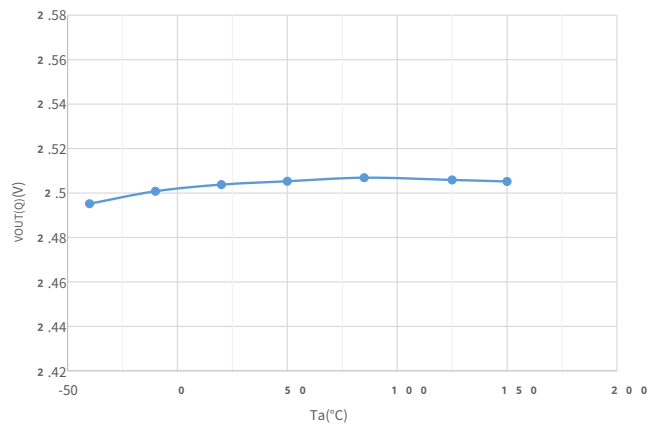


Vout error vs. Ta (20A)

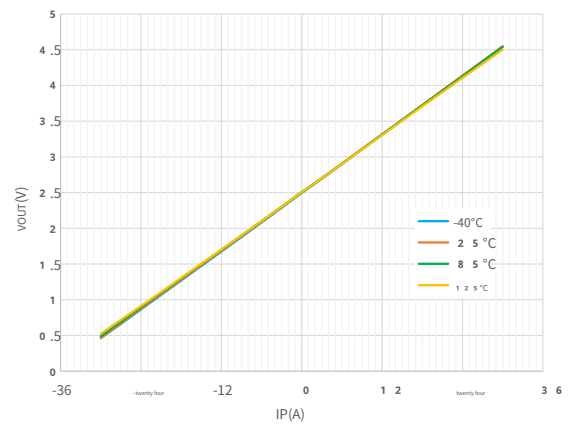


Sens error vs. Ta (20A)

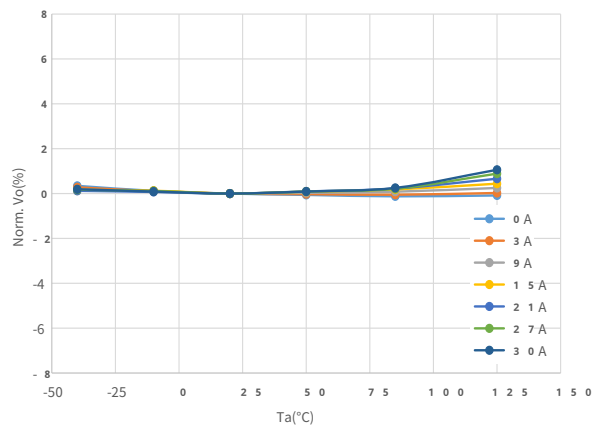
3.0 A Series



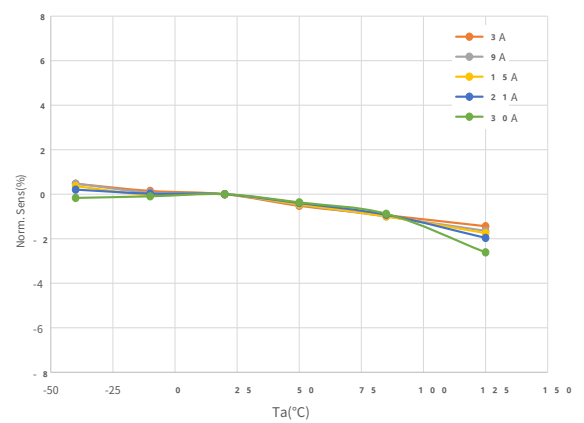
Vout(Q) vs. Ta (30A)



Vout vs. IP (30A)



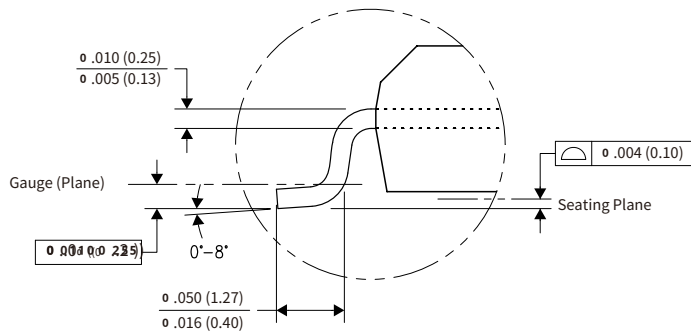
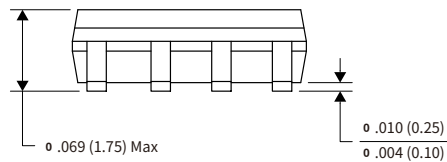
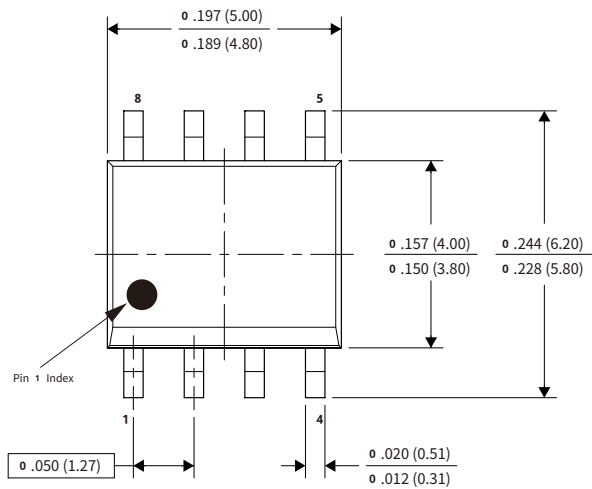
Vout error vs. Ta (30A)



Sens error vs. Ta (30A)

Dimensions

SOP8 Encapsulation



note:

1. The dimensions are in inches (millimeters).

Marking:

first row: CC6902S0product name

second line:ELC-XXA

● XX: Detection current range

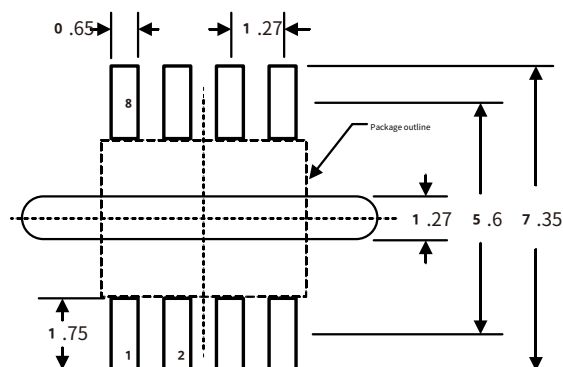
The third row: XYYWWW

● XX – Code

● YY – Last two digits of the year

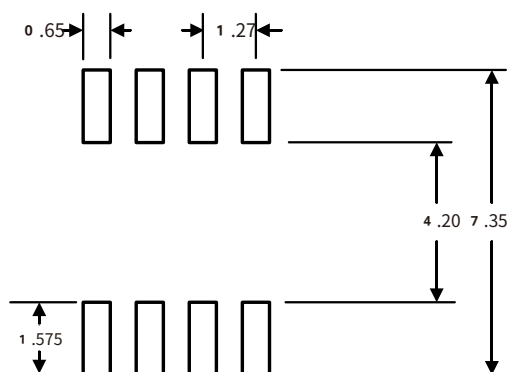
● WW – Number of weeks

Package reference



Reference one:PCB Grooving to increase creepage distance

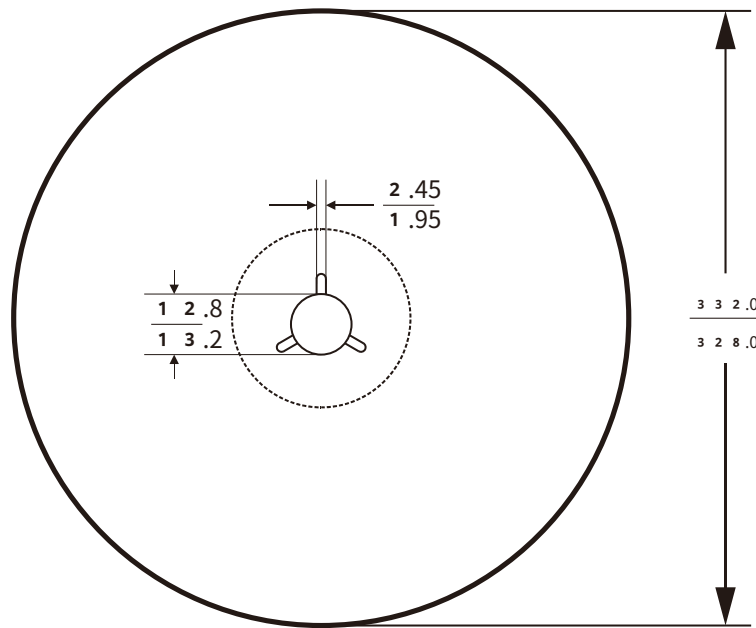
note:layout Layout requirements: under the chip, it is not recommended to wire, **Prohibit** Take the high current line



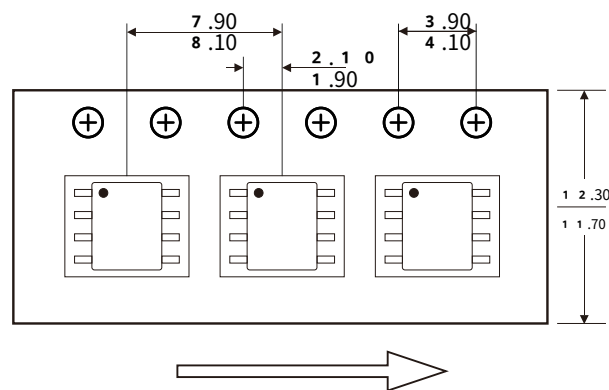
Reference 2 : Shorten the pad length and increase the creepage distance

note:layout Layout requirements: under the chip, it is not recommended to wire, **Prohibit** Take the high current line

Packaging & Taping



Reel size information



User Direction of Feed

Note: the front and back of each tape is empty ± 2 grid

About Xinjin

Chengdu Xinjin Electronics Co., Ltd. (CrossChip Microsystems Inc.) was founded in 2013. In 1995, it was a national high-tech enterprise engaged in the design and sales of integrated circuits. The company has strong technical strength and has more than 40 patents of various types, which are mainly used in Hall sensor signal processing. It has the following product lines:

- ✓ High-precision linear Hall sensor
- ✓ Various Hall switches
- ✓ Single-phase motor driver
- ✓ Single chip current sensor
- ✓ AMR Magneto-resistive sensor

contact us

Chengdu

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URL:http://www.crosschipmicro.com

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Shanghai

Address: Huji Highway, Jiading District, Shanghai 4 4 7 6 Rubik's Cube Community 3 Lou Chuang Guest Workshop