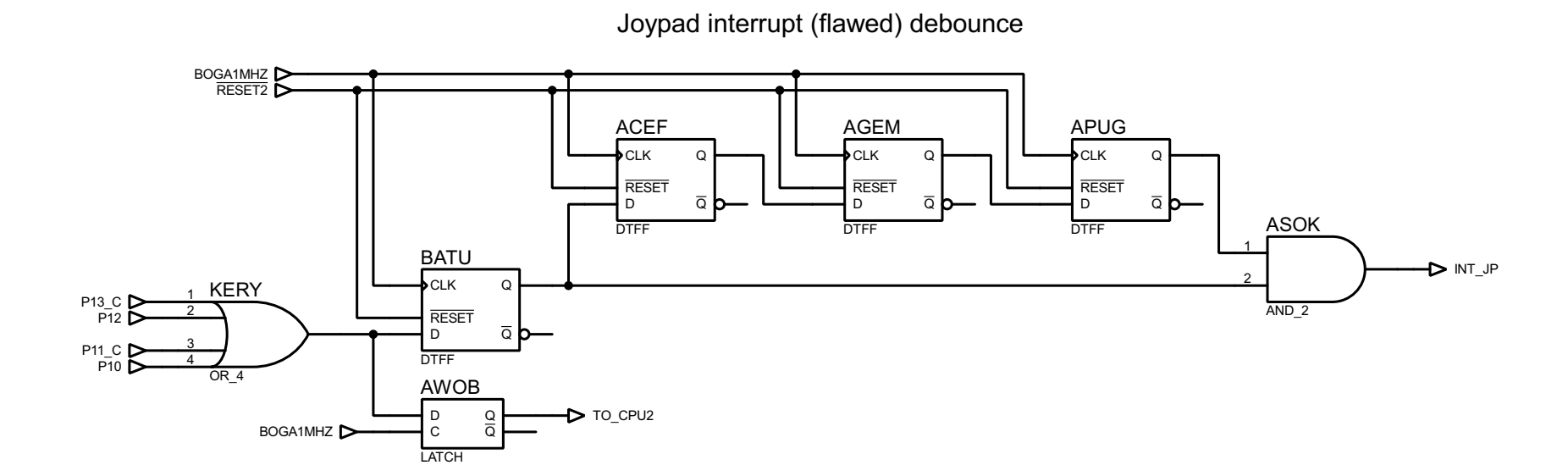
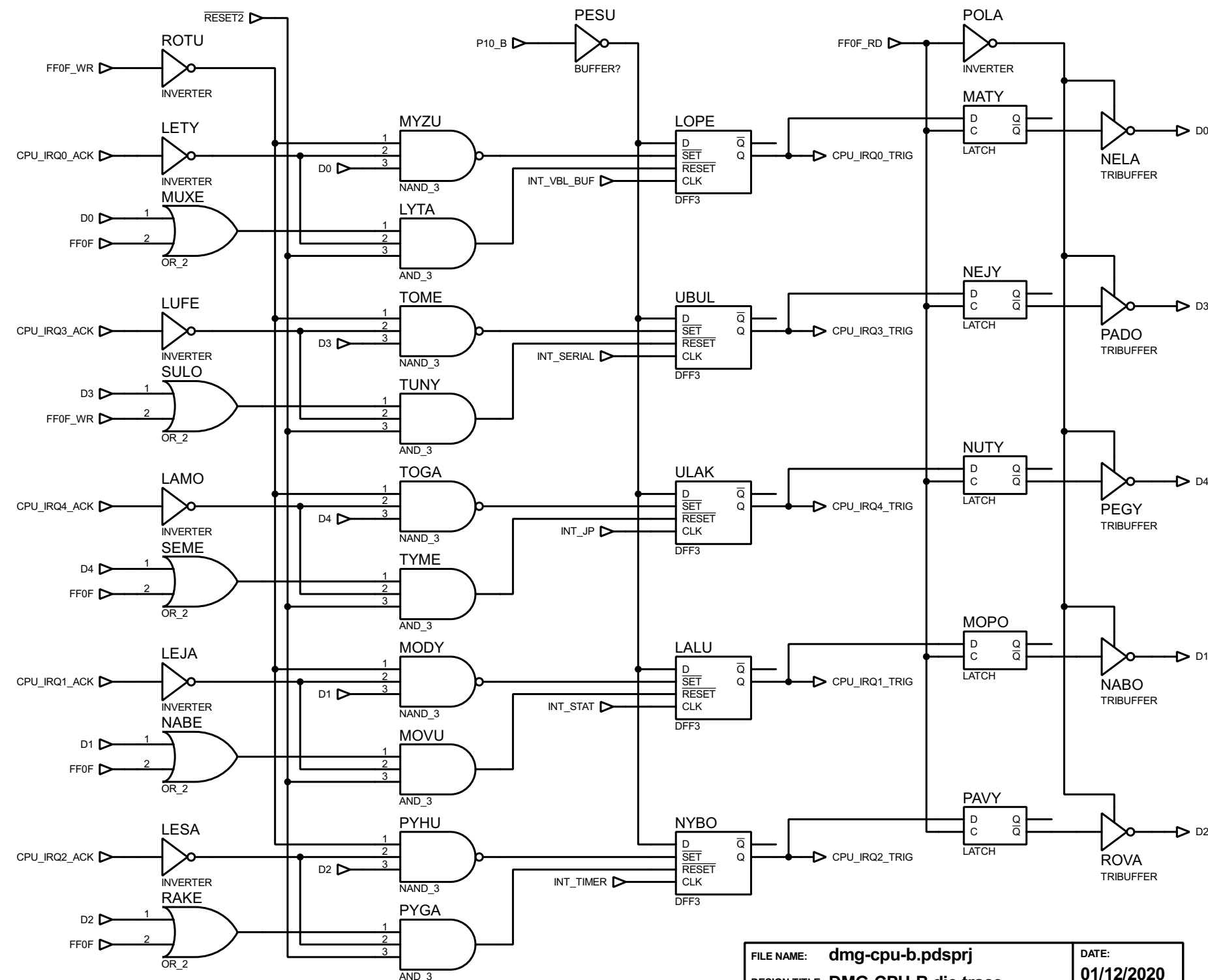


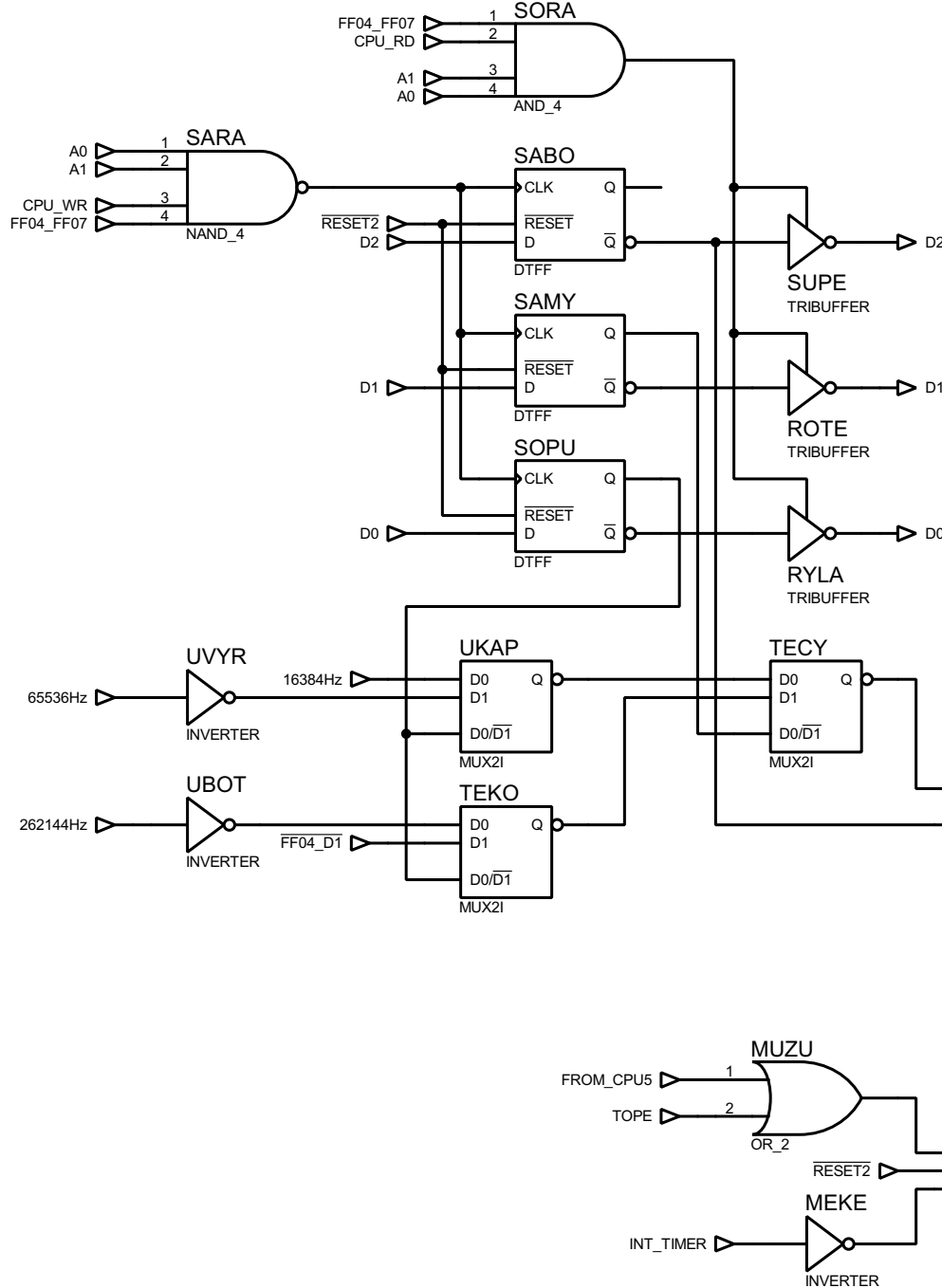
FF04 DIV



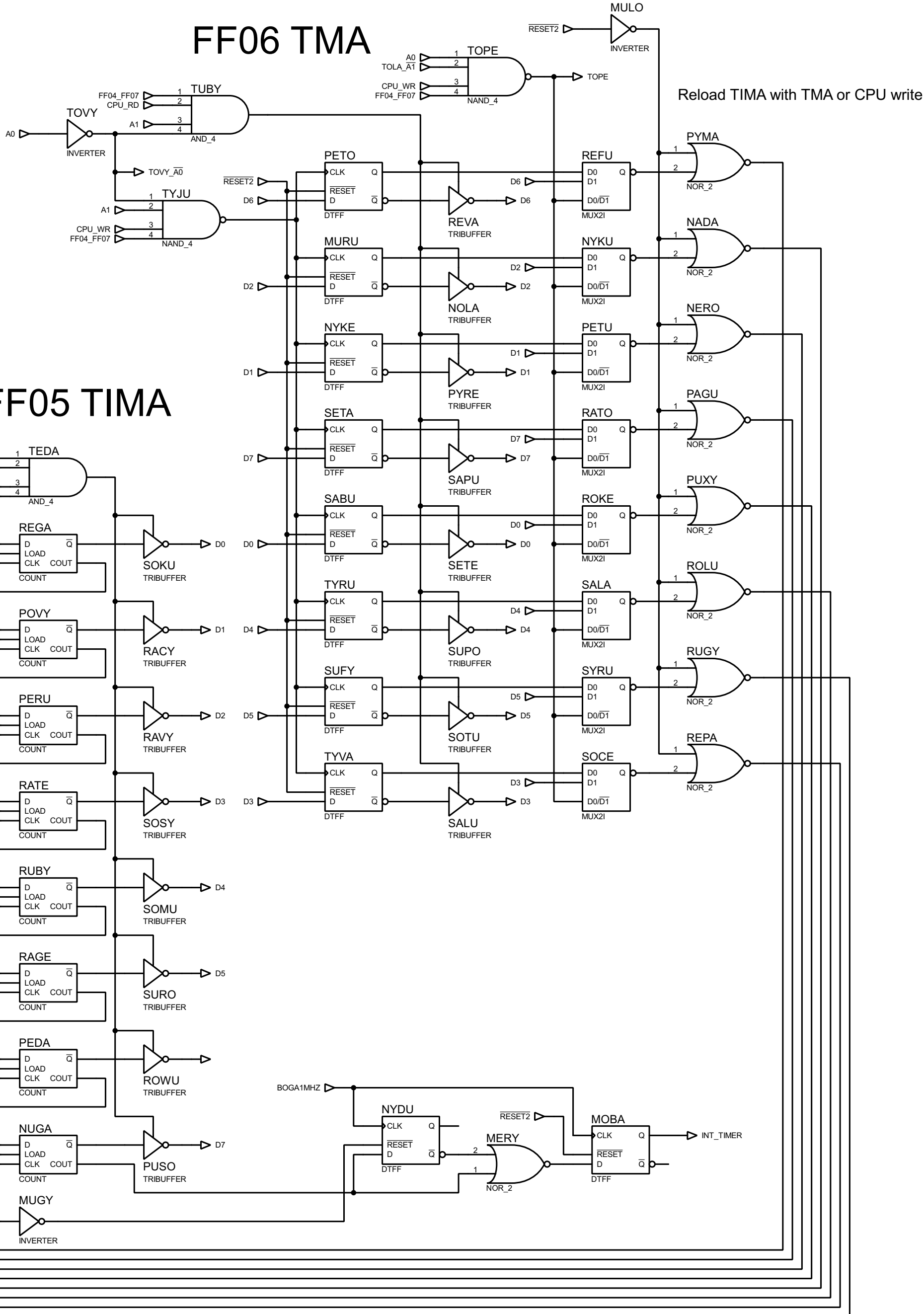
FF0F IF



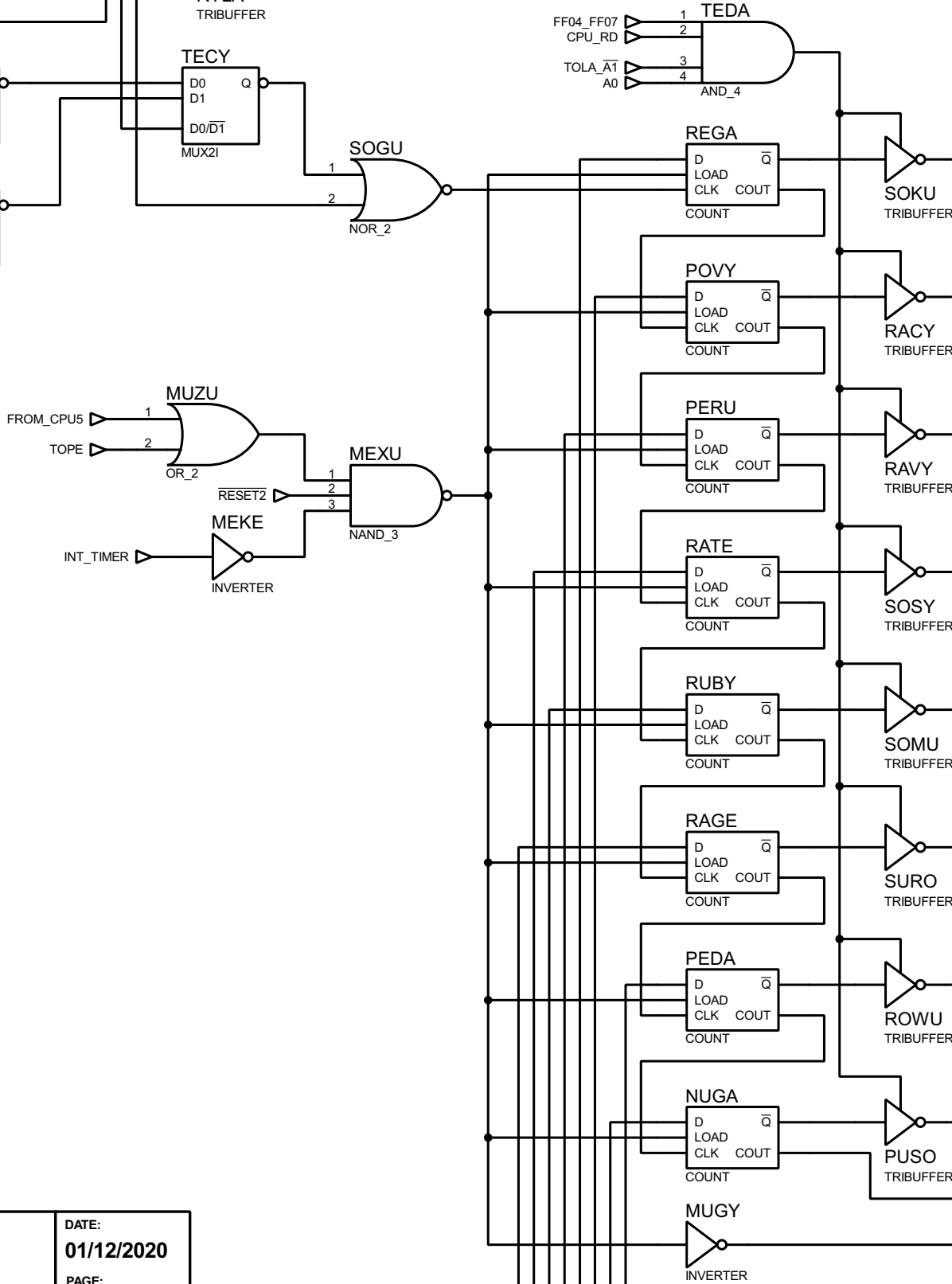
FF07 TAC



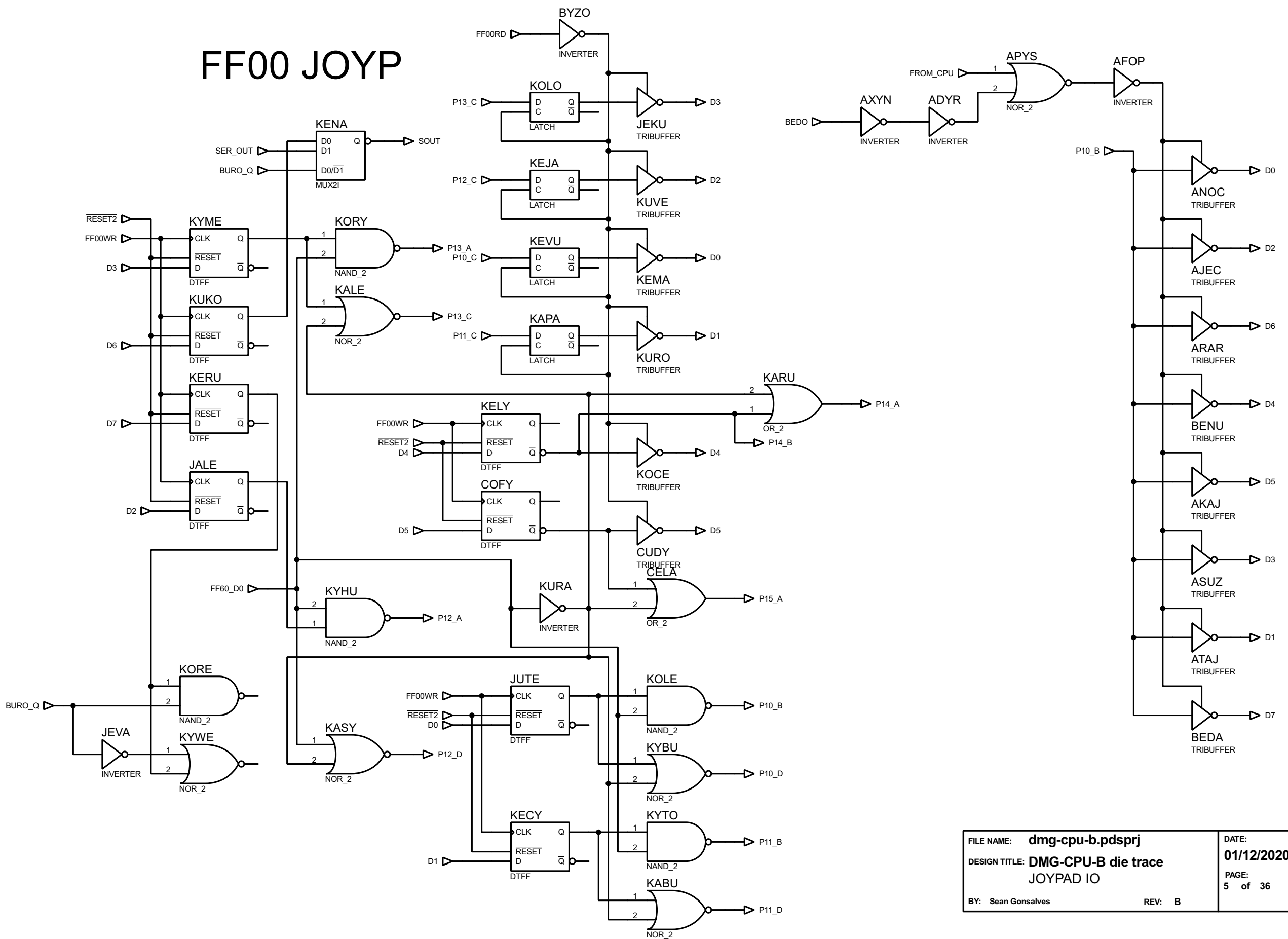
FF06 TMA



FF05 TIMA

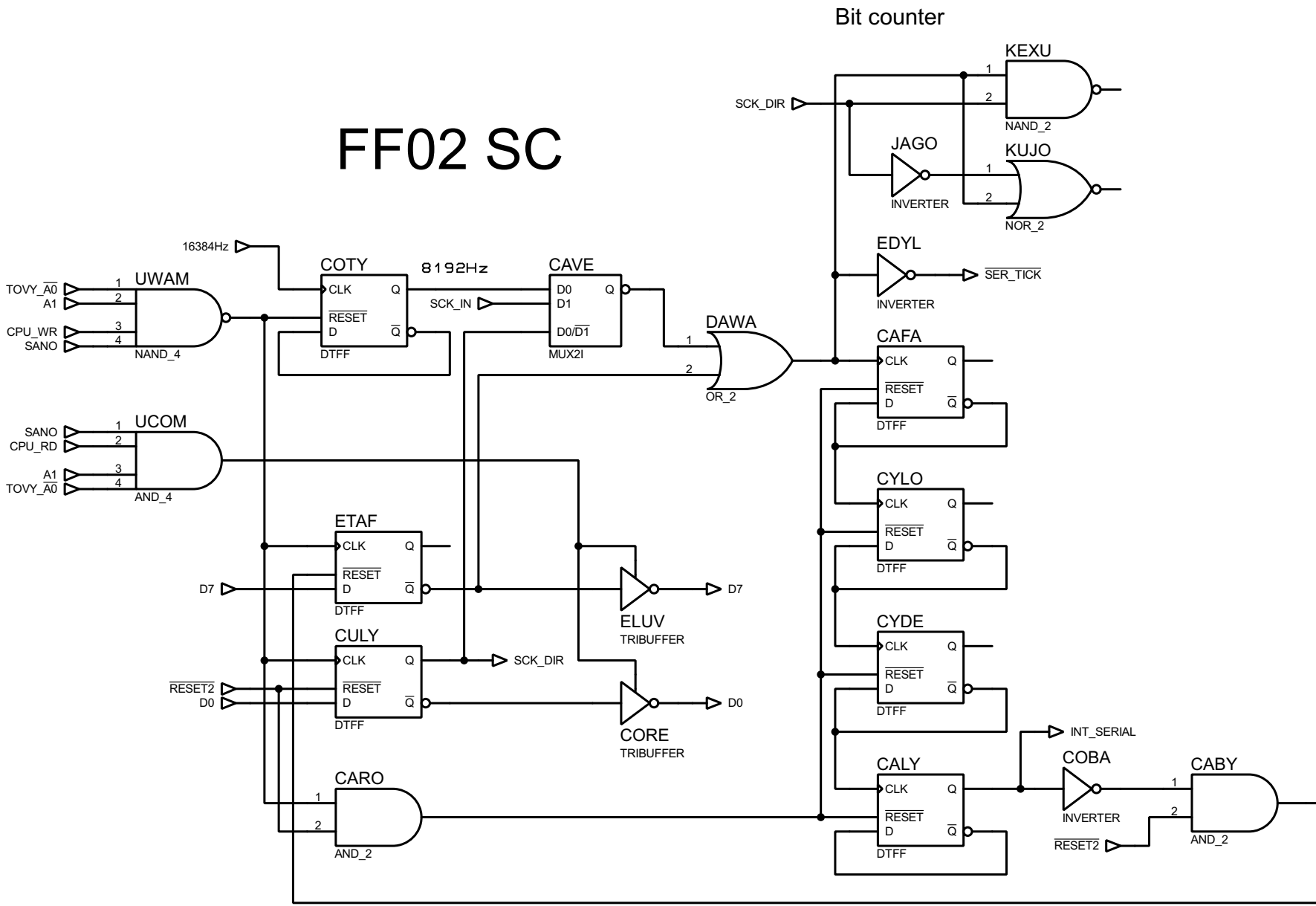


FF00 JOYP

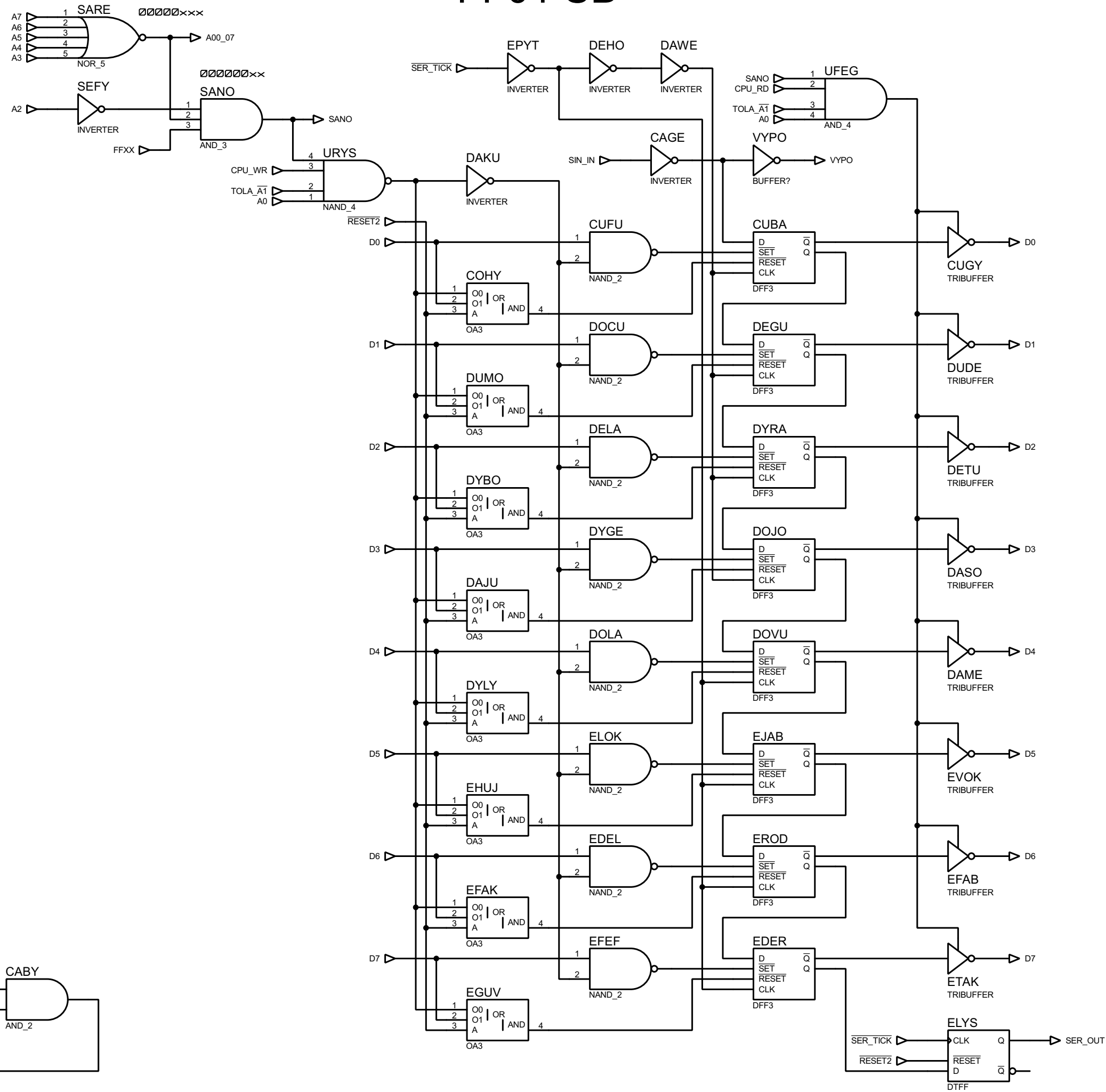


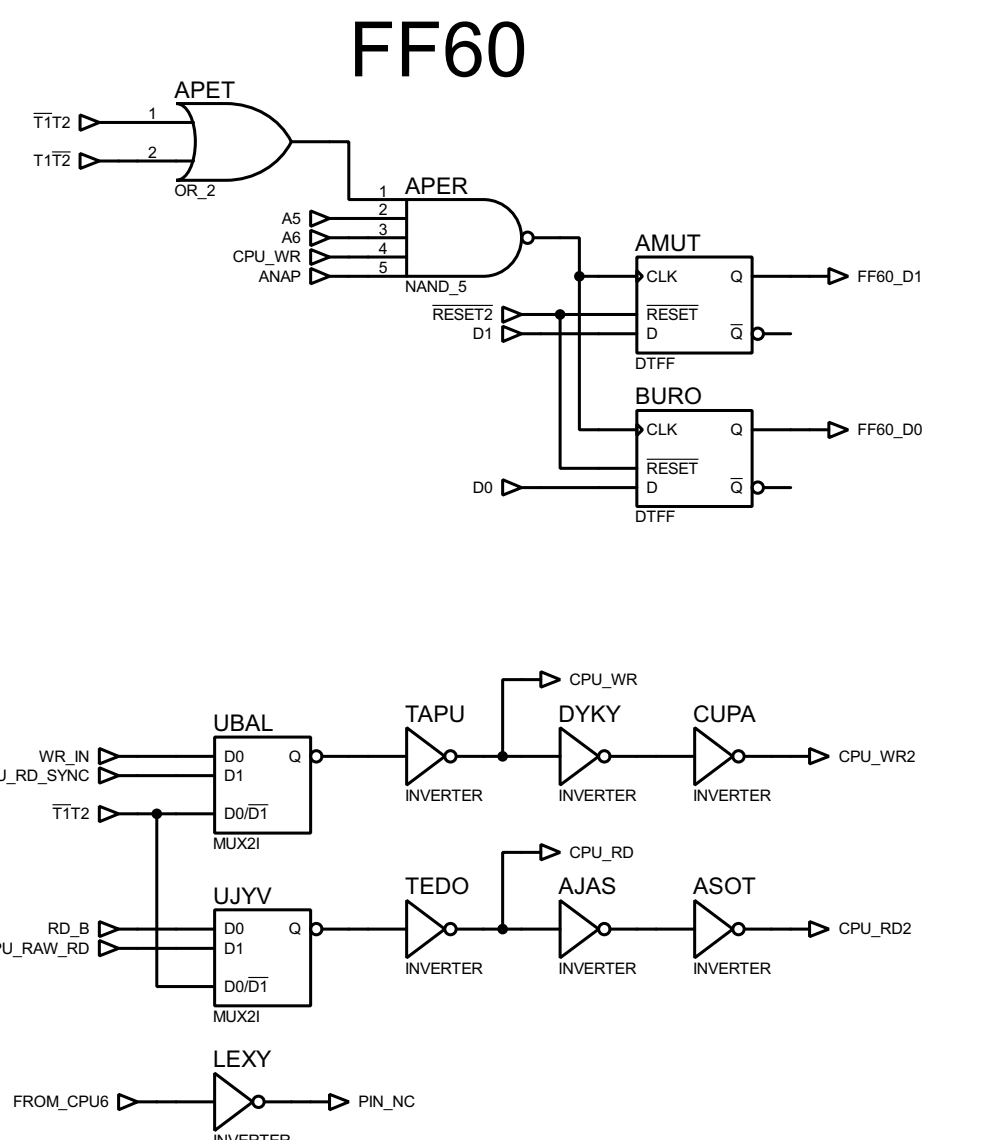
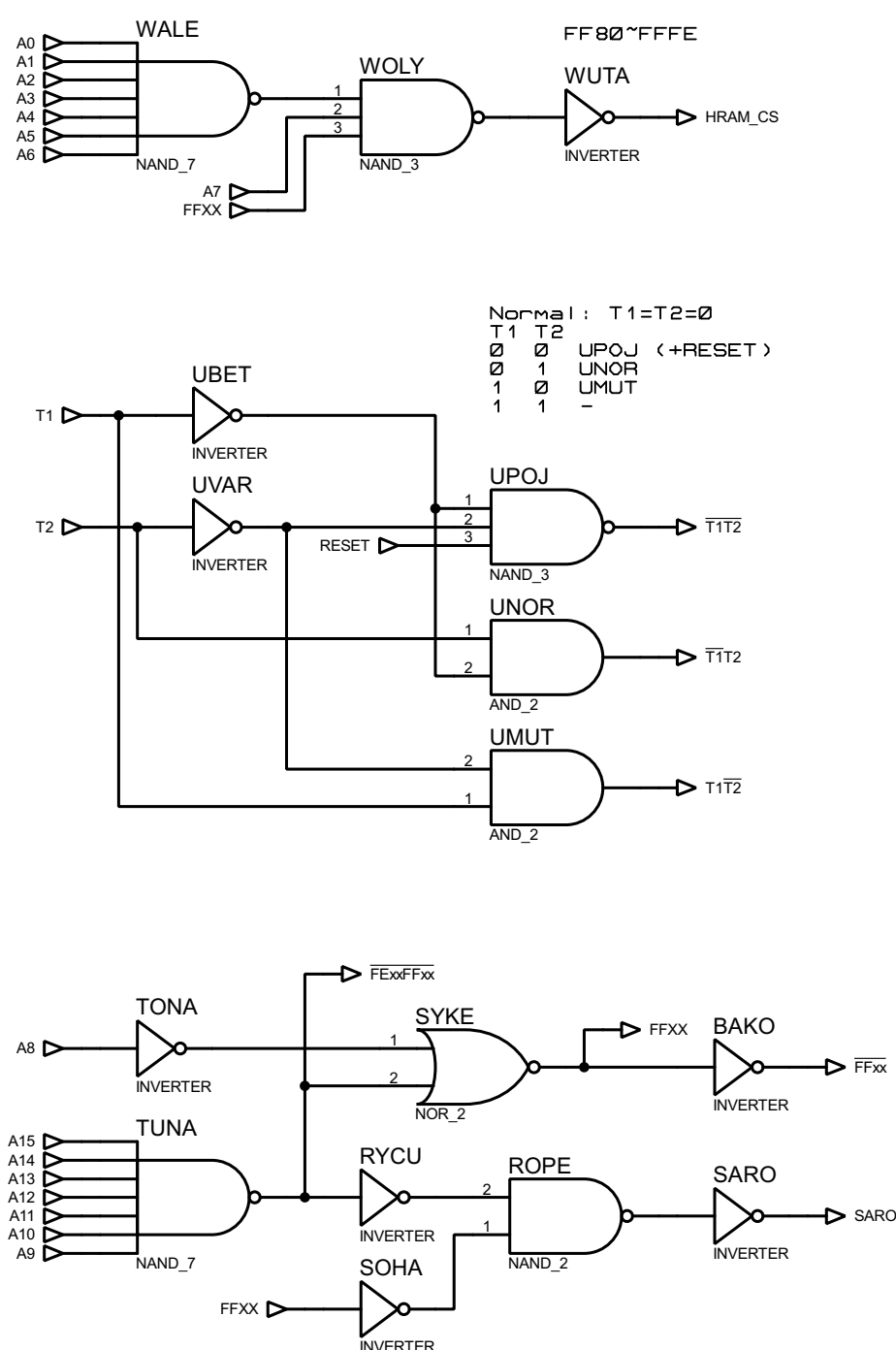
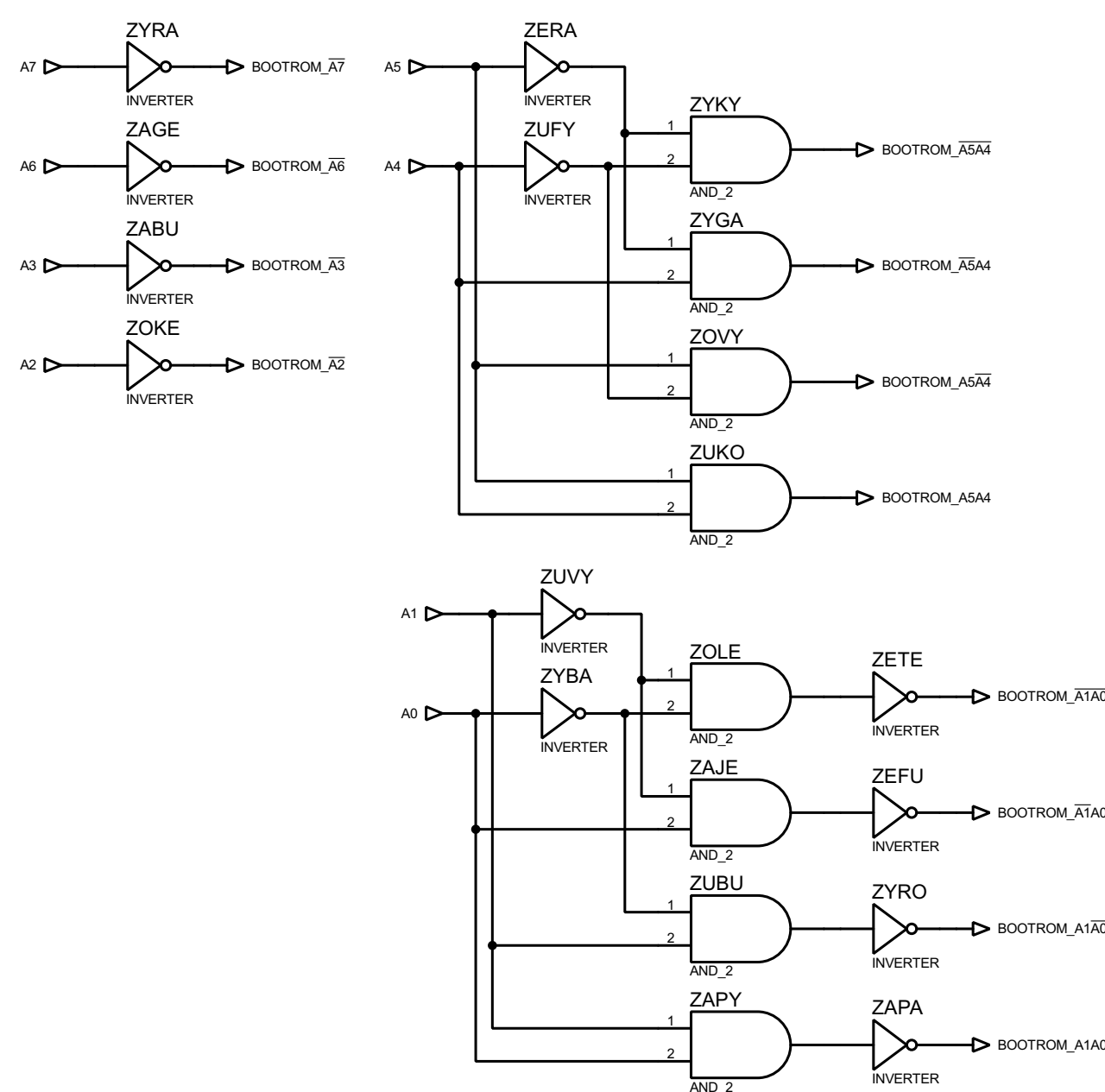
FILE NAME: dmg-cpu-b.pdsprj	DATE: 01/12/2020
DESIGN TITLE: DMG-CPU-B die trace JOYPAD IO	PAGE: 5 of 36
BY: Sean Gonsalves	REV: B

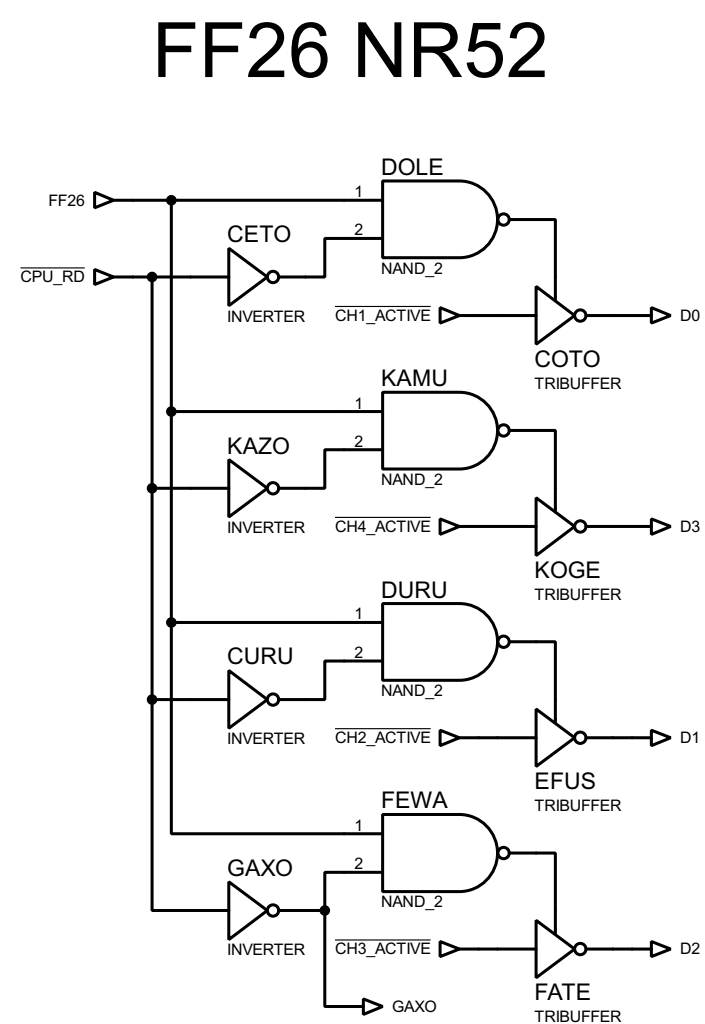
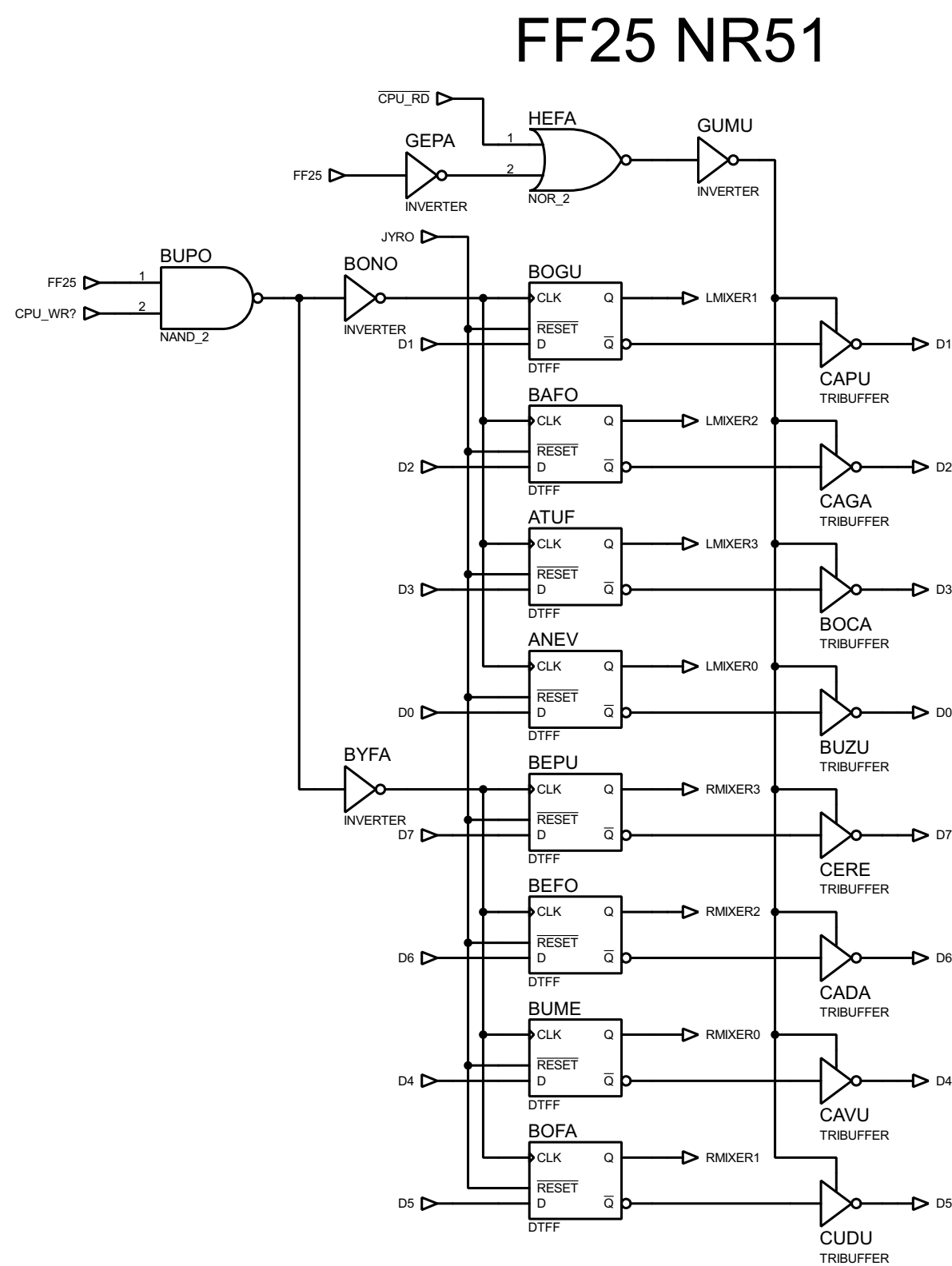
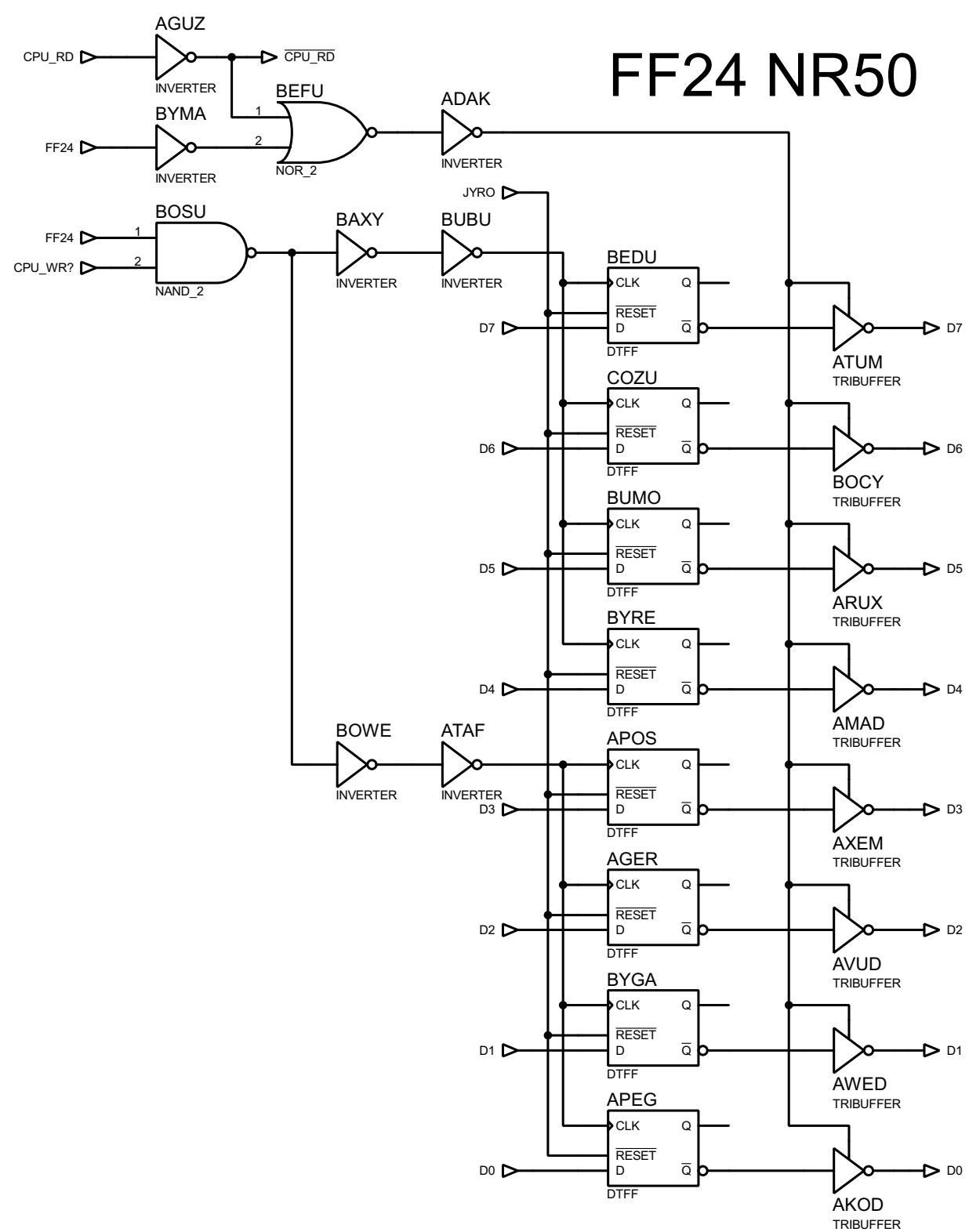
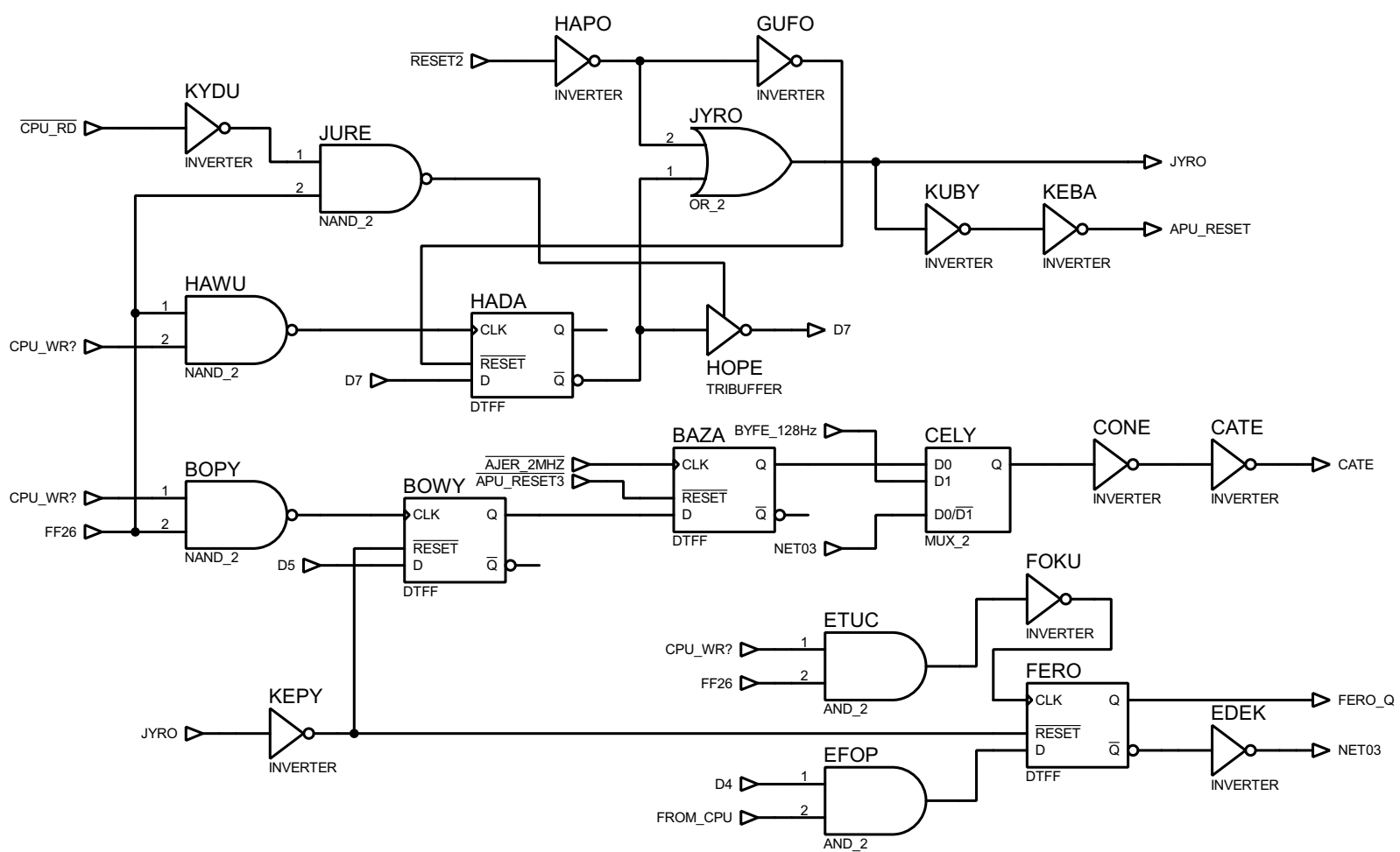
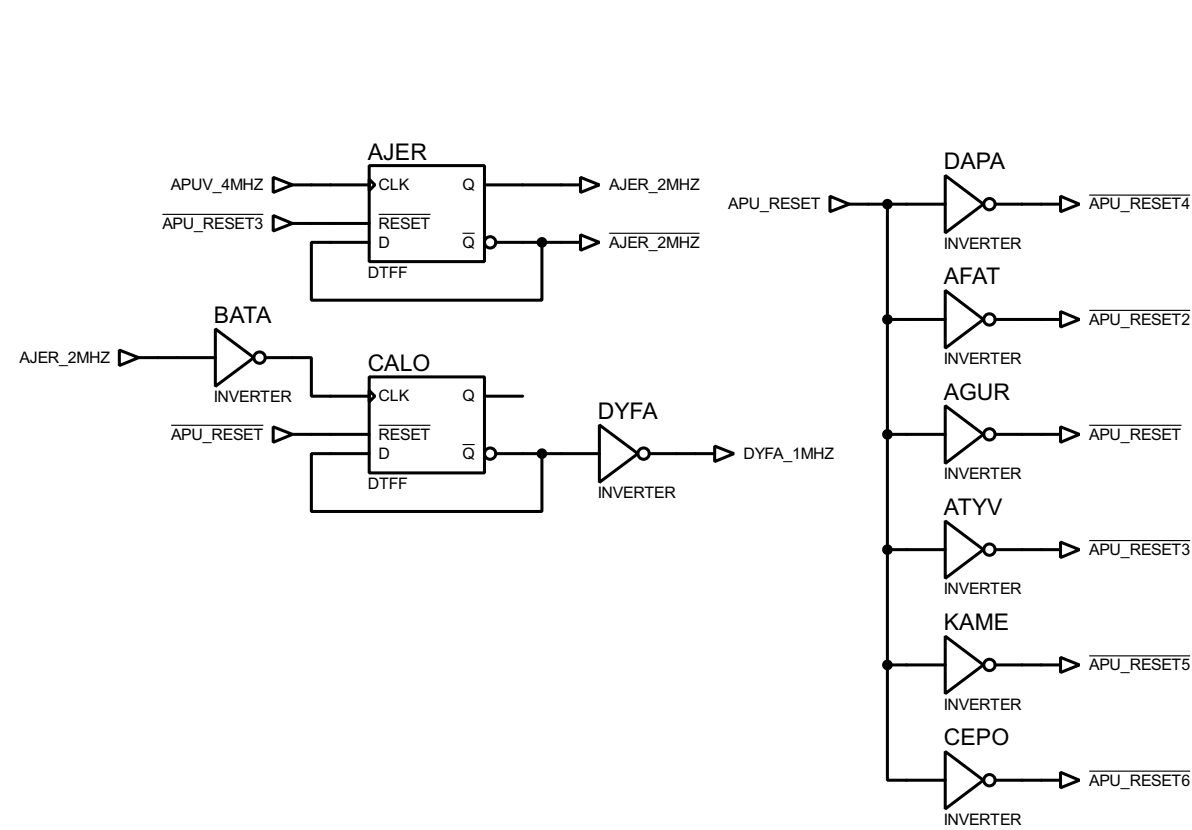
FF02 SC

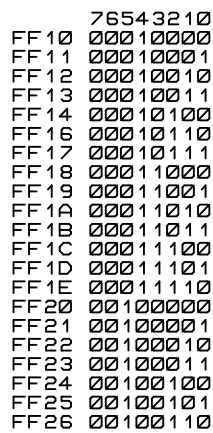
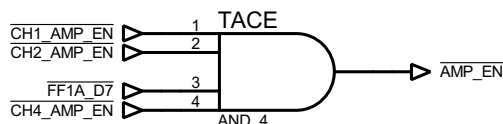
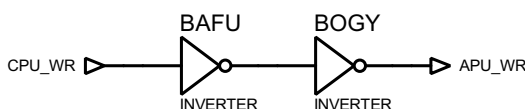
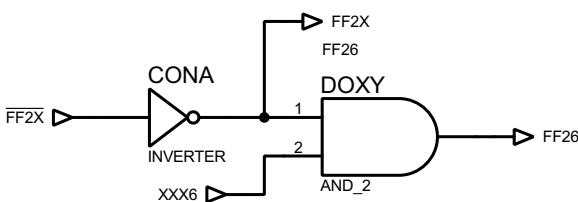
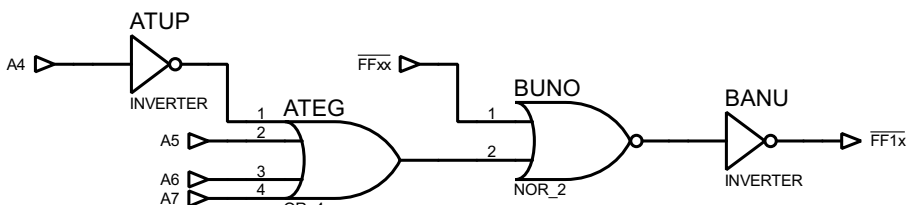
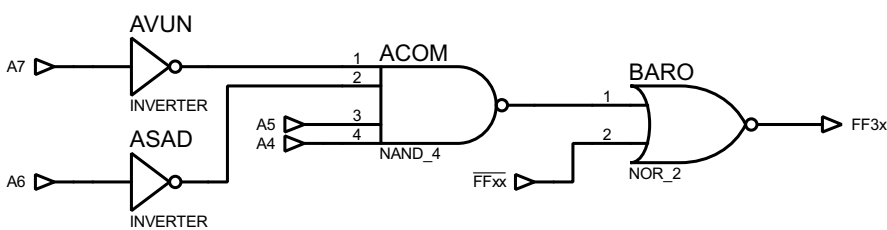
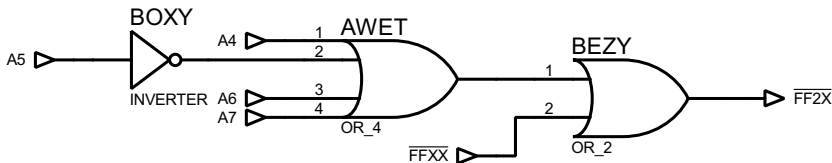


FF01 SB

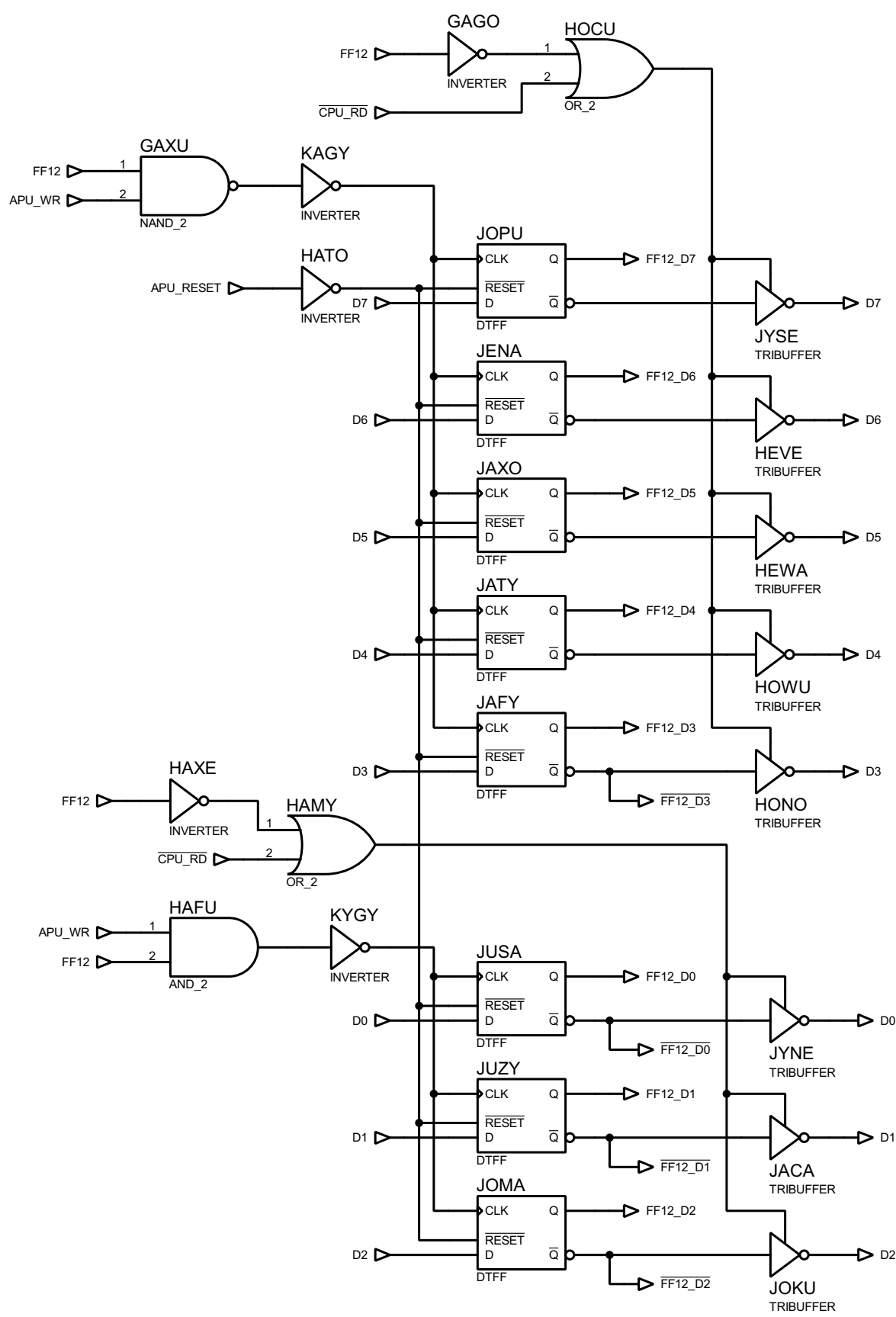




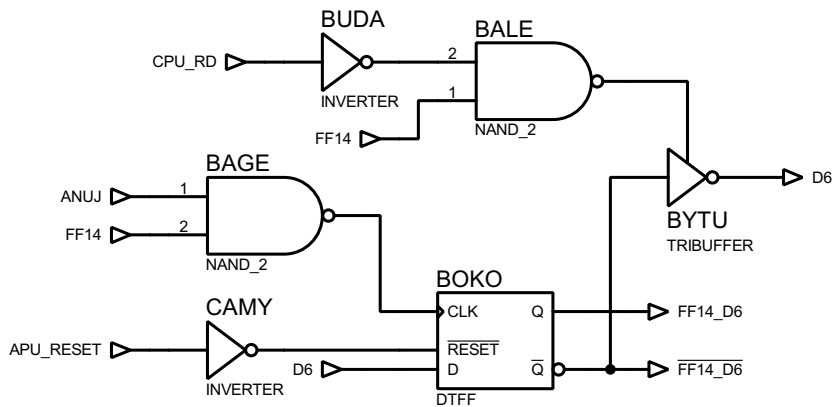




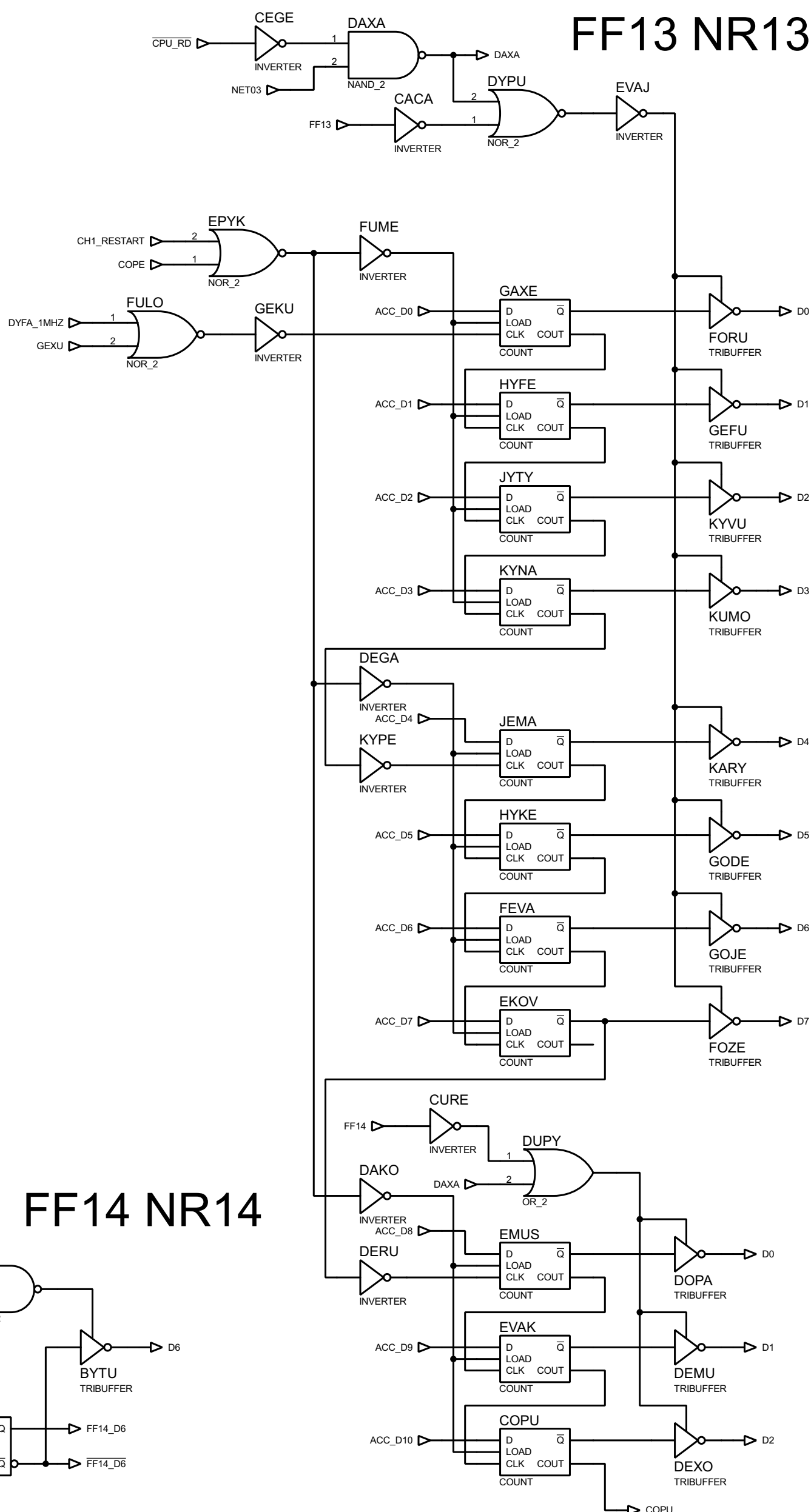
FF12 NR12



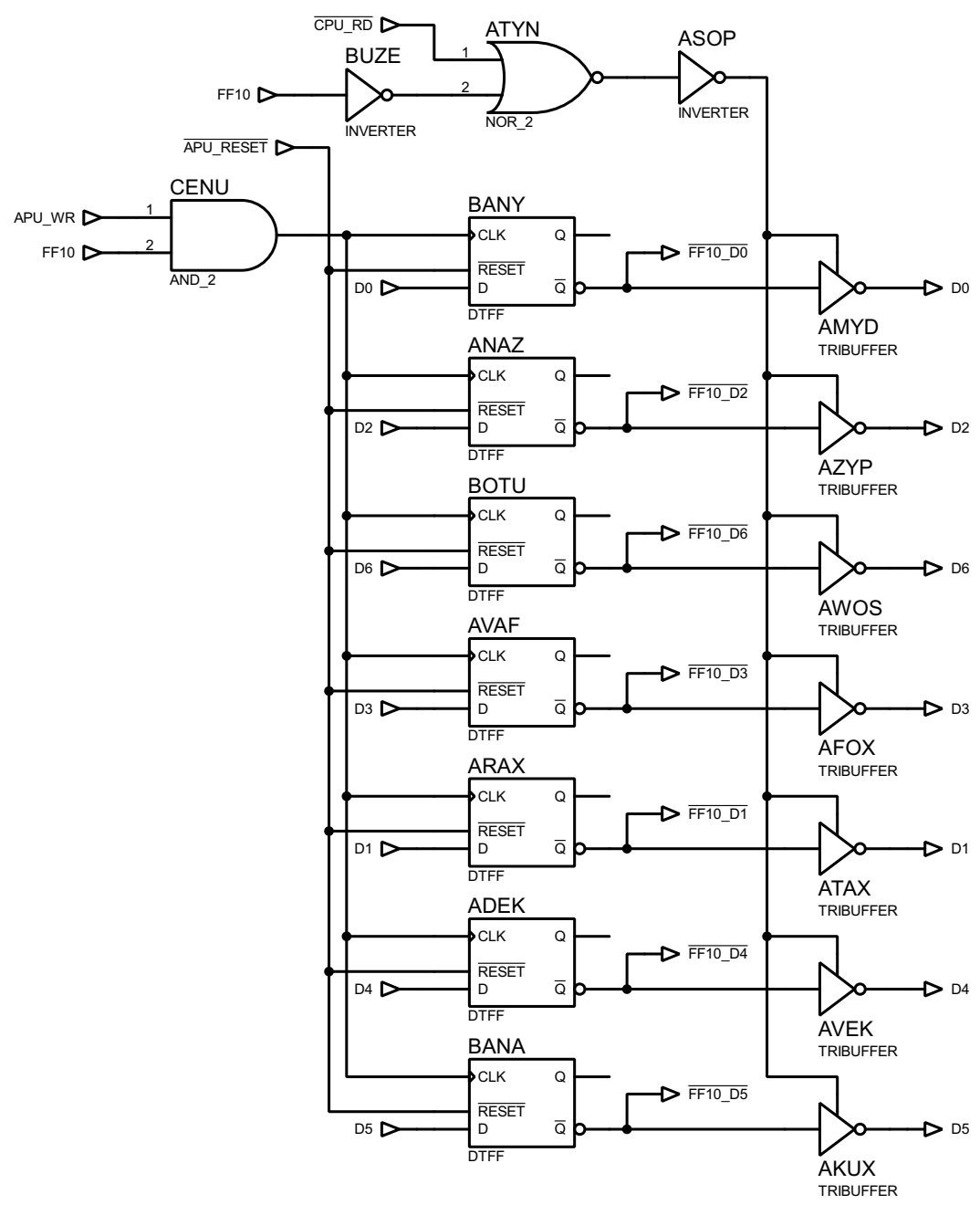
FF14 NR14



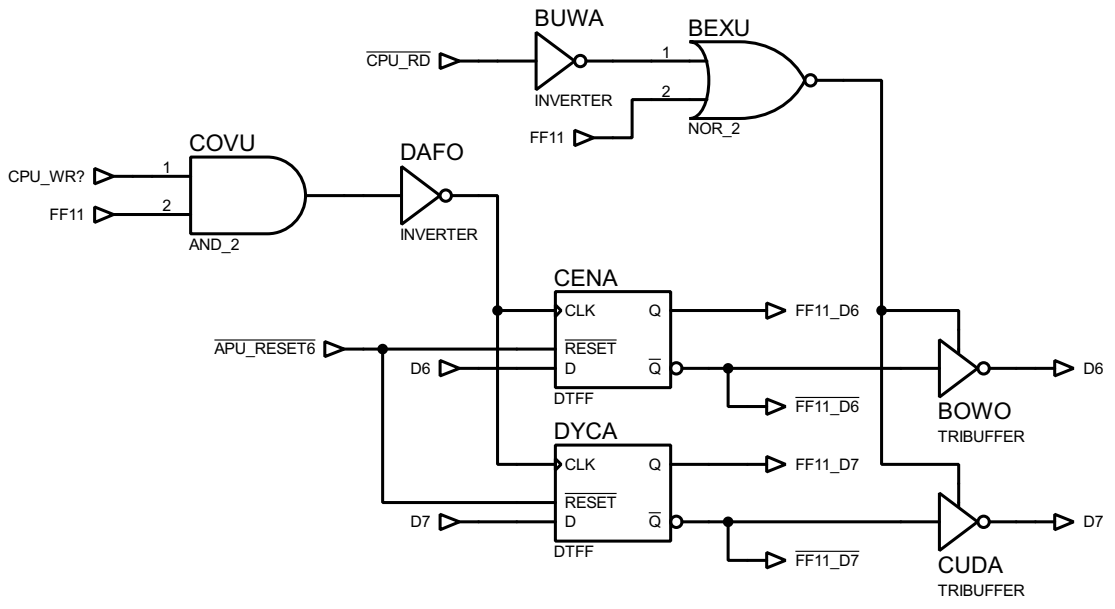
FF13 NR13

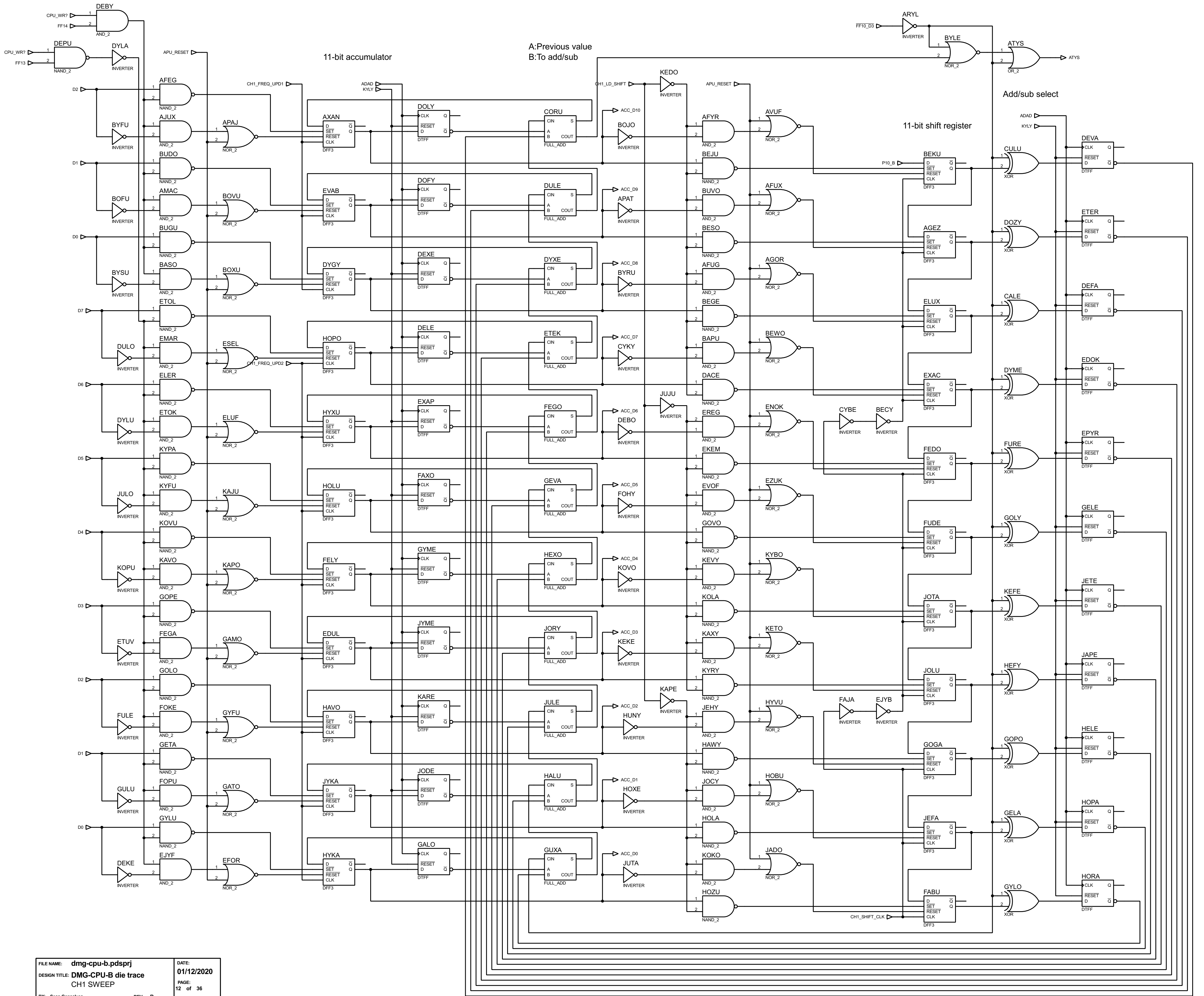


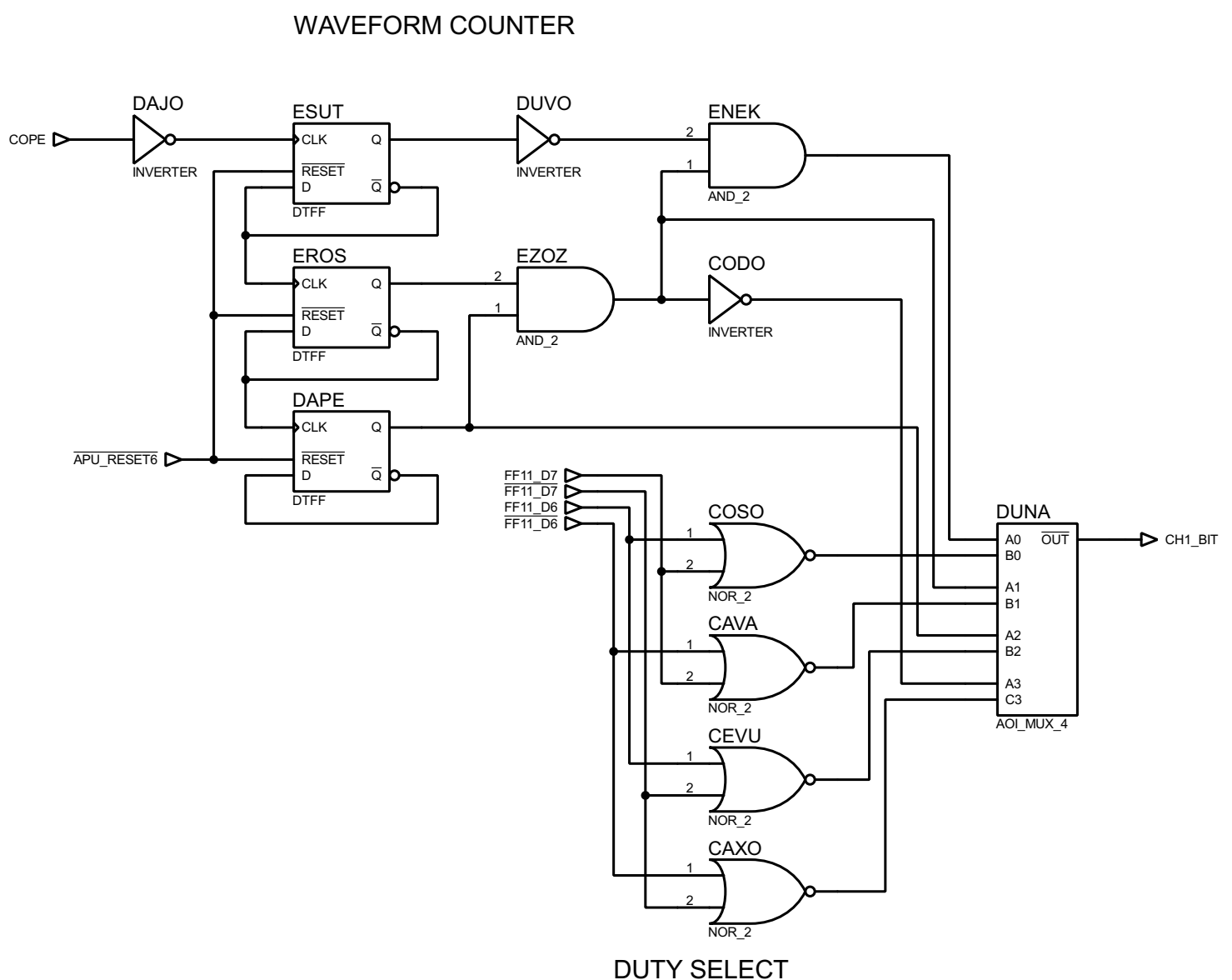
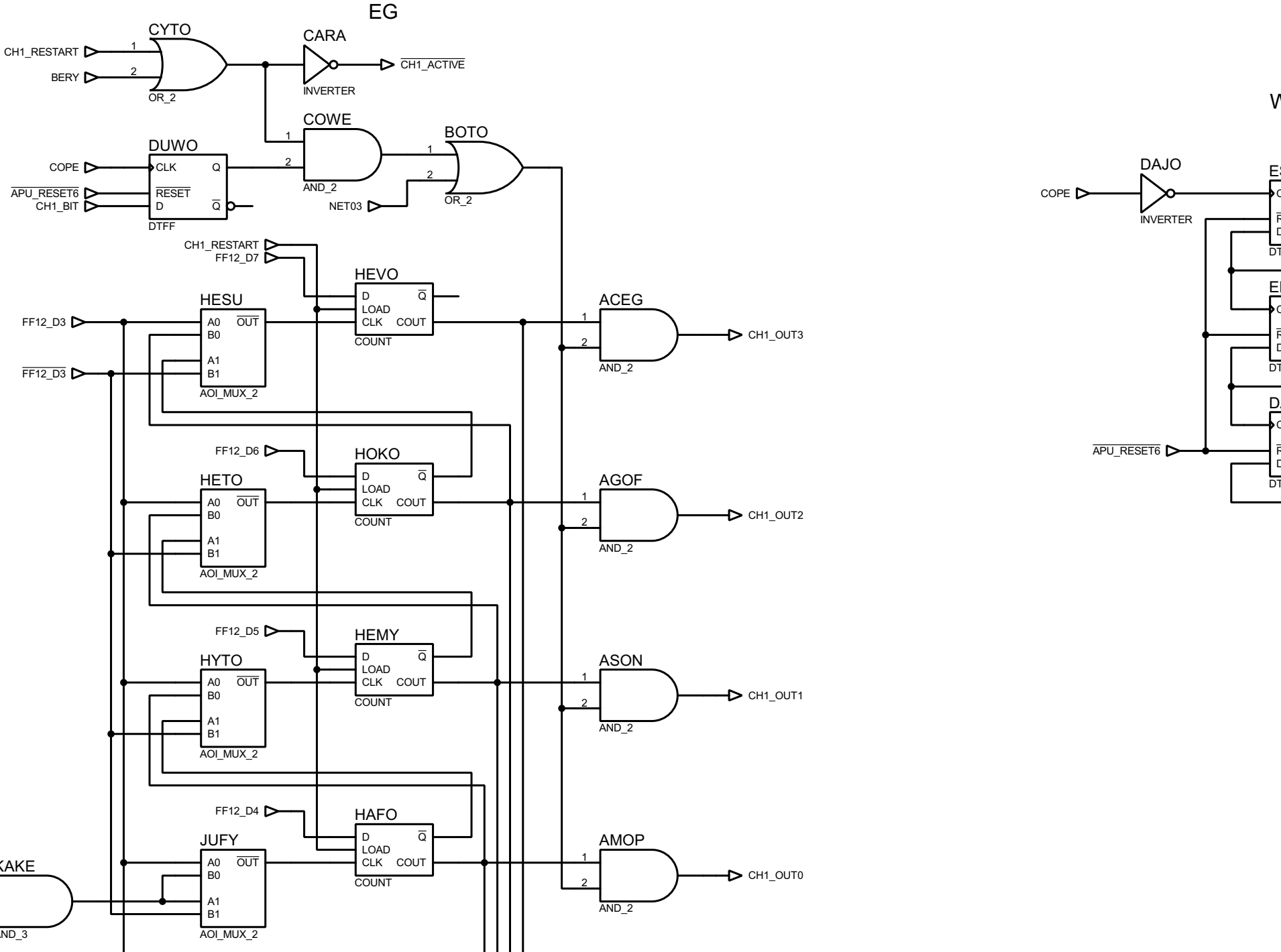
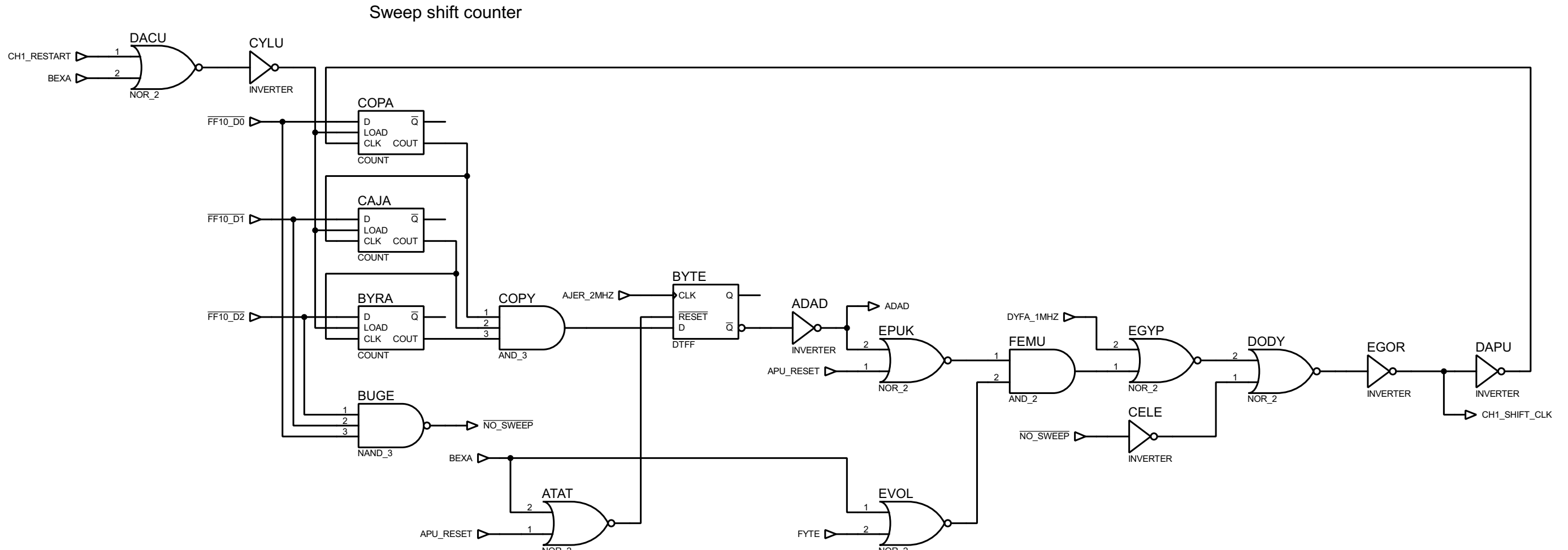
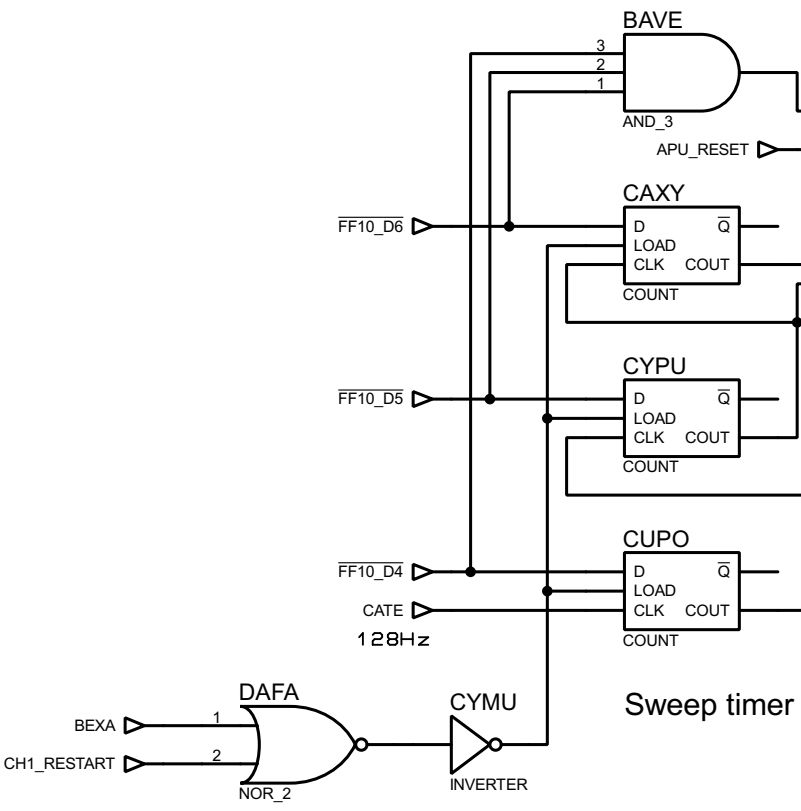
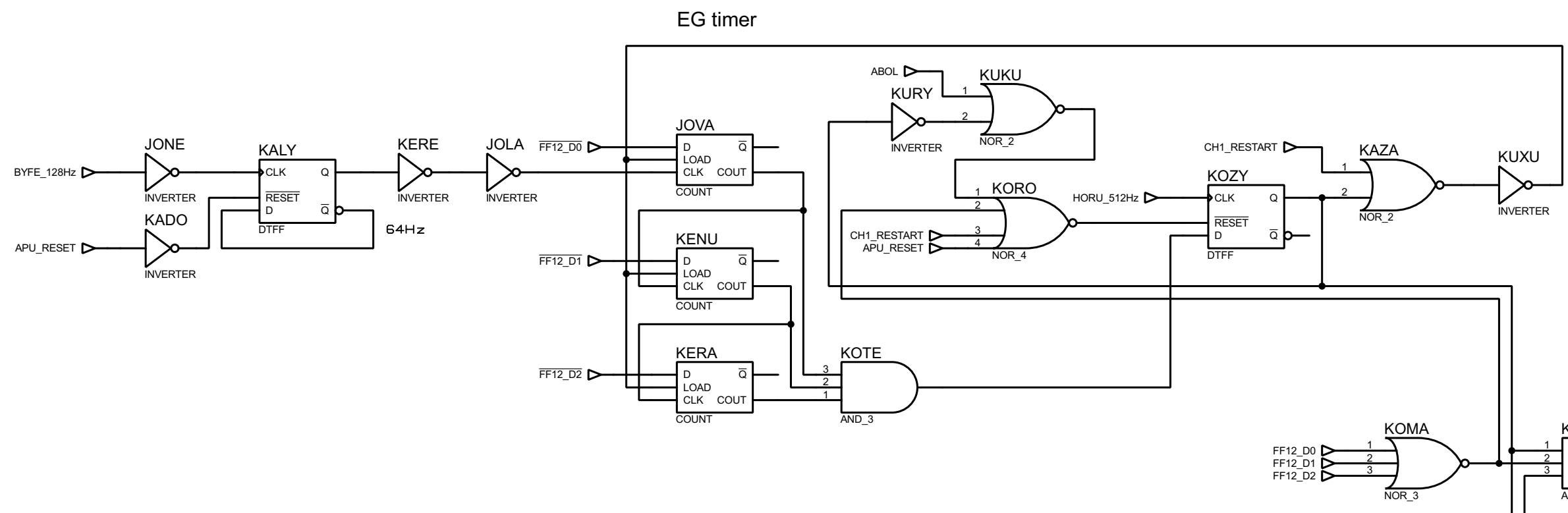
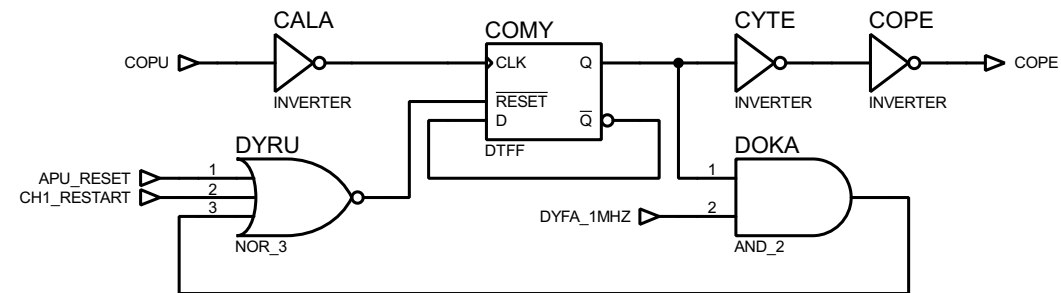
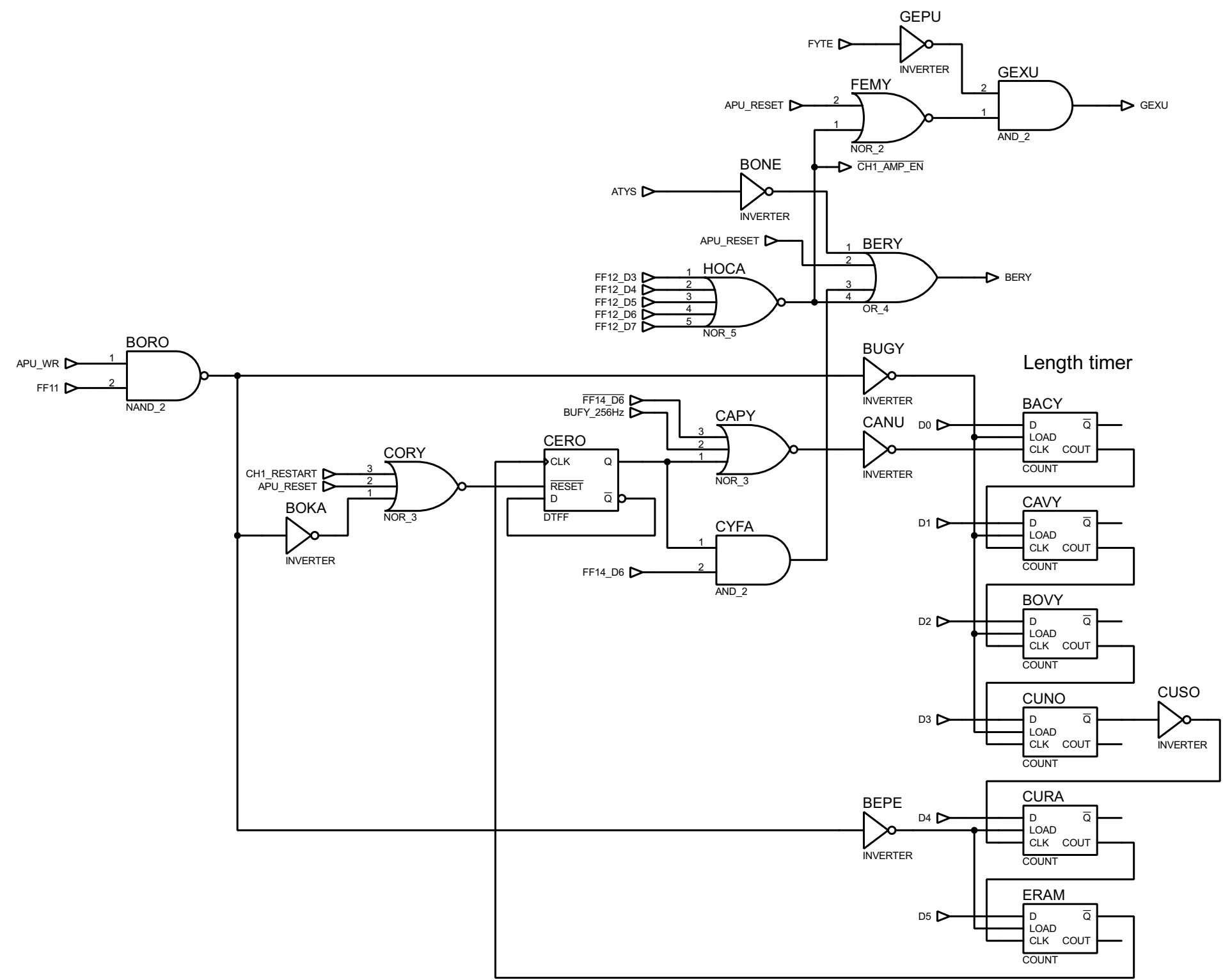
FF10 NR10



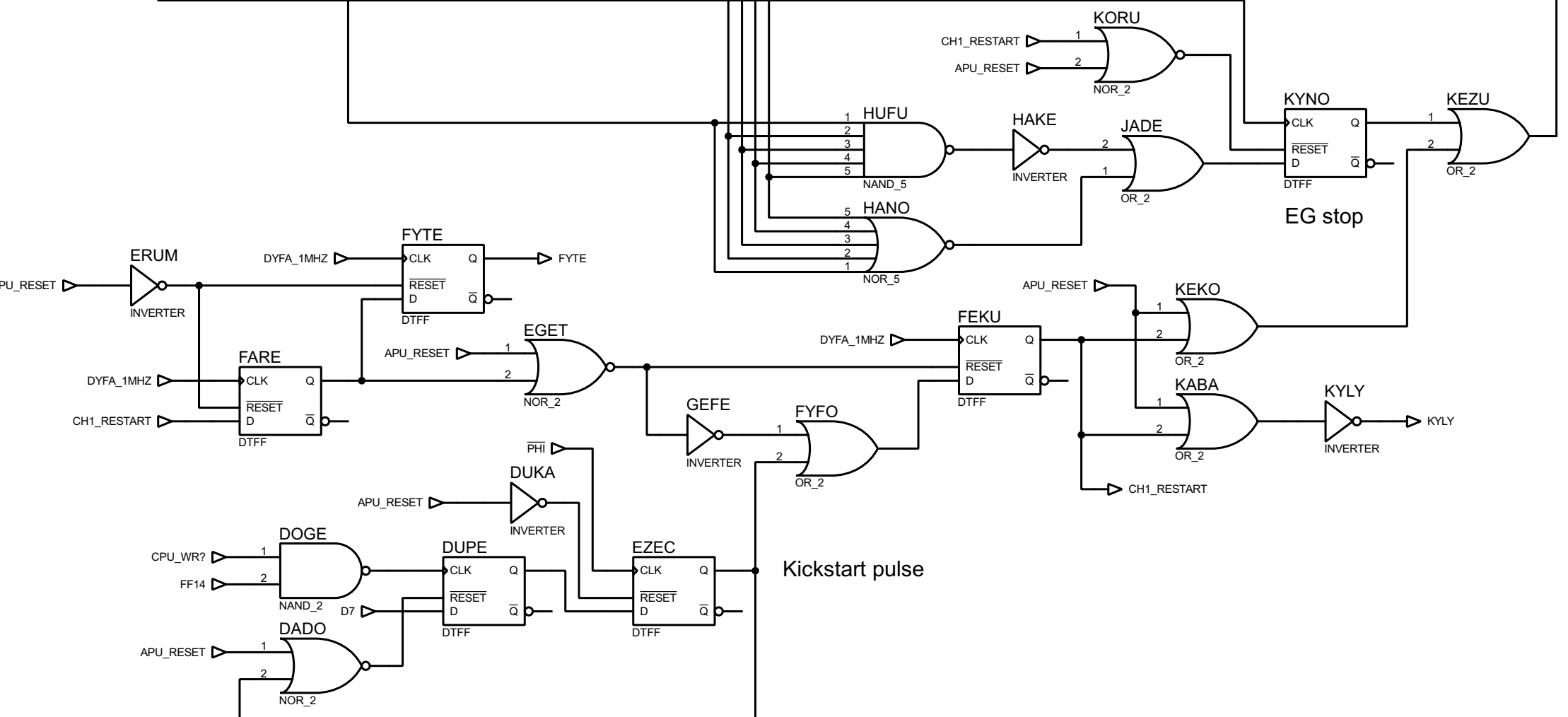
FF11 NR11



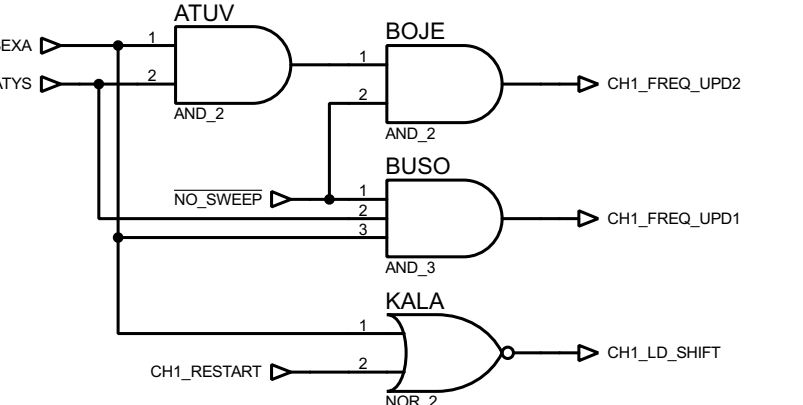




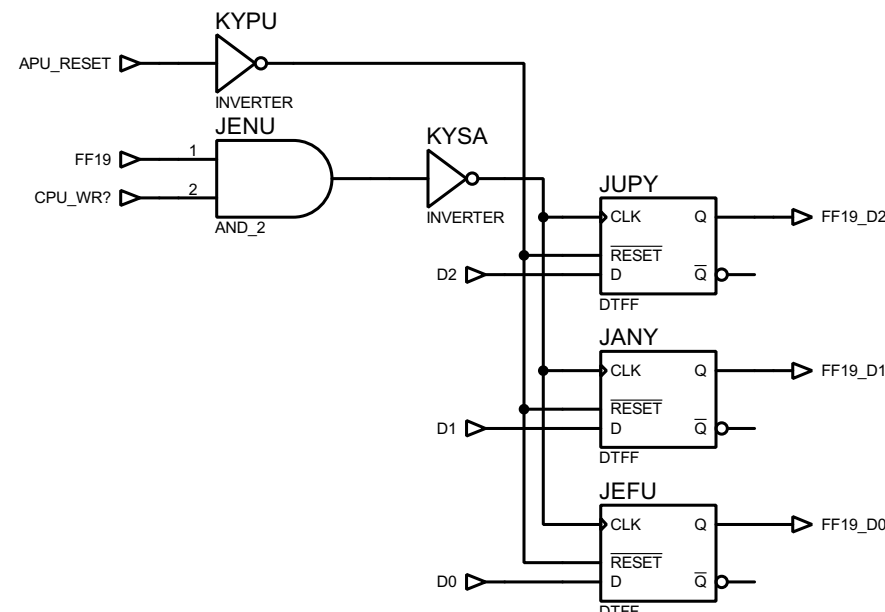
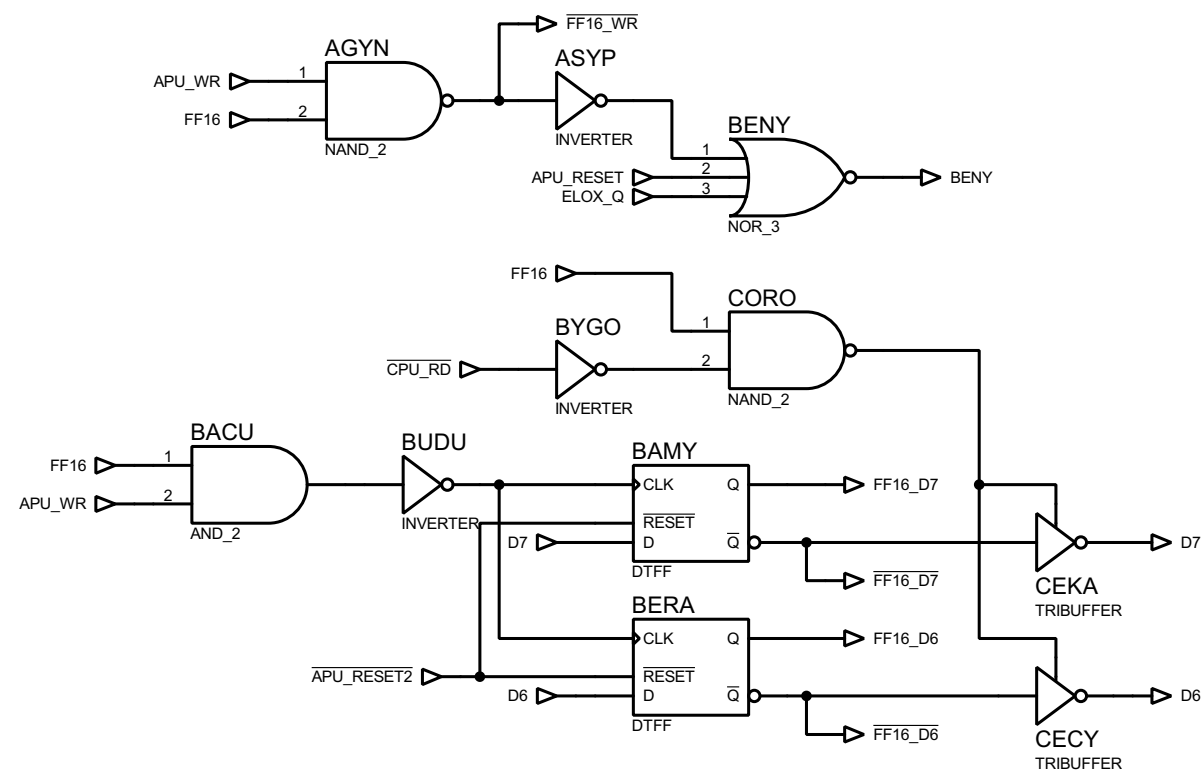
DUTY SELECT



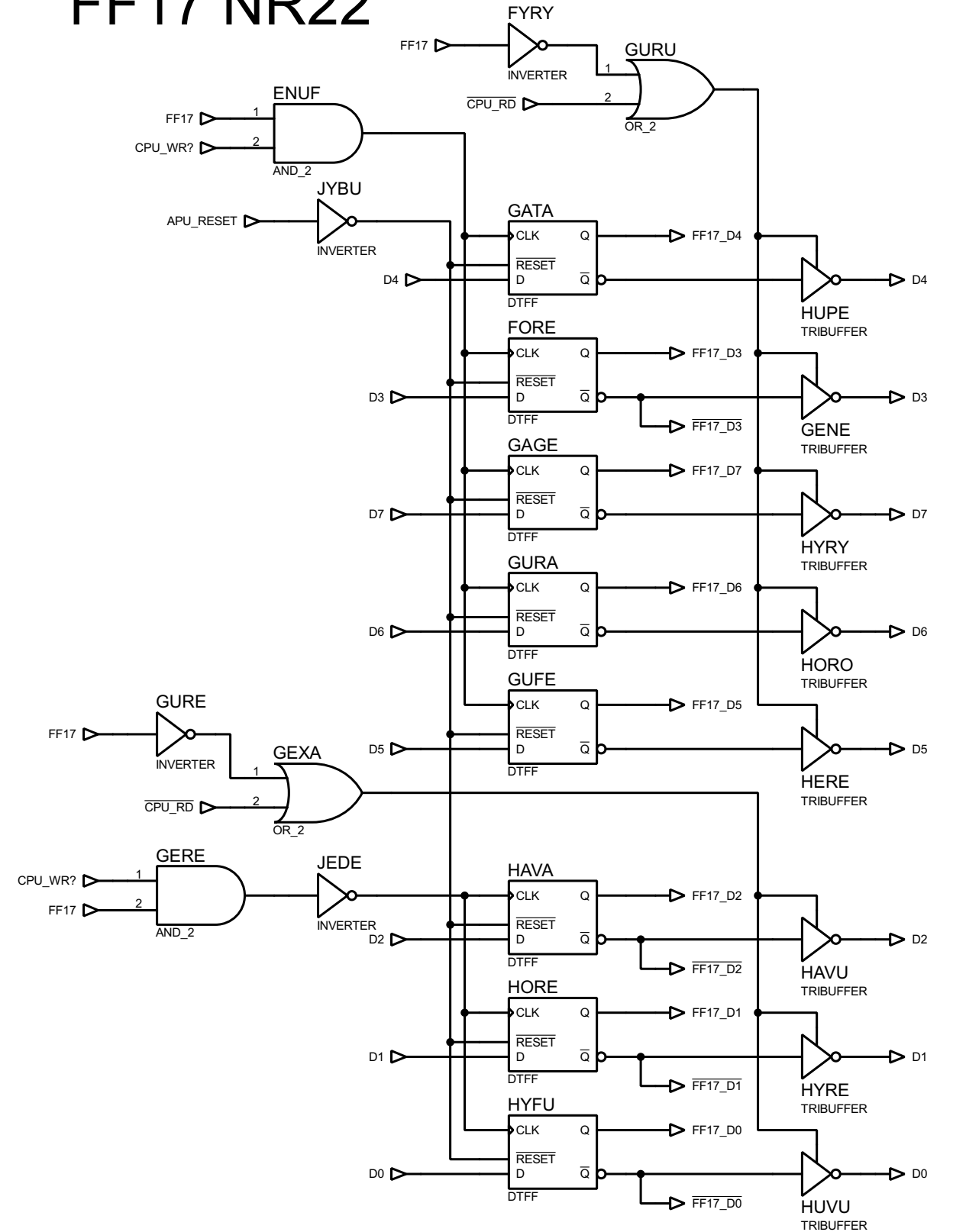
Kickstart pulse



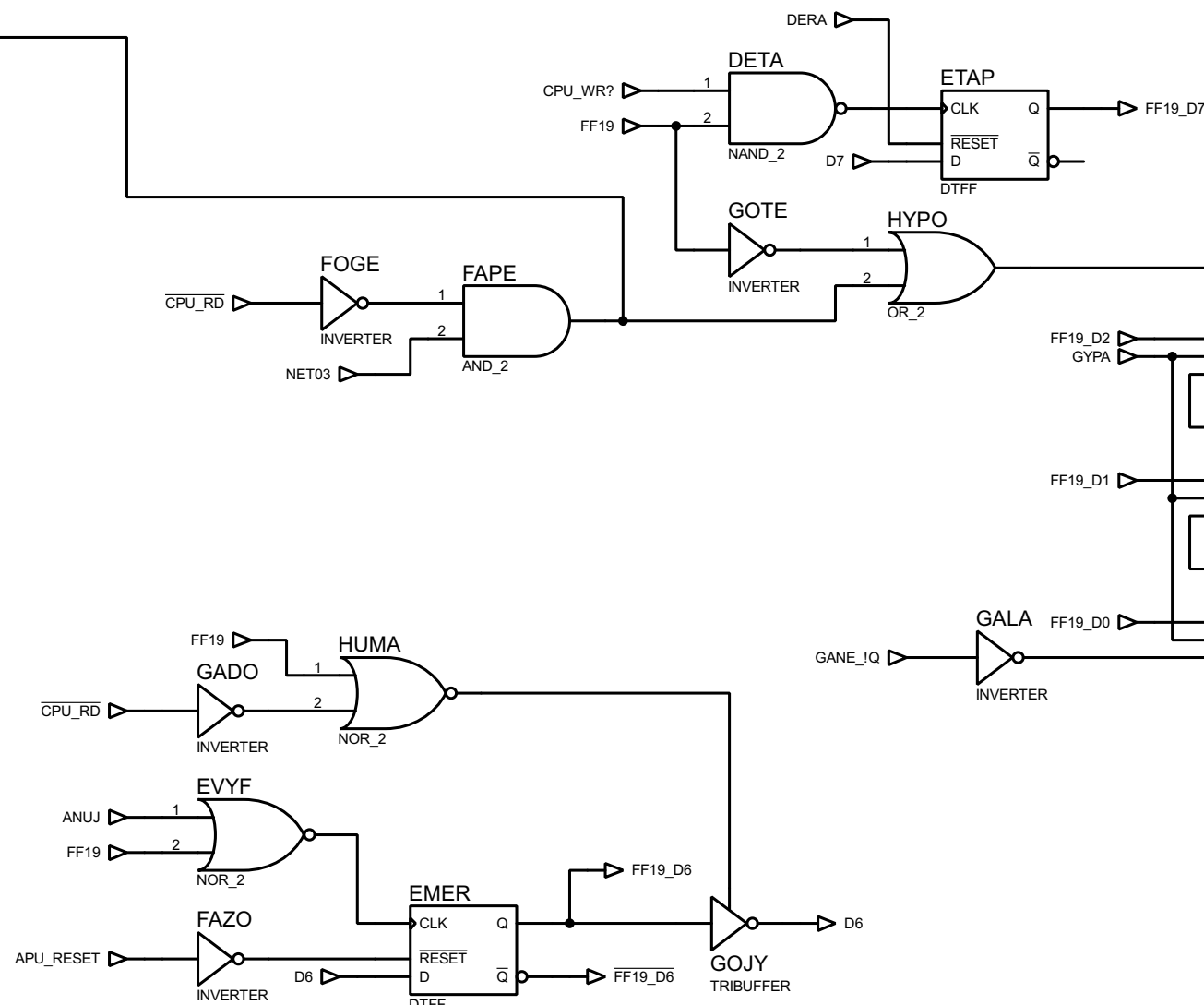
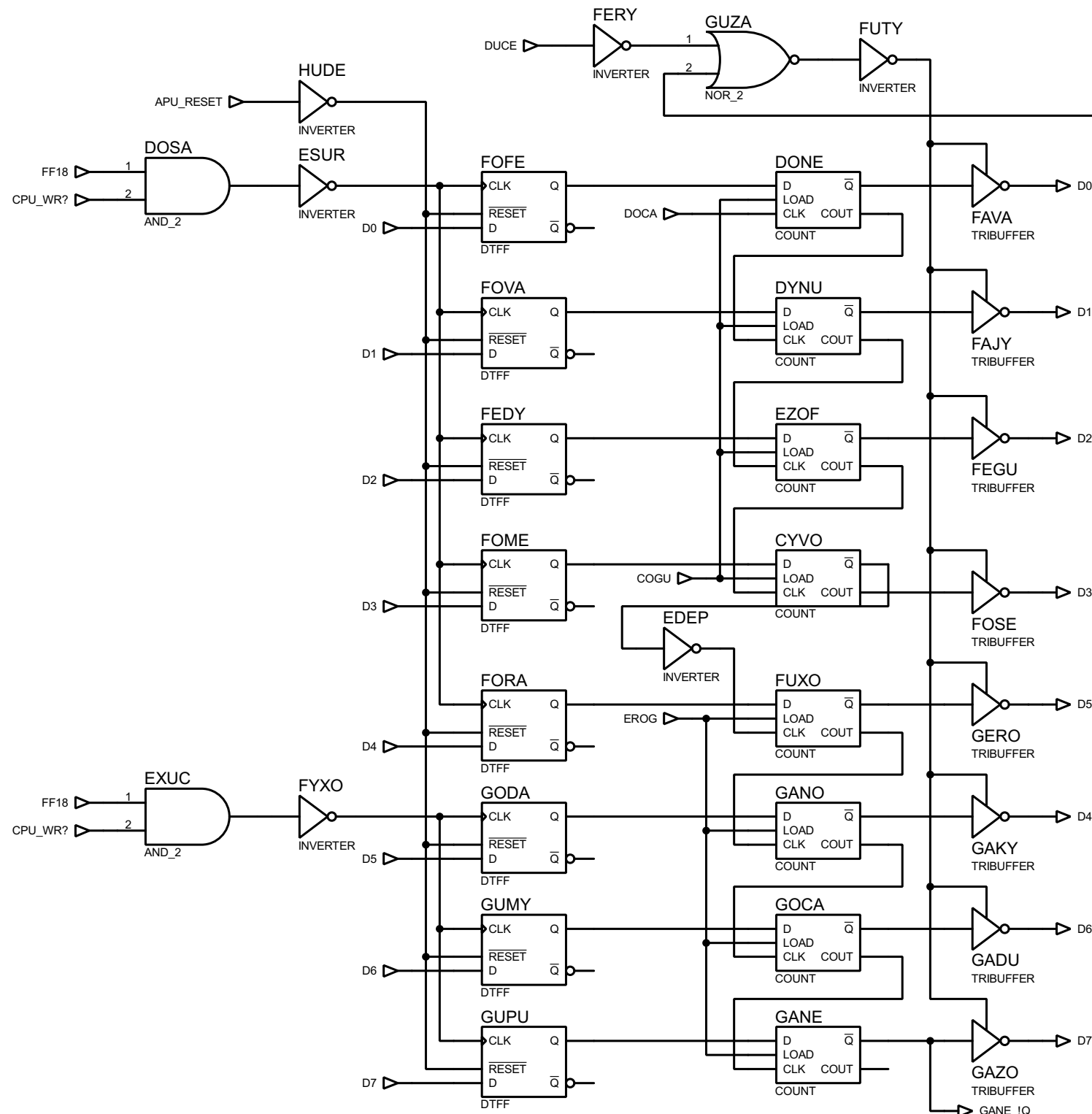
FF16 NR21



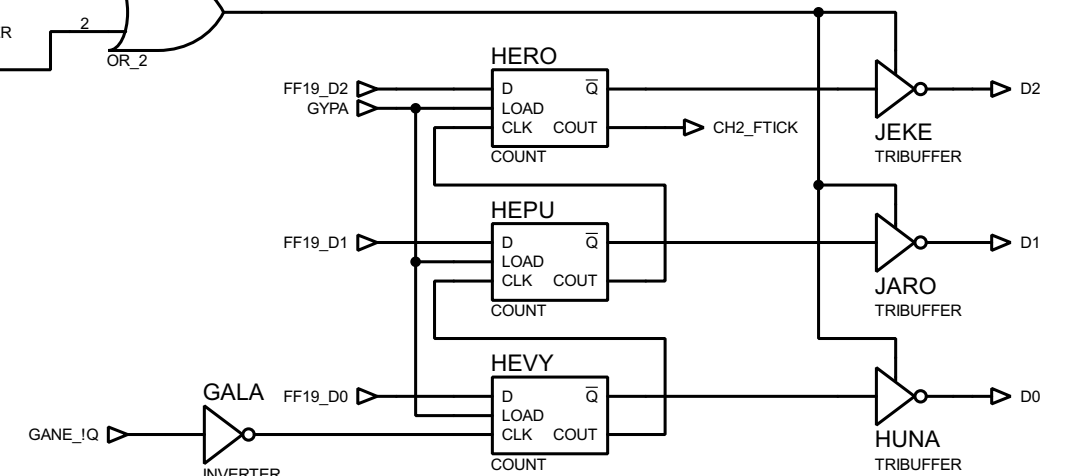
FF17 NR22

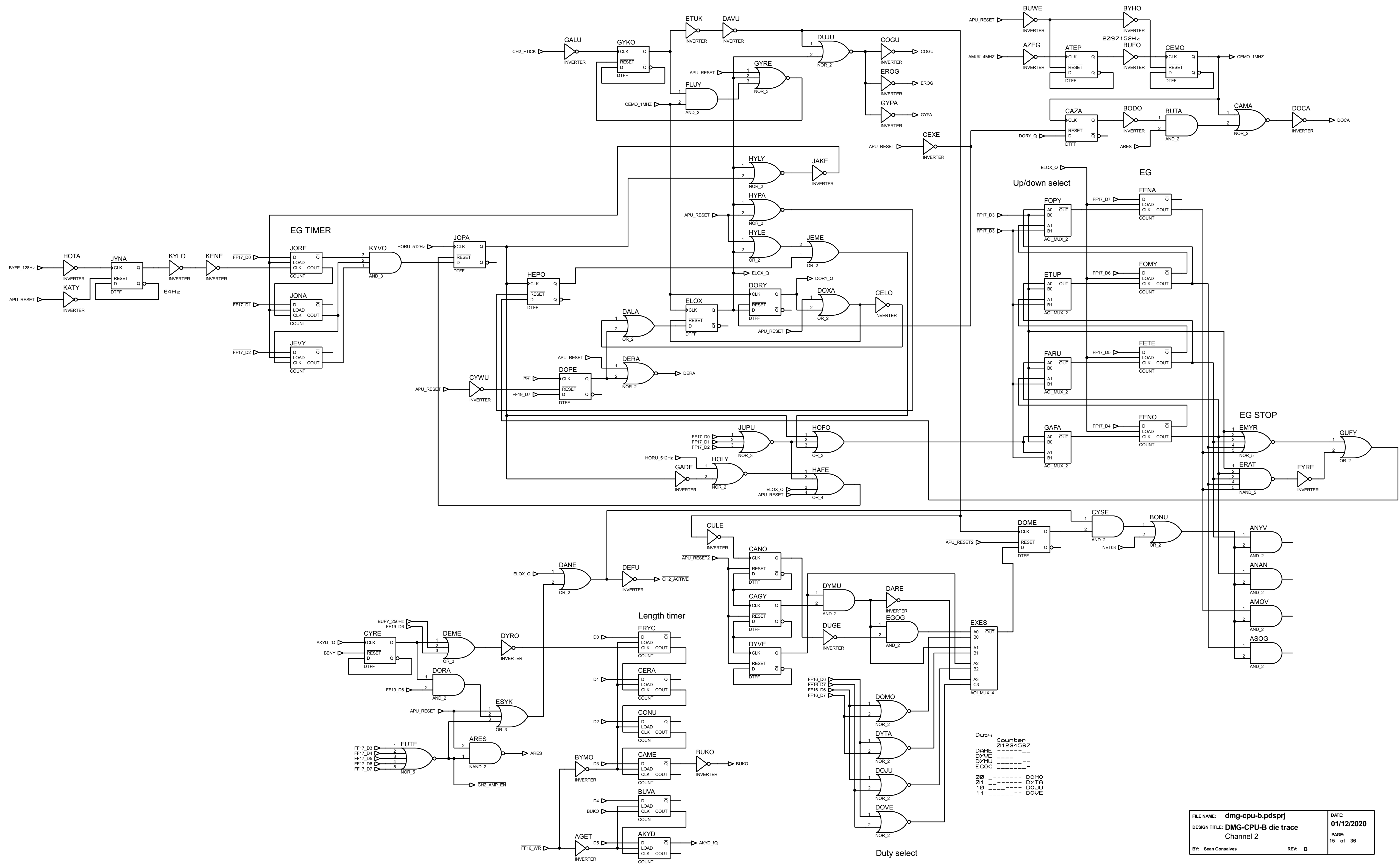


FF18 NR23

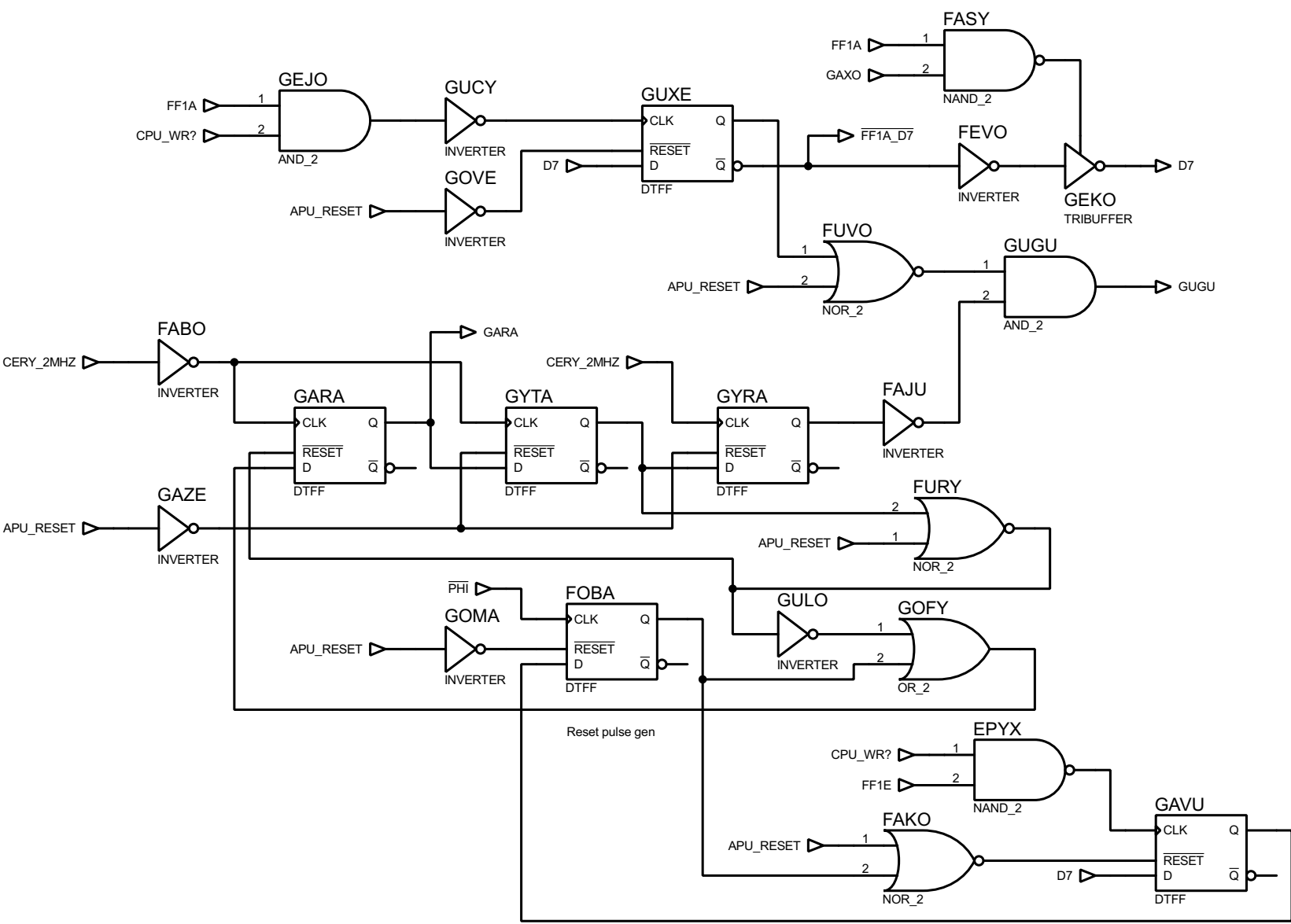


FF19 NR24

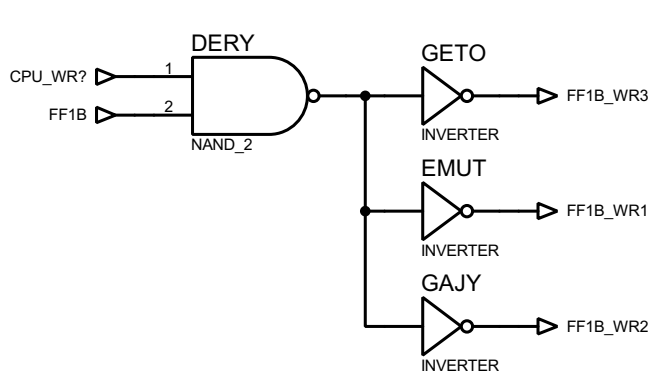




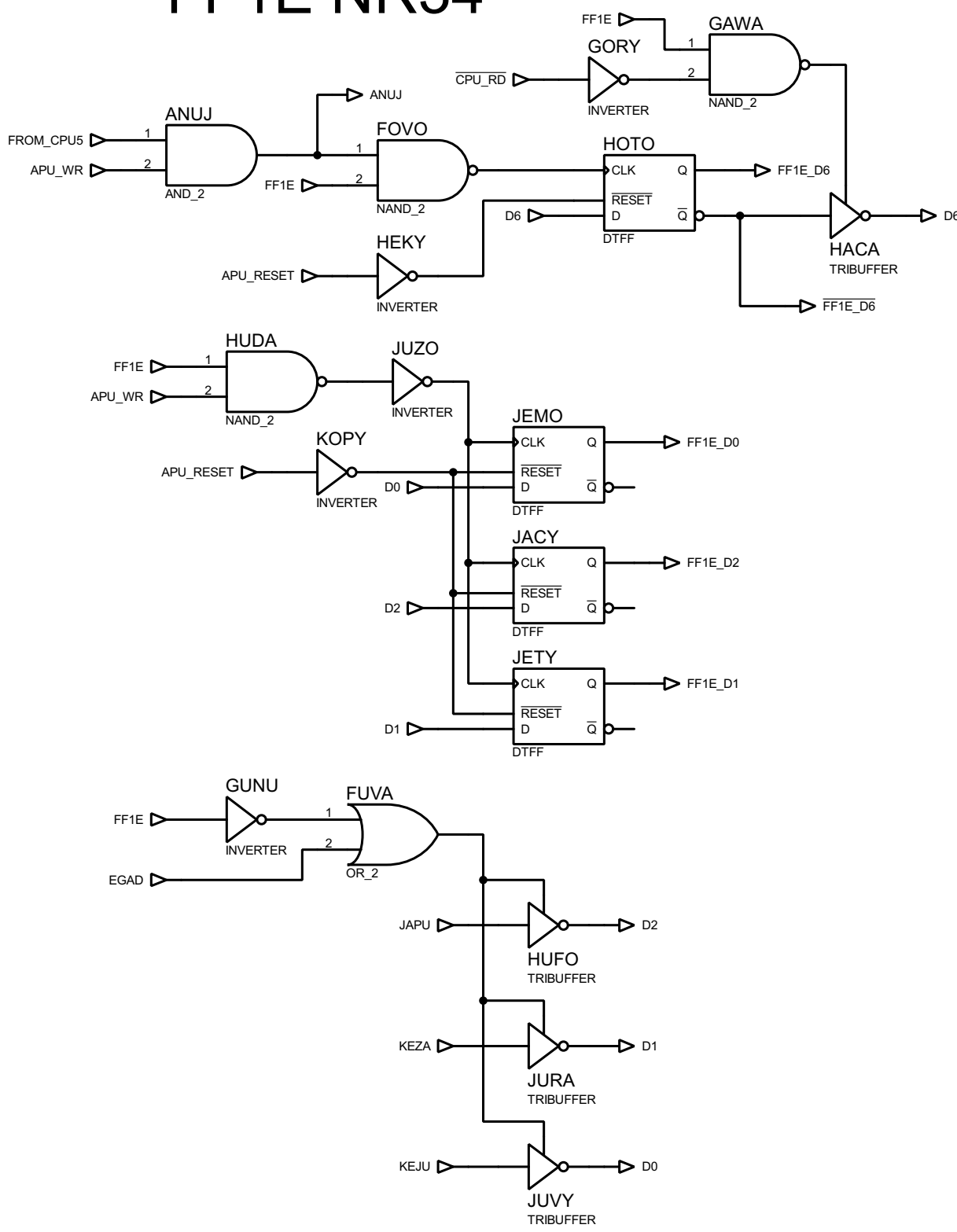
FF1A NR30



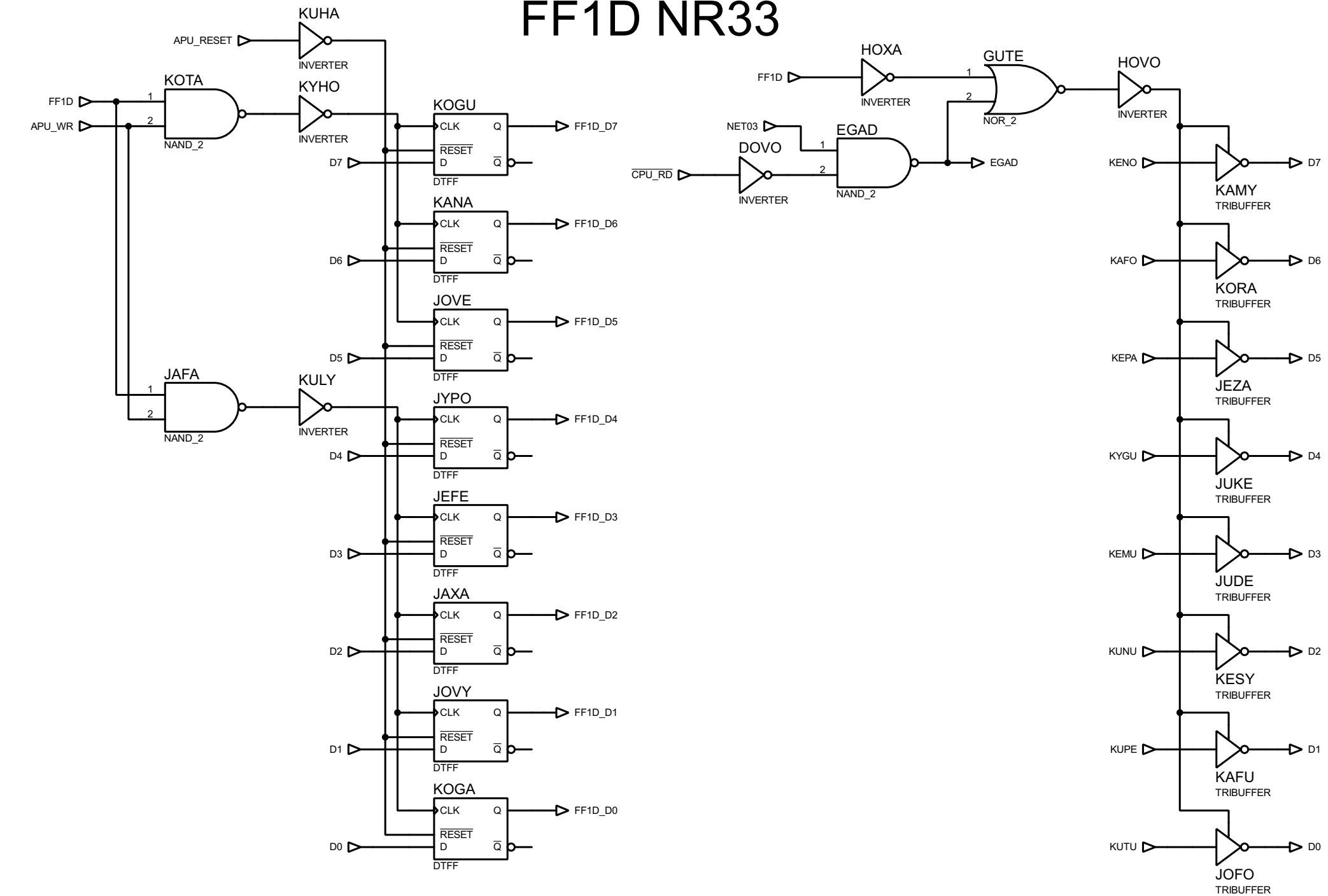
FF1B NR31



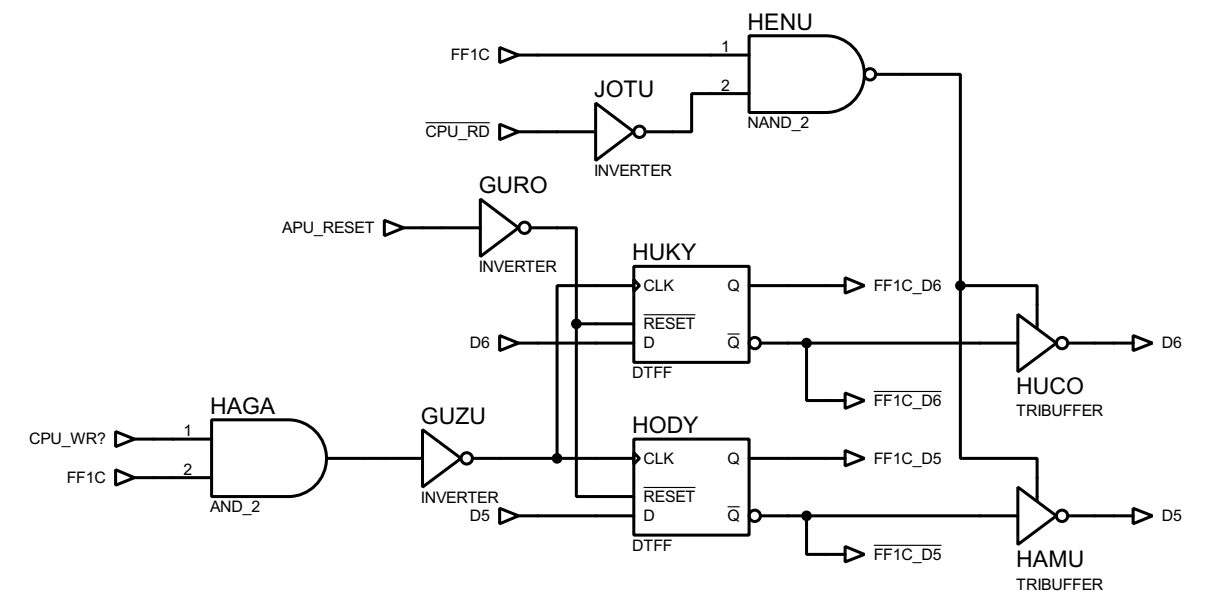
FF1E NR34

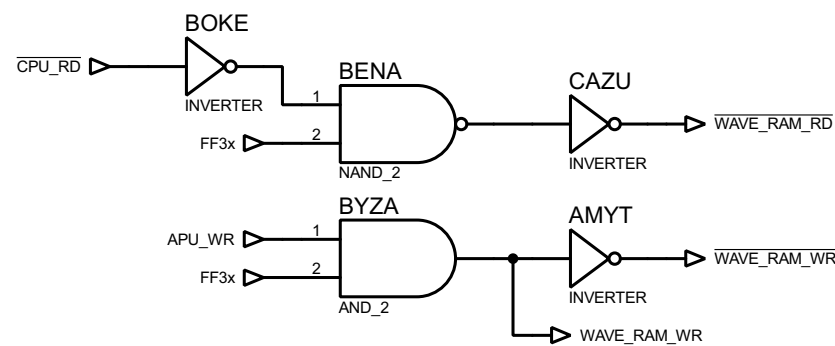
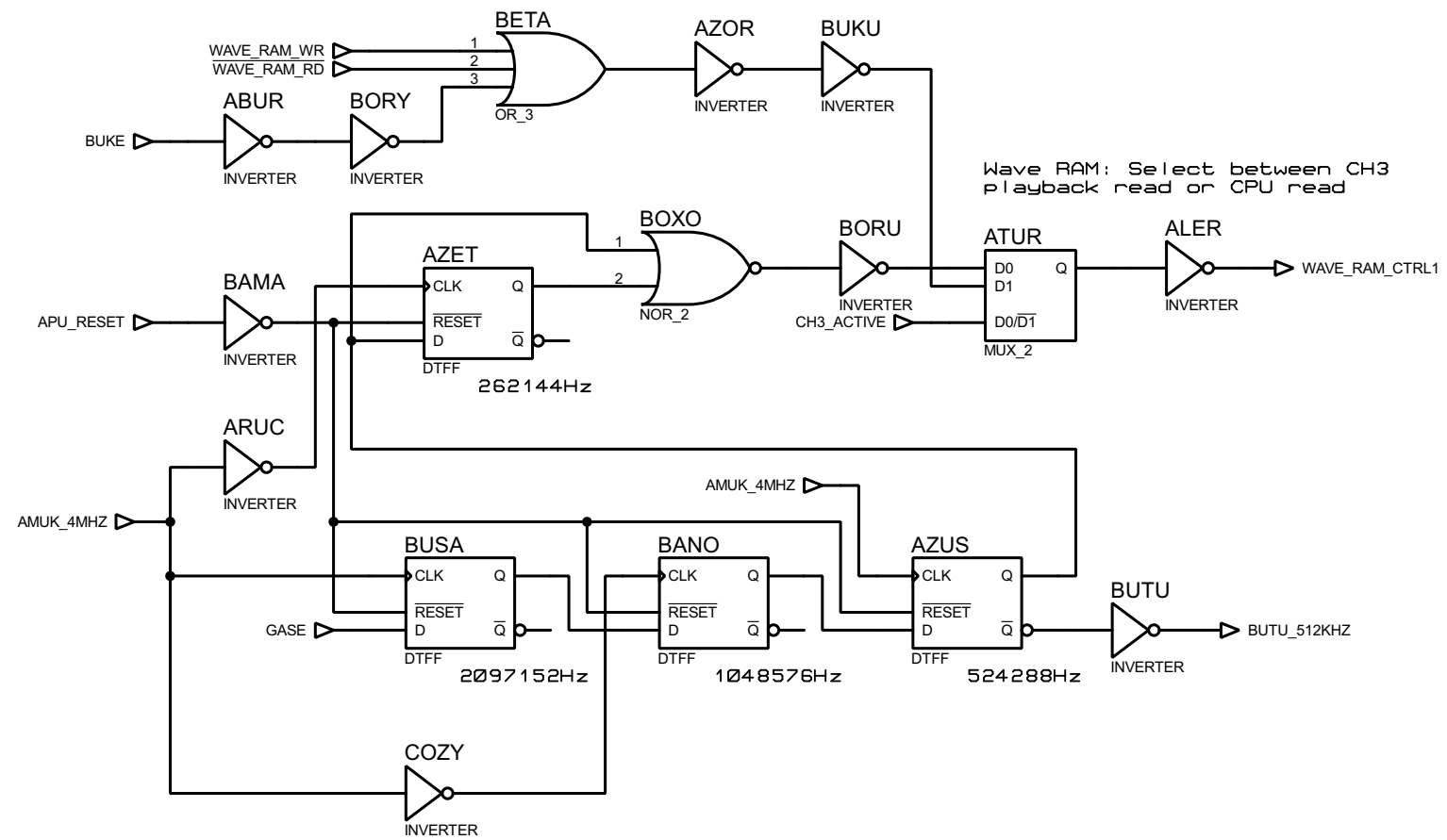
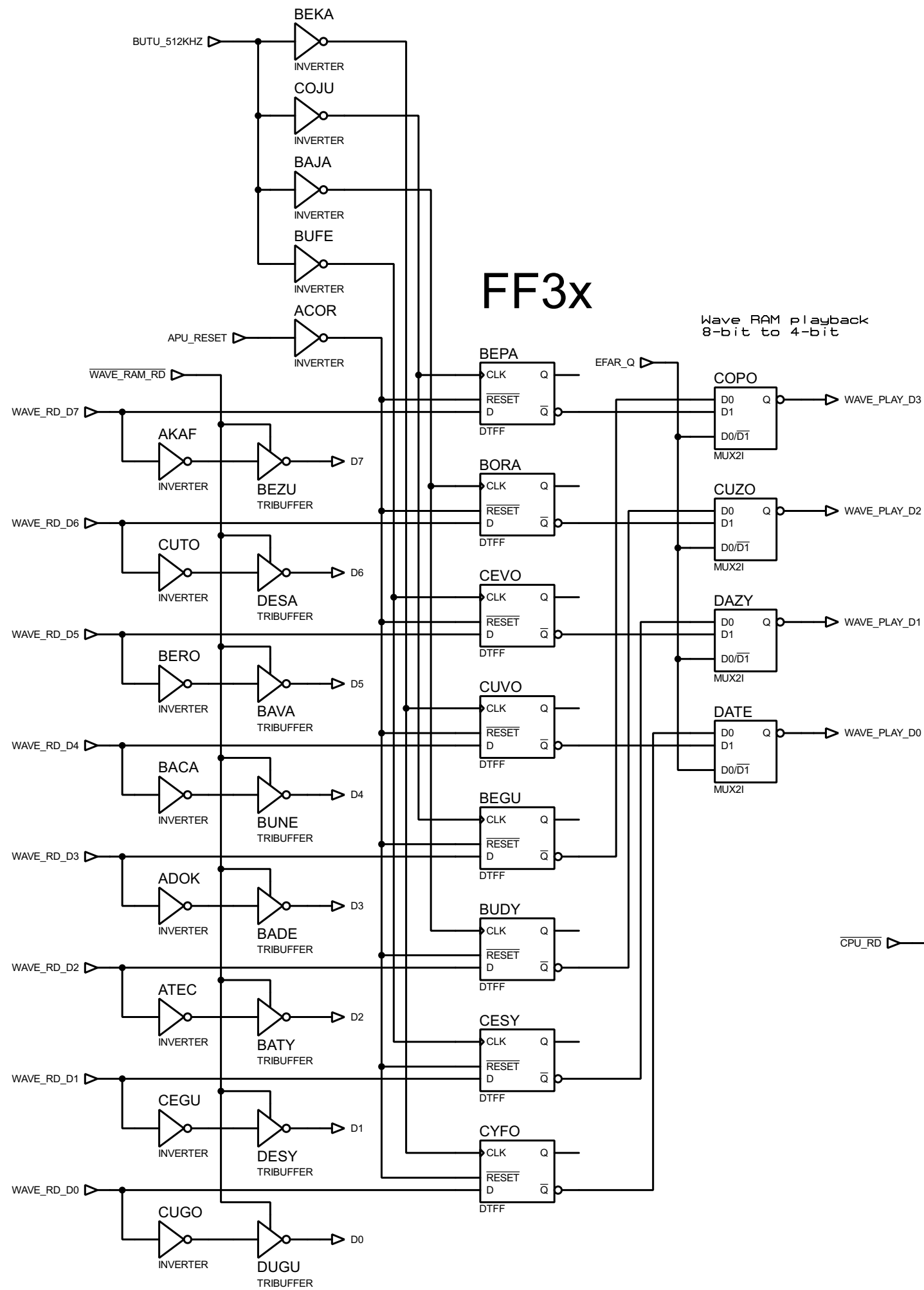


FF1D NR33

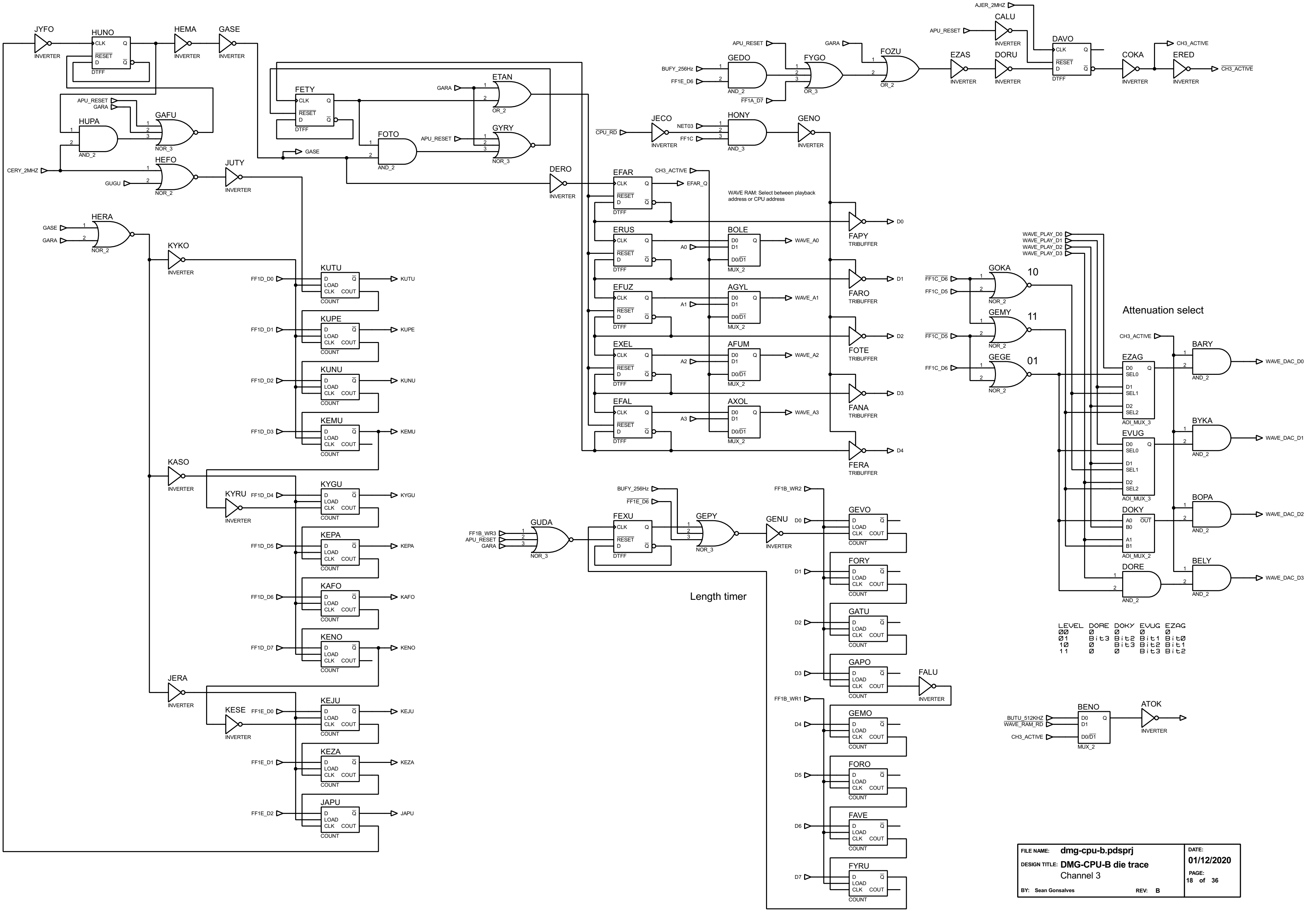


FF1C NR32

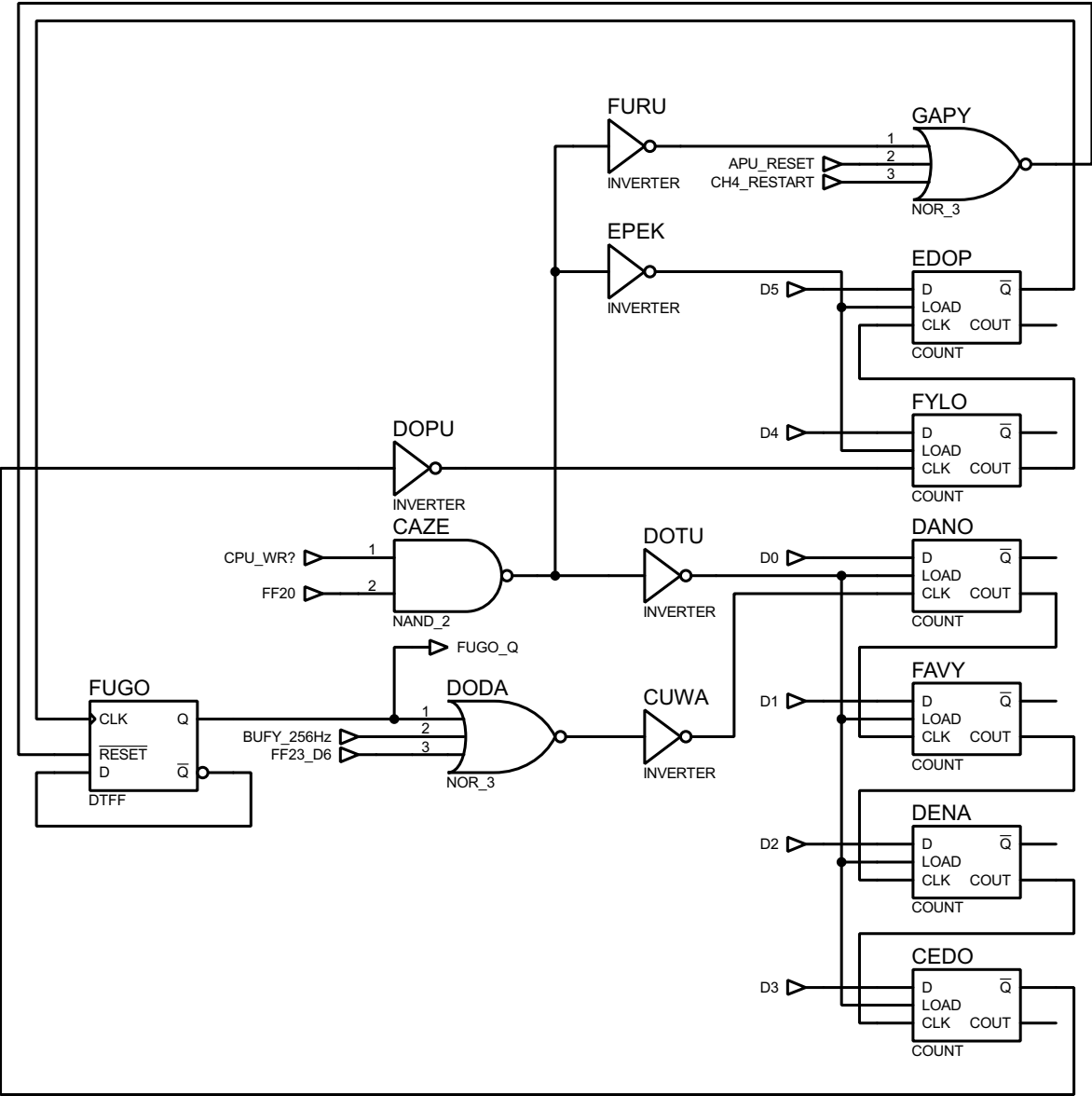




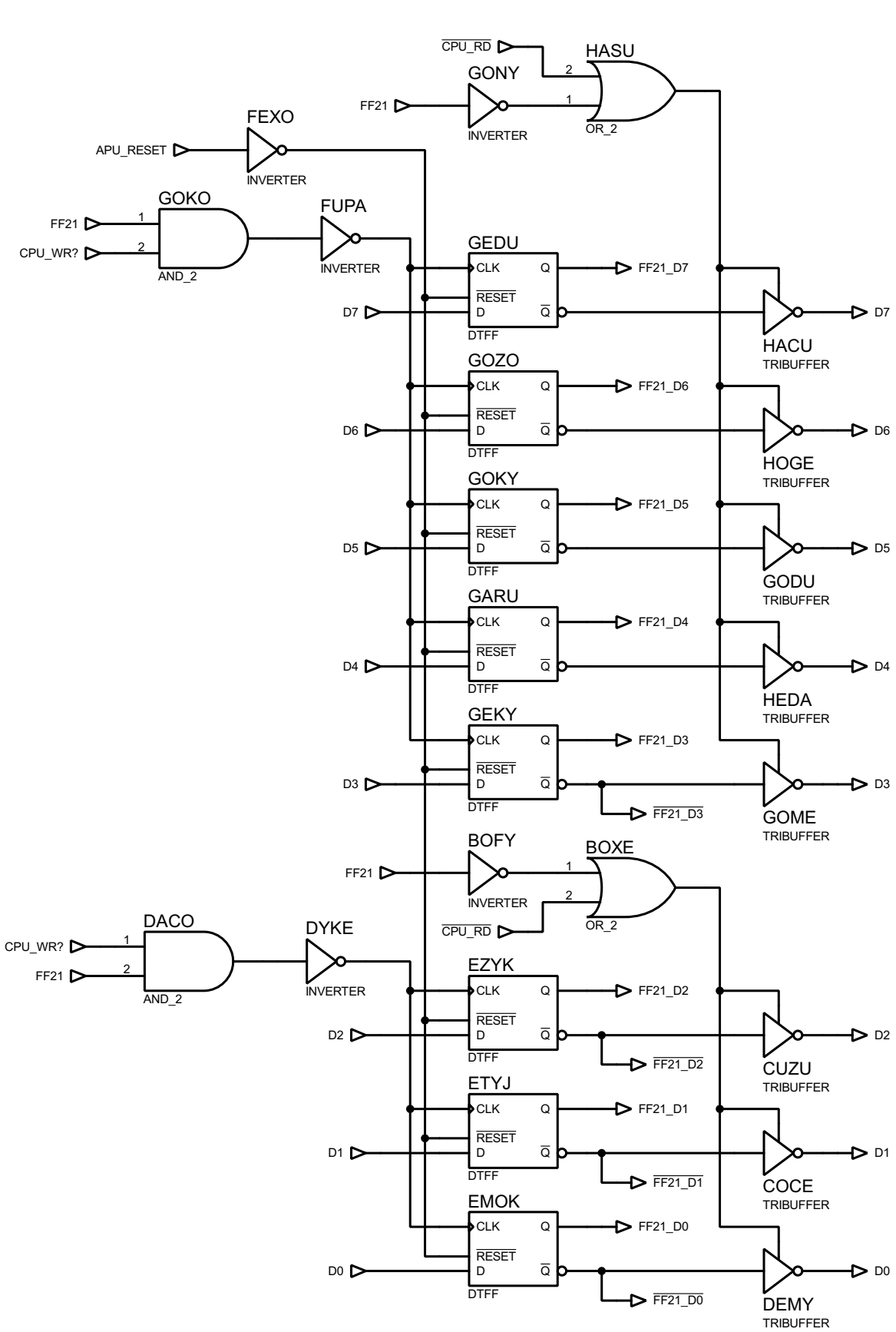
FILE NAME:	dmg-cpu-b.pdspri	DATE:	01/12/2020
DESIGN TITLE:	DMG-CPU-B die trace	PAGE:	17 of 36
BY:	Sean Gonsalves	REV:	B



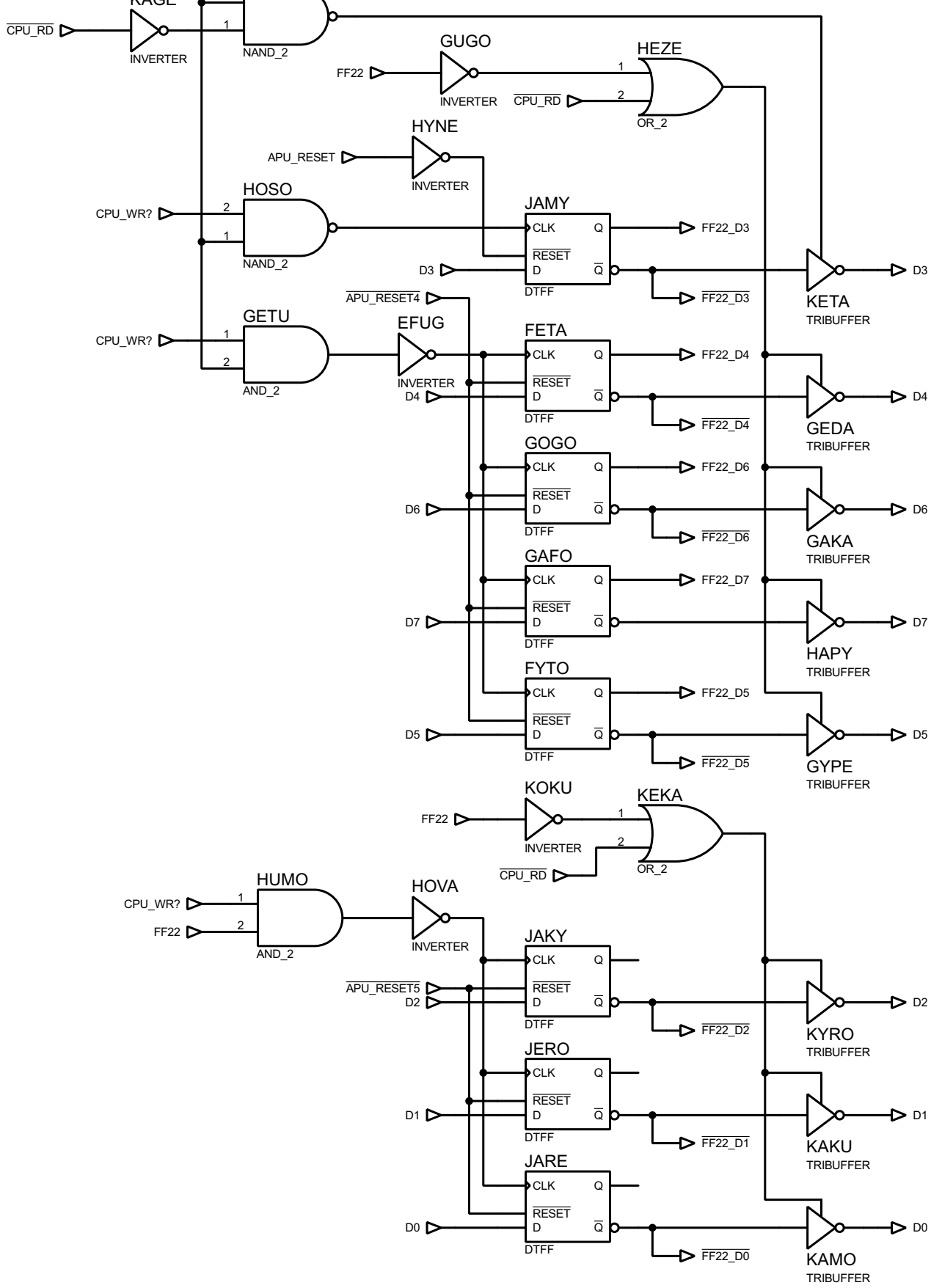
FF20 NR41



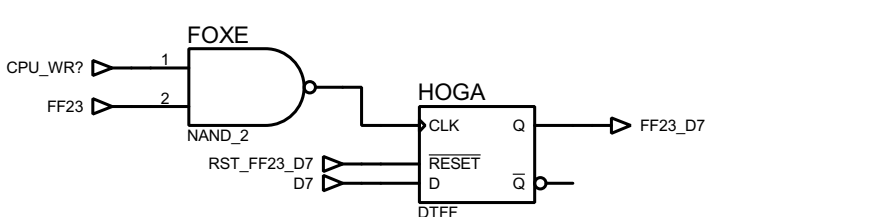
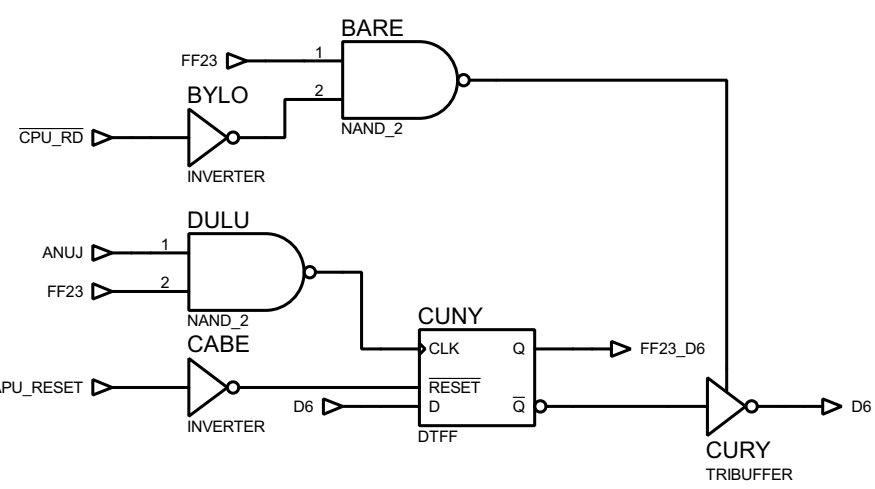
FF21 NR42

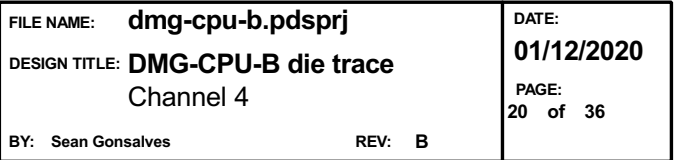


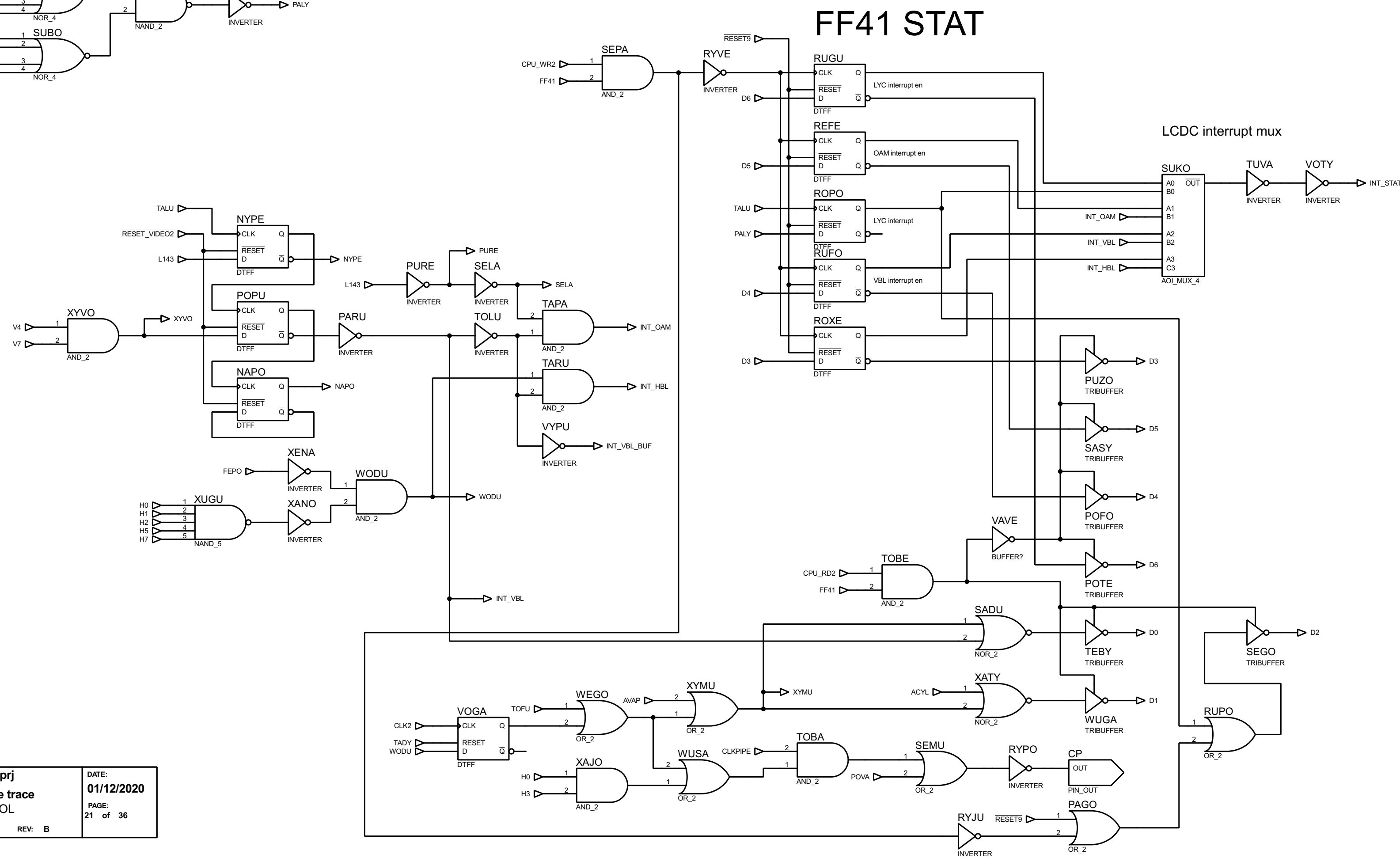
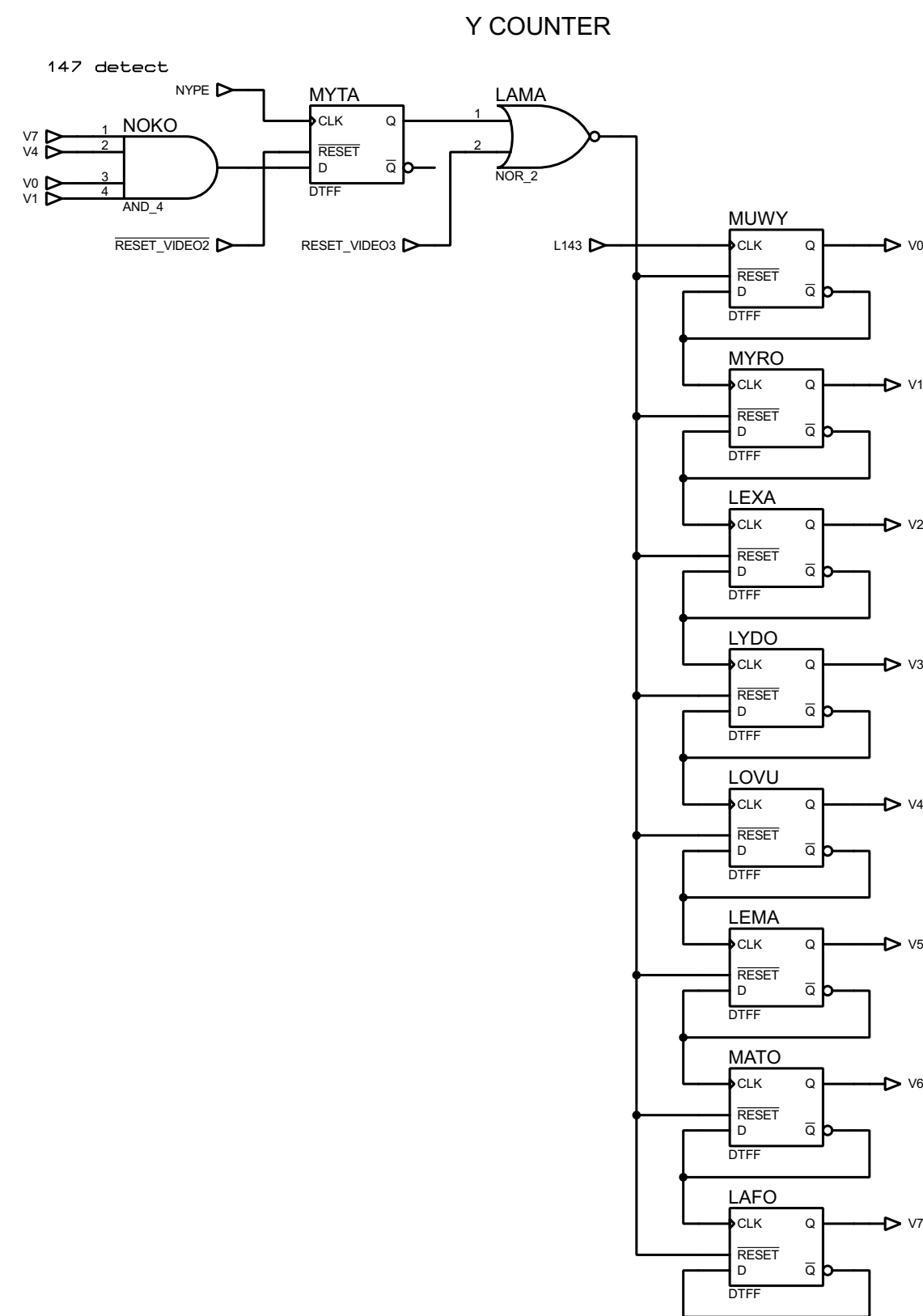
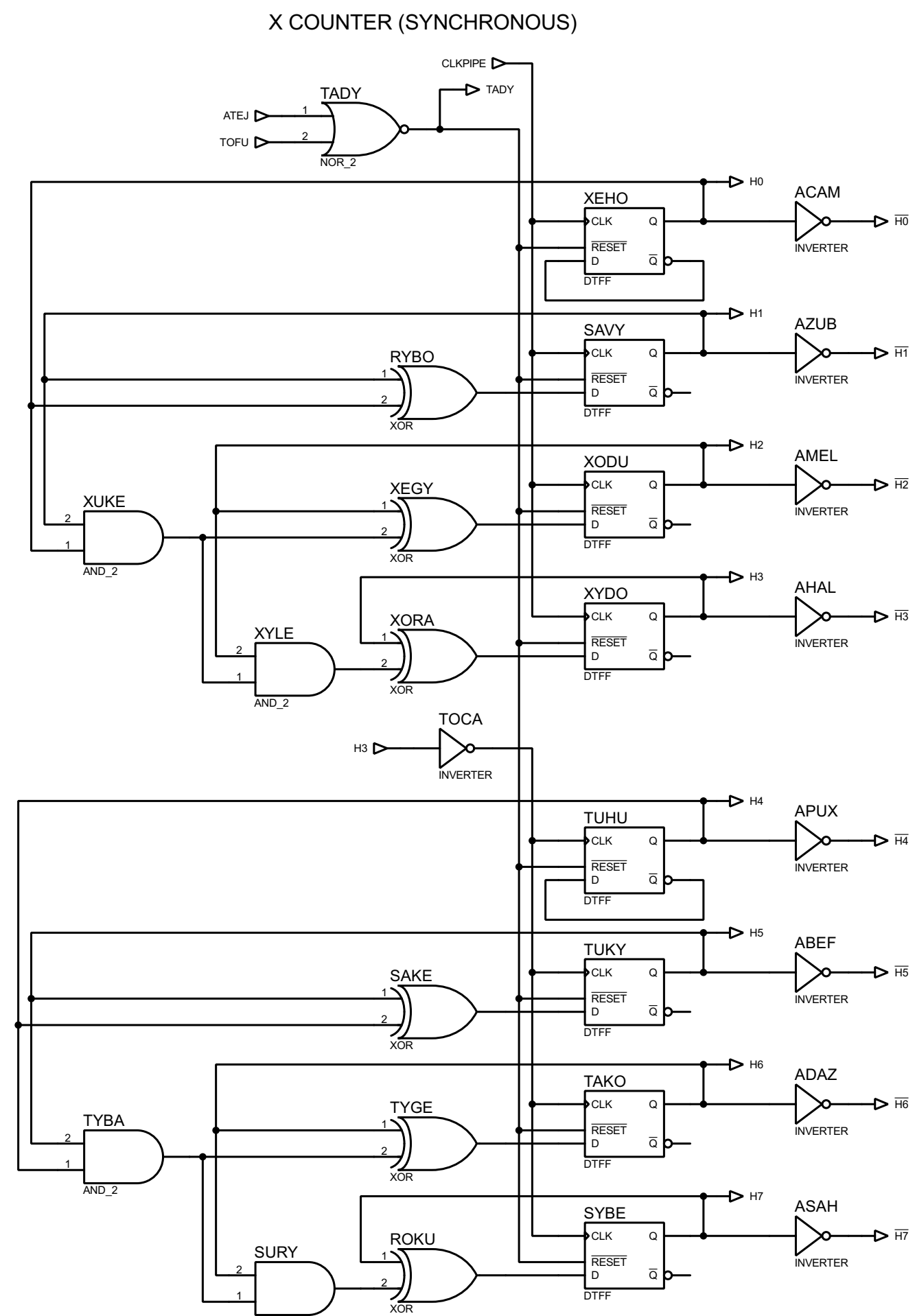
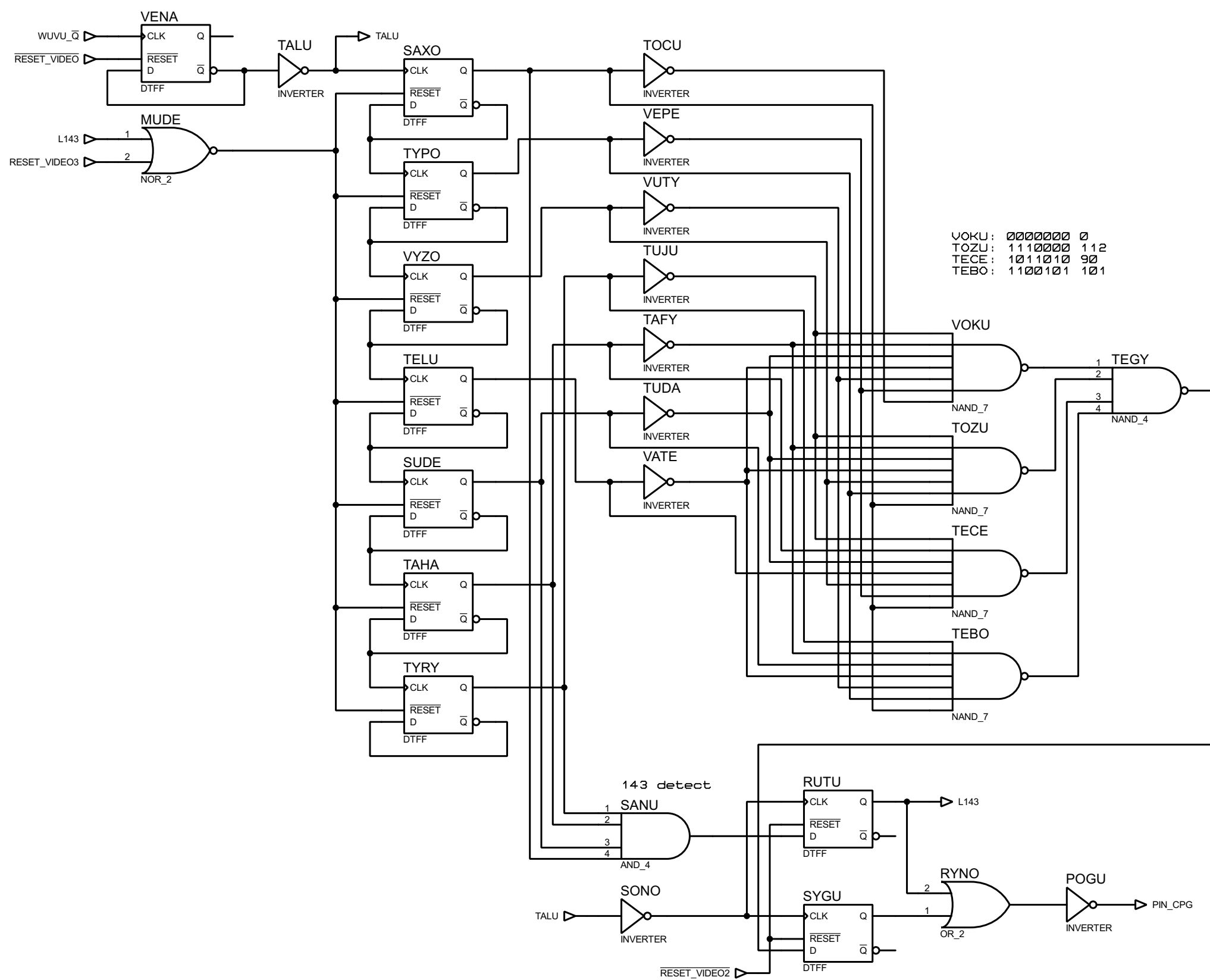
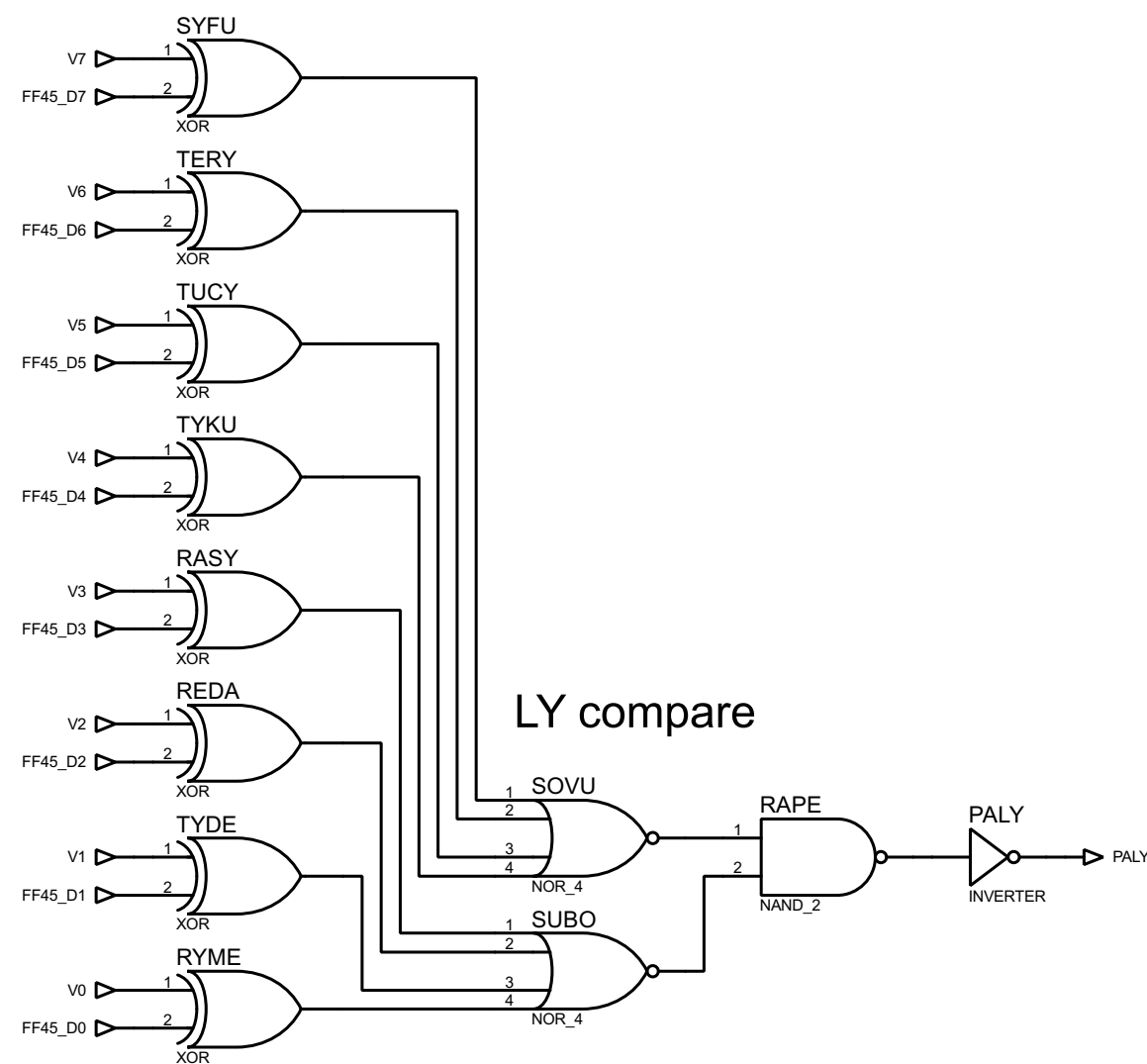
FF22 NR43

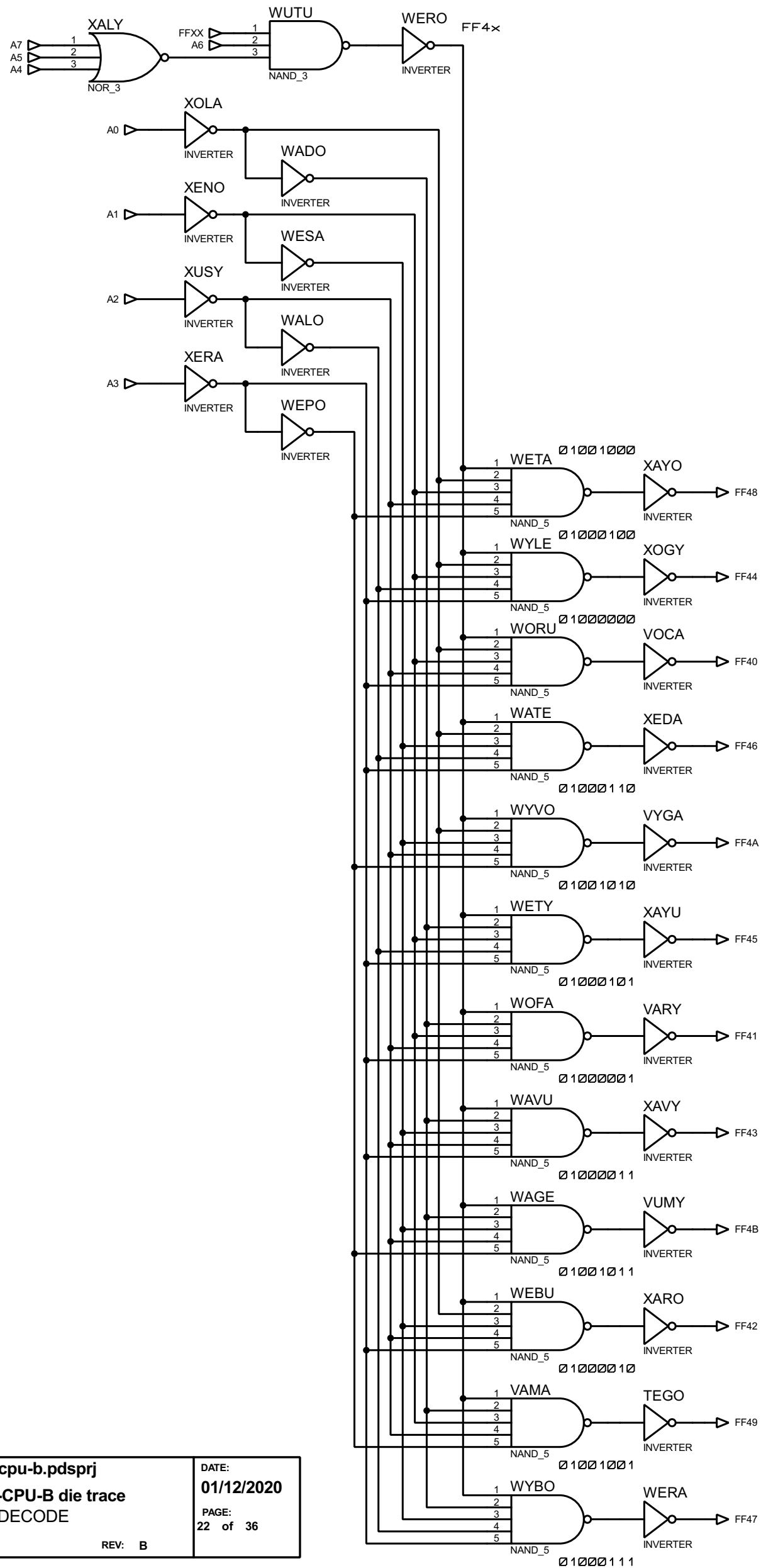


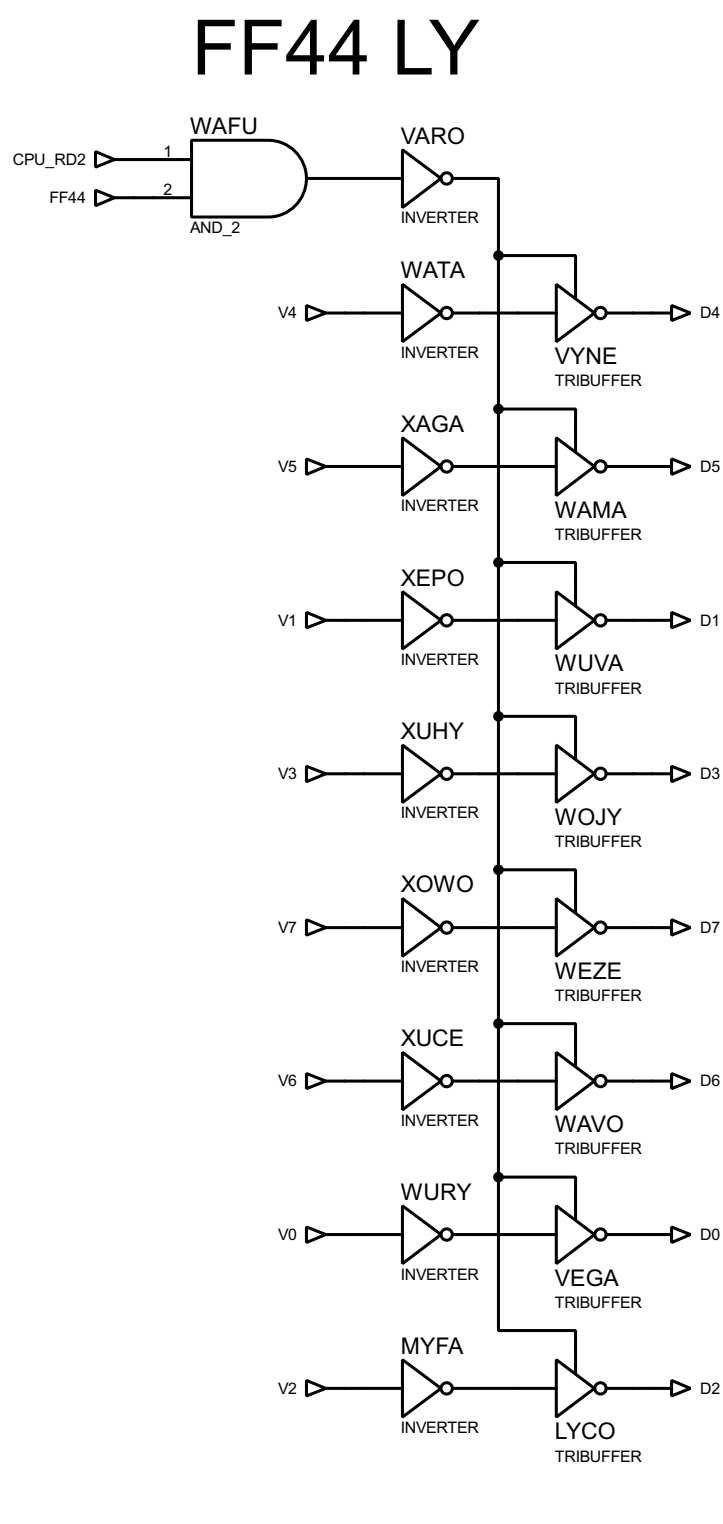
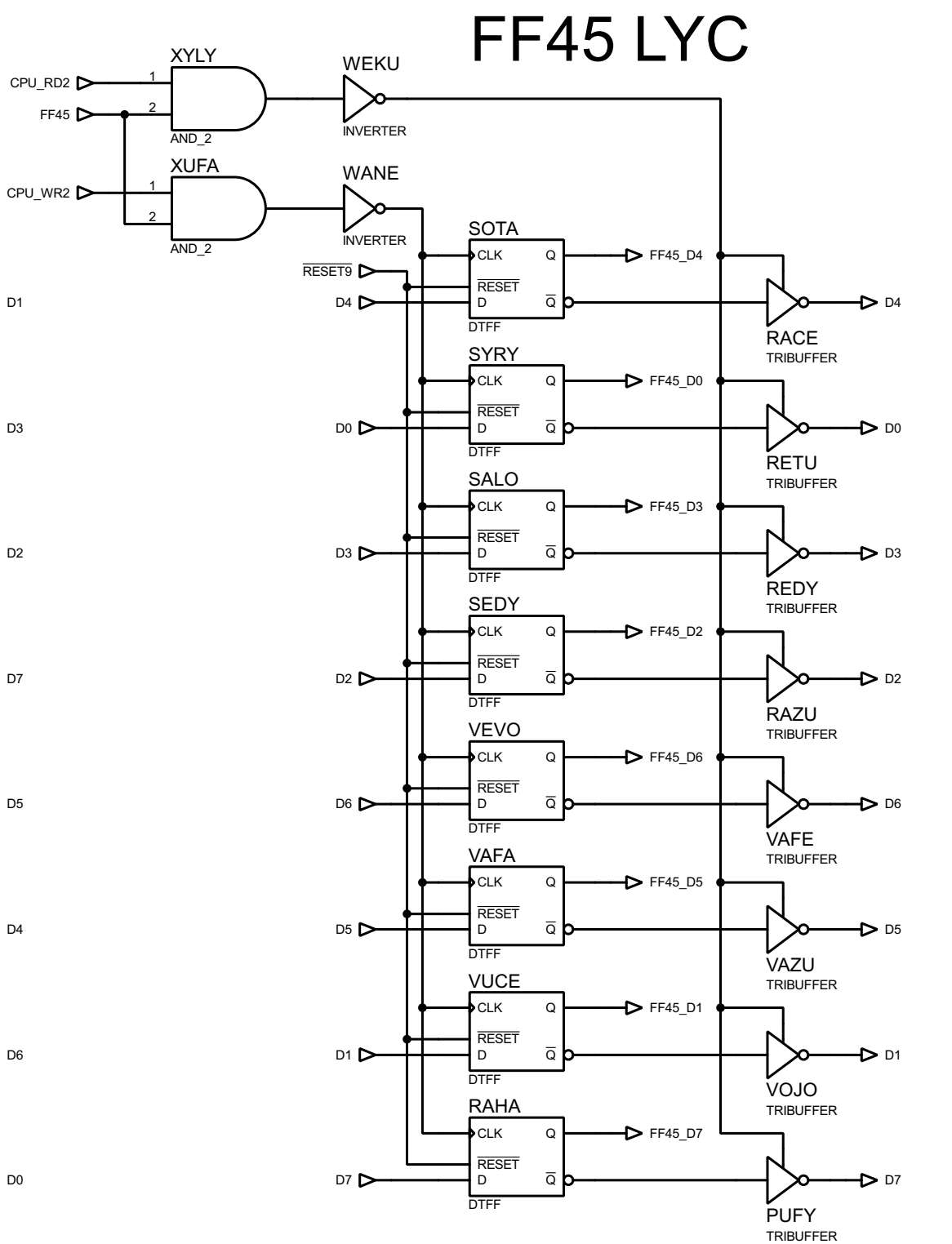
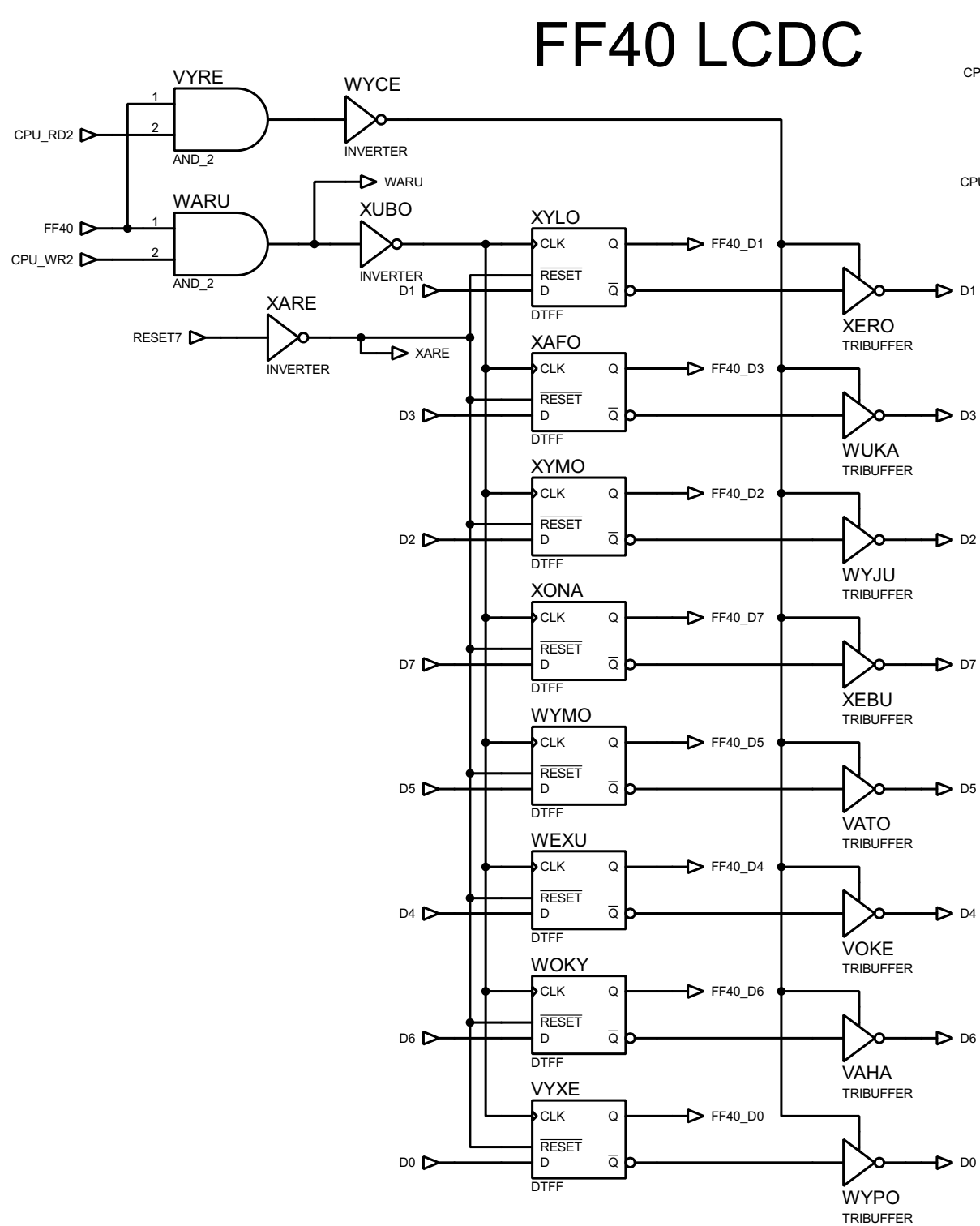
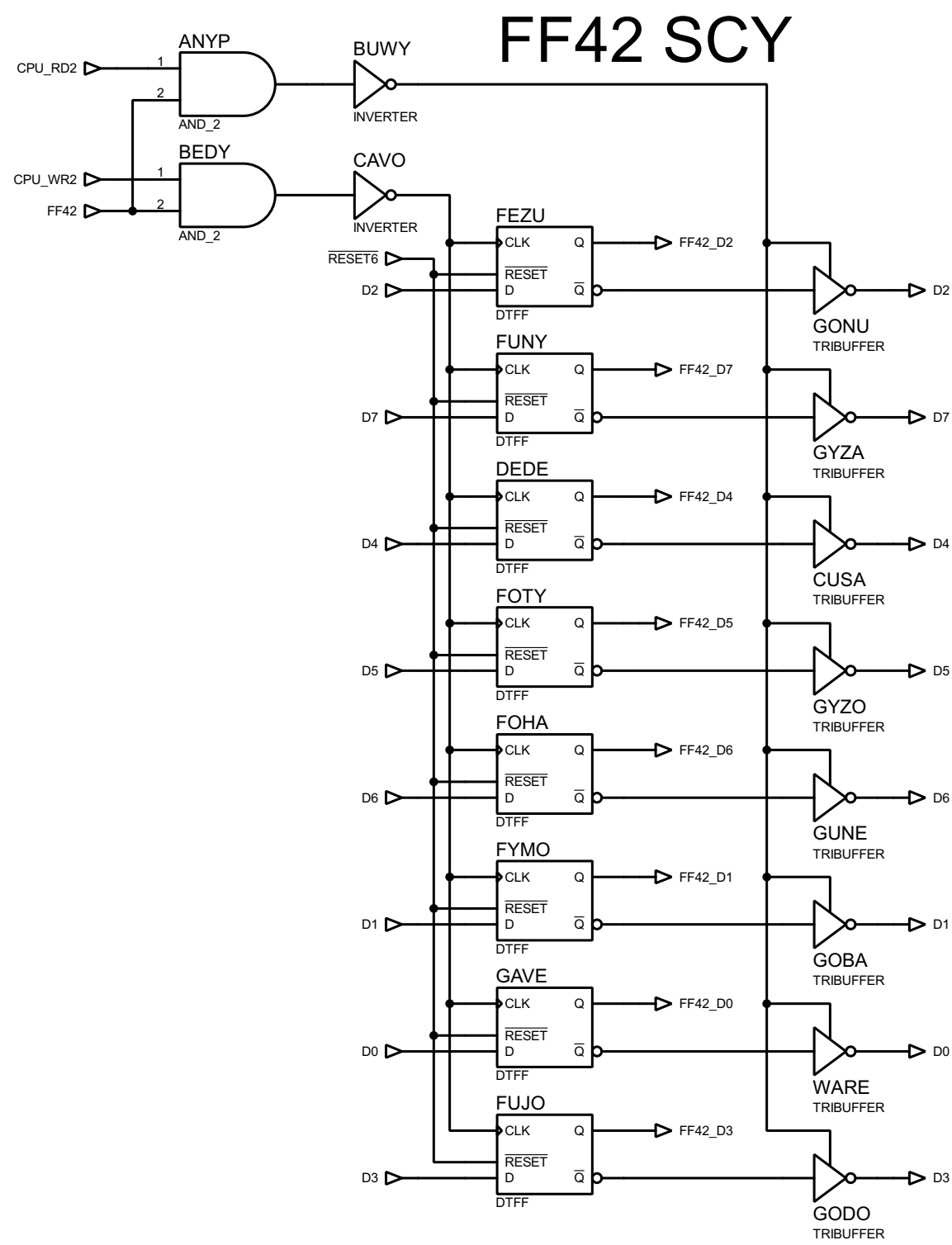
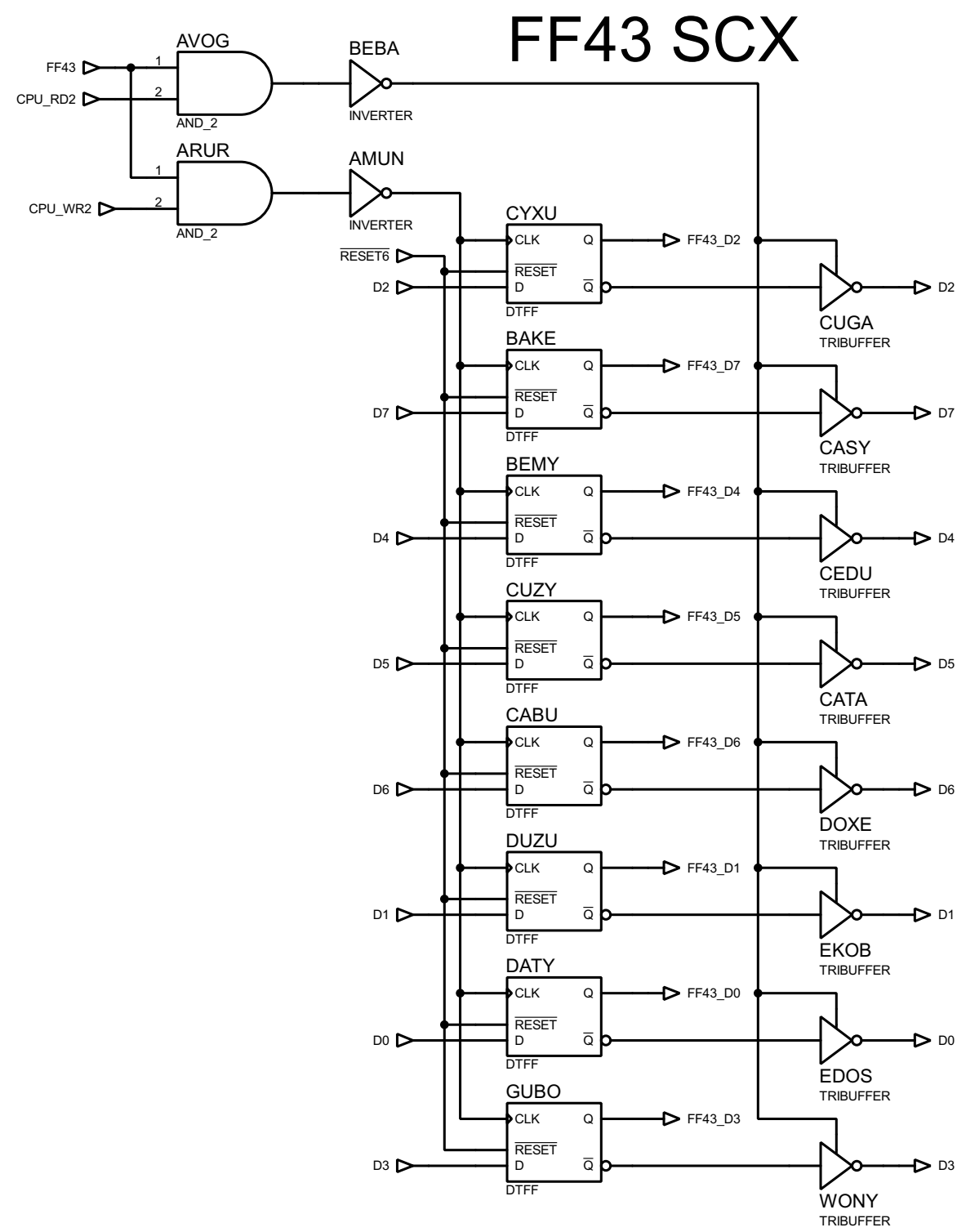
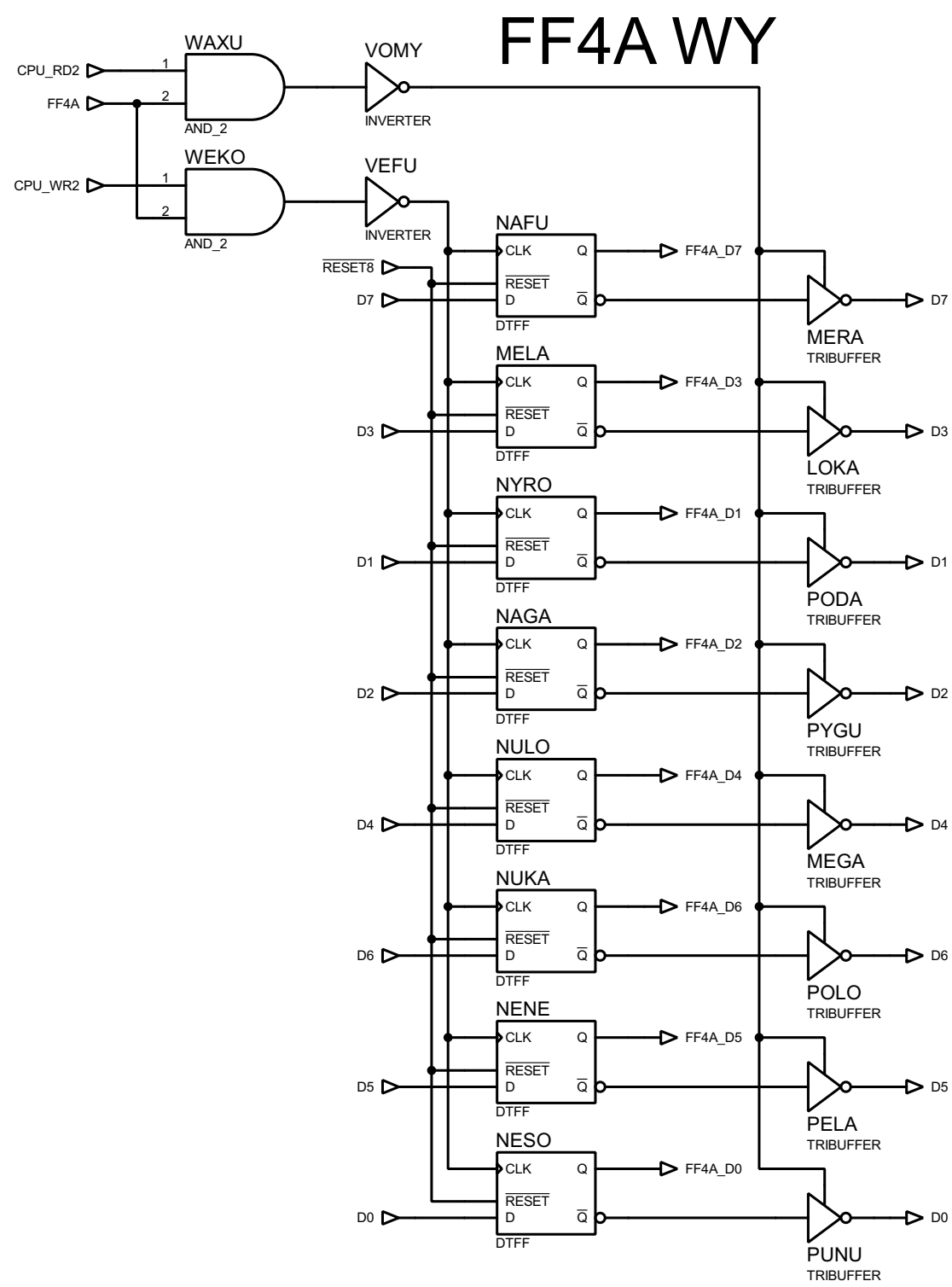
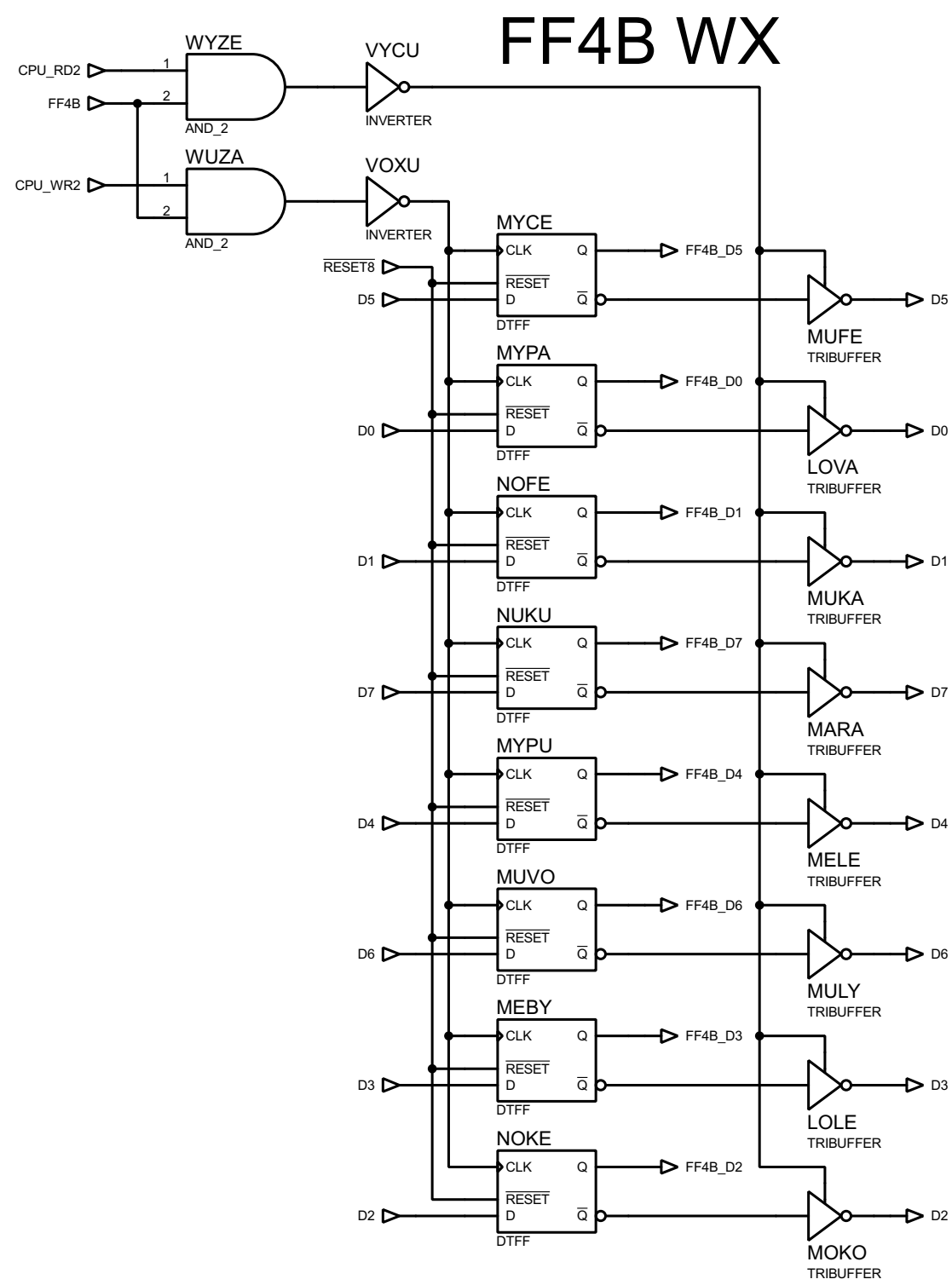
FF23 NR44

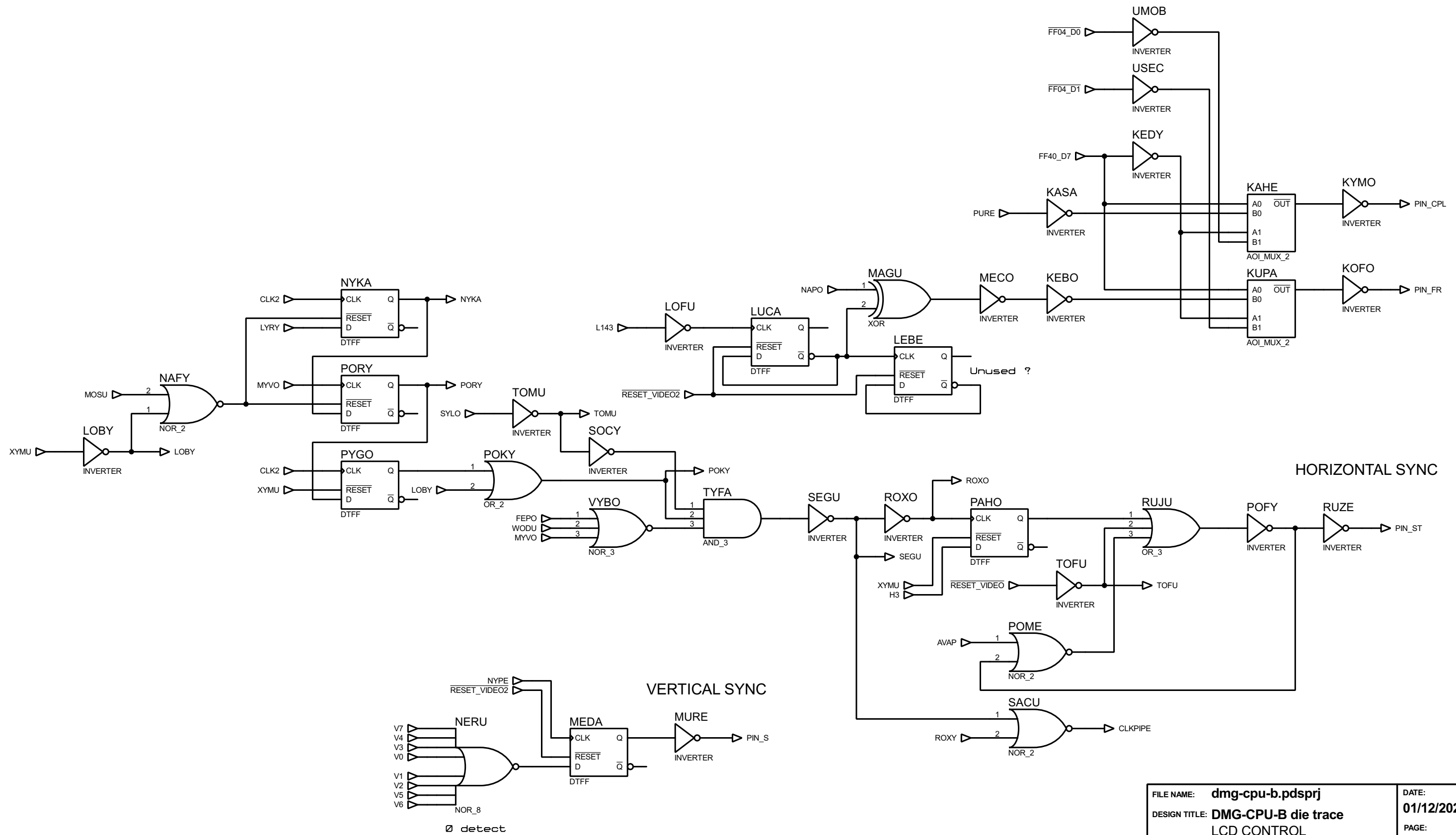




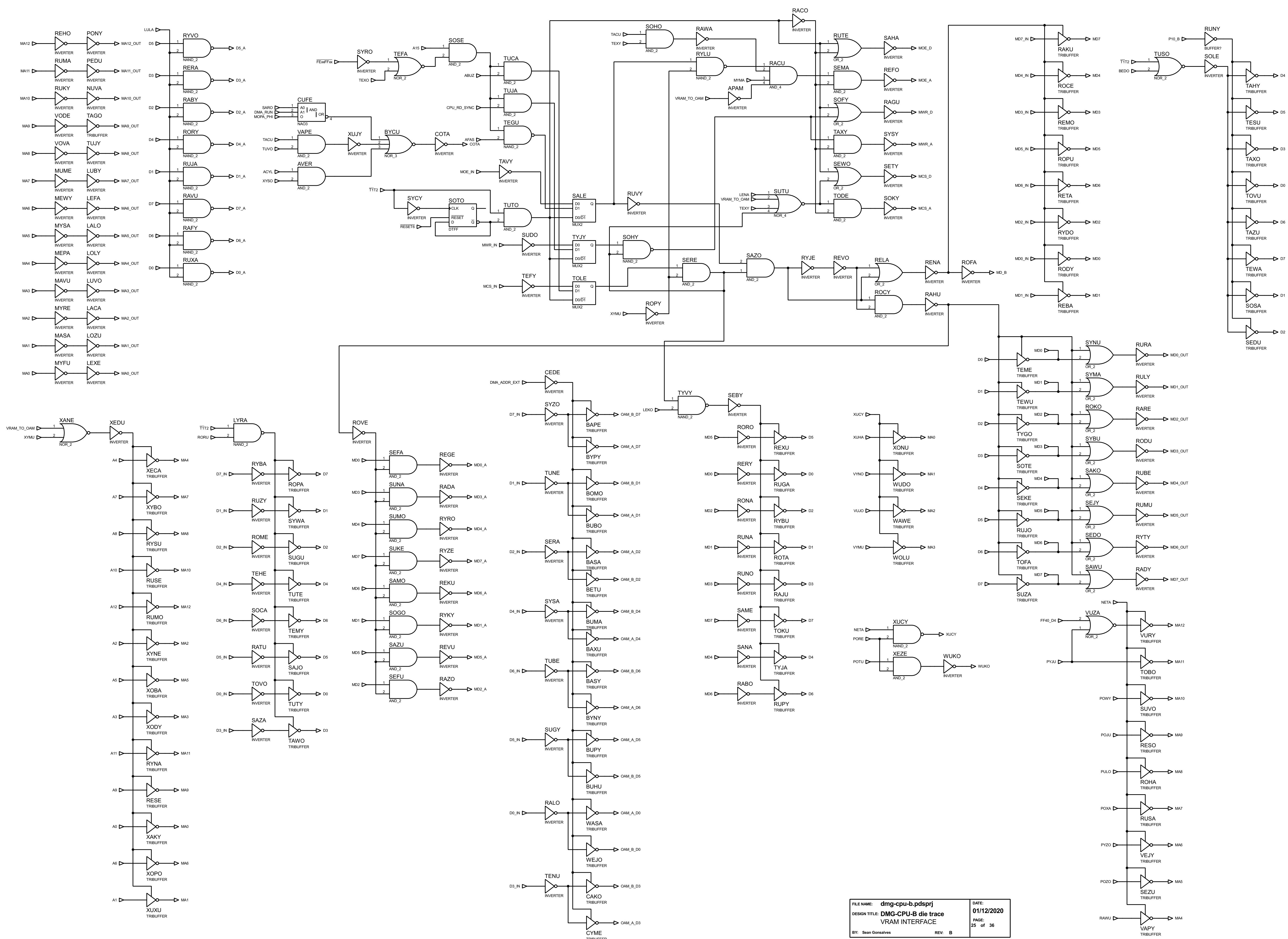


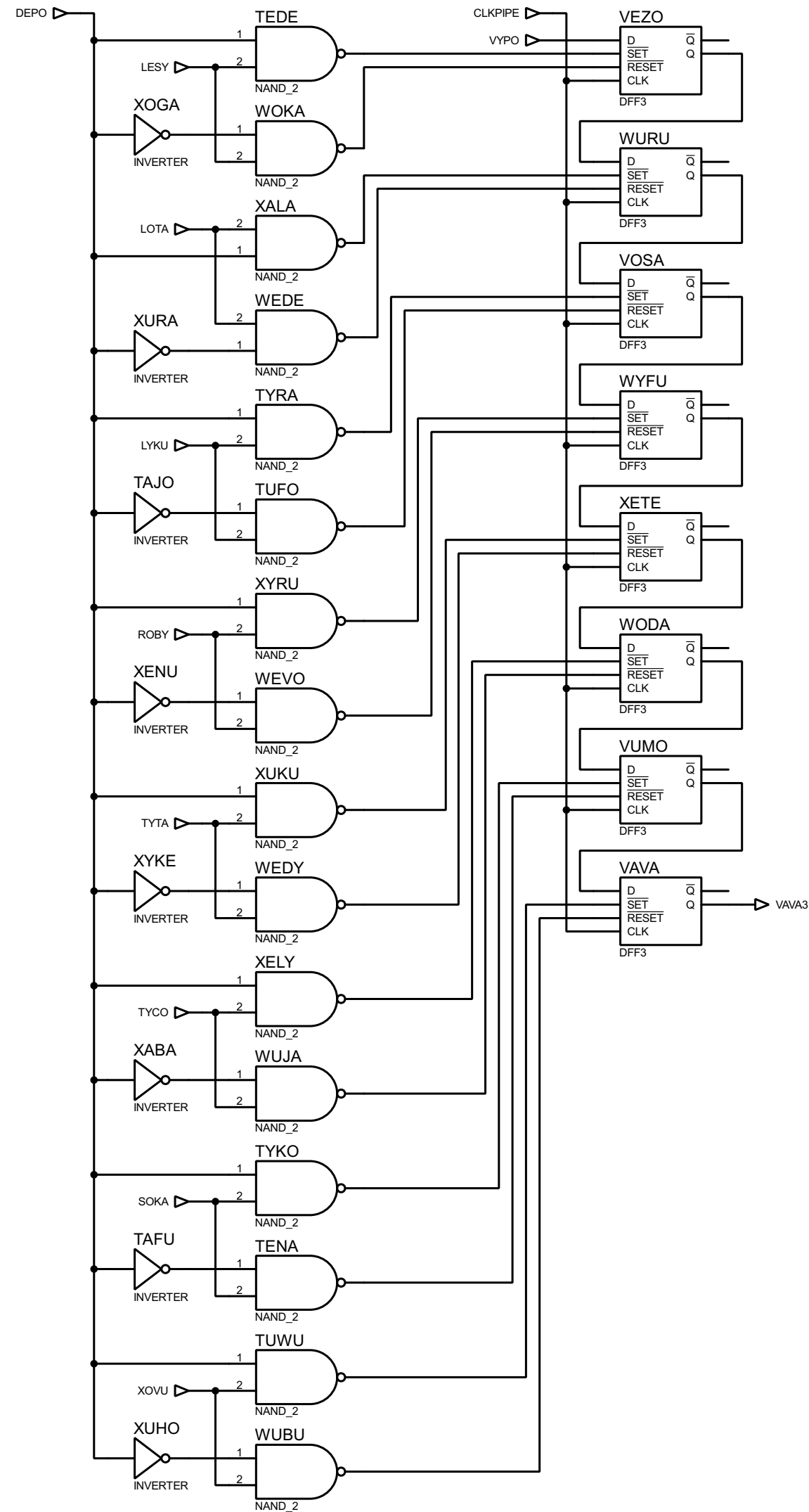
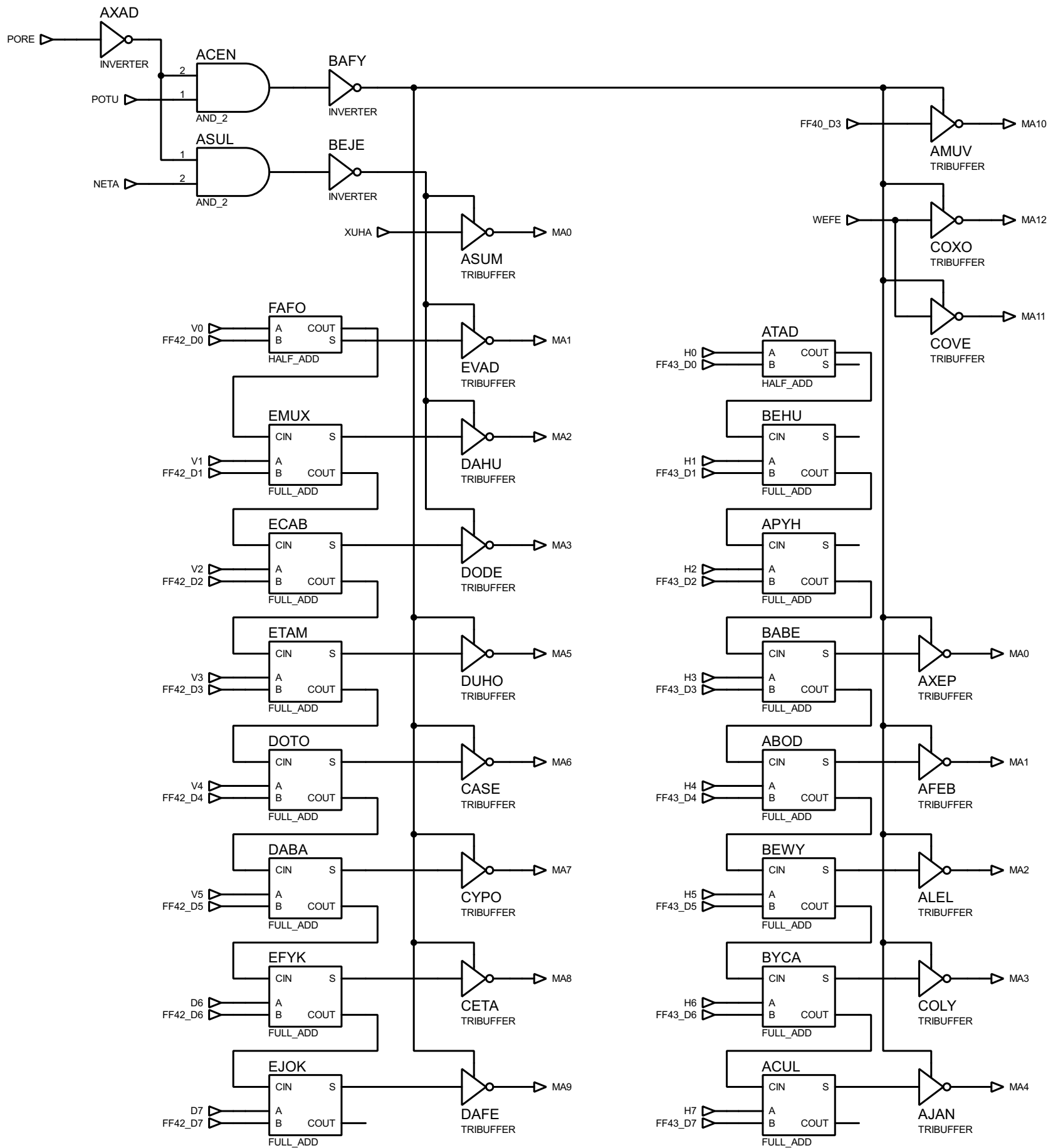


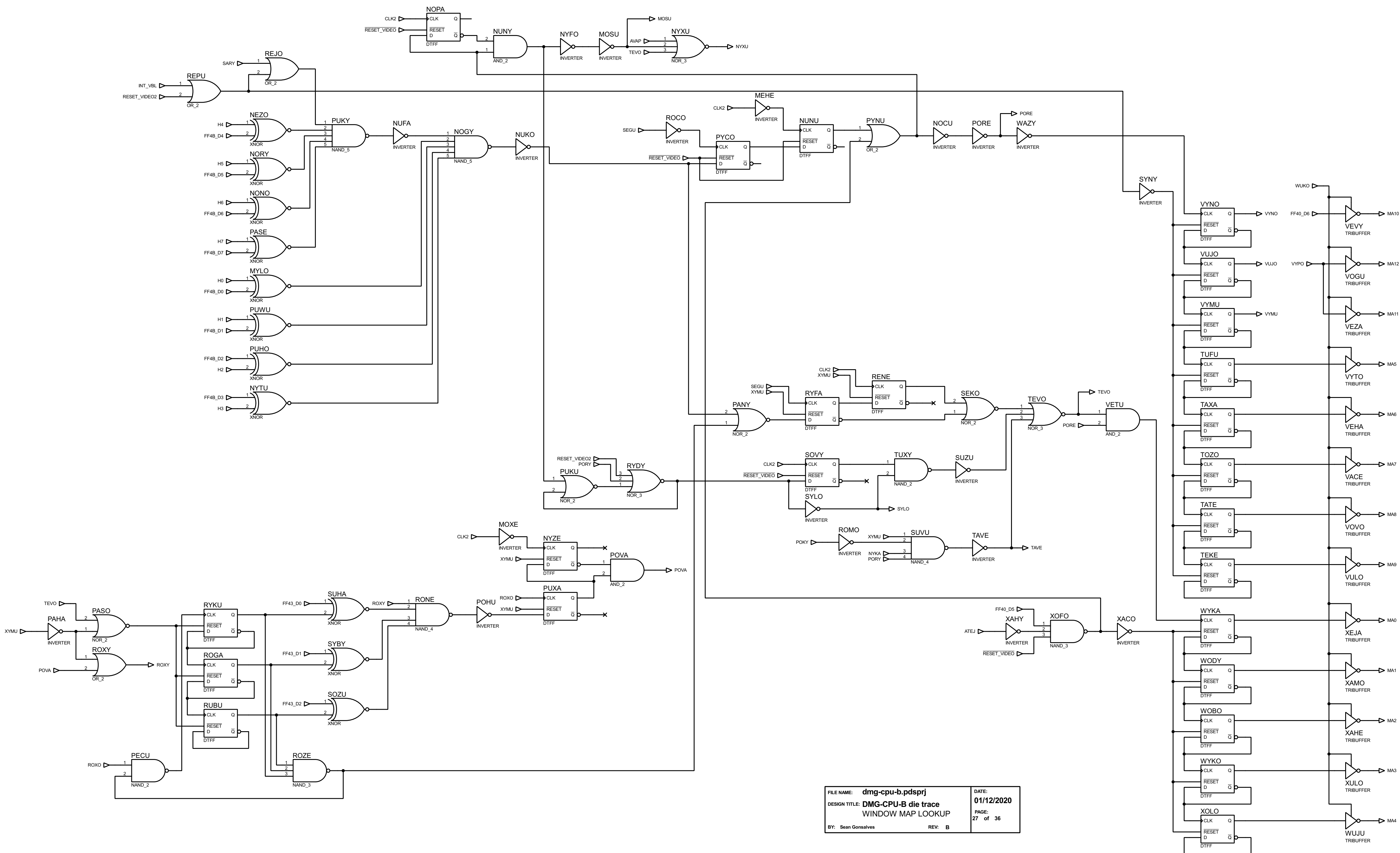
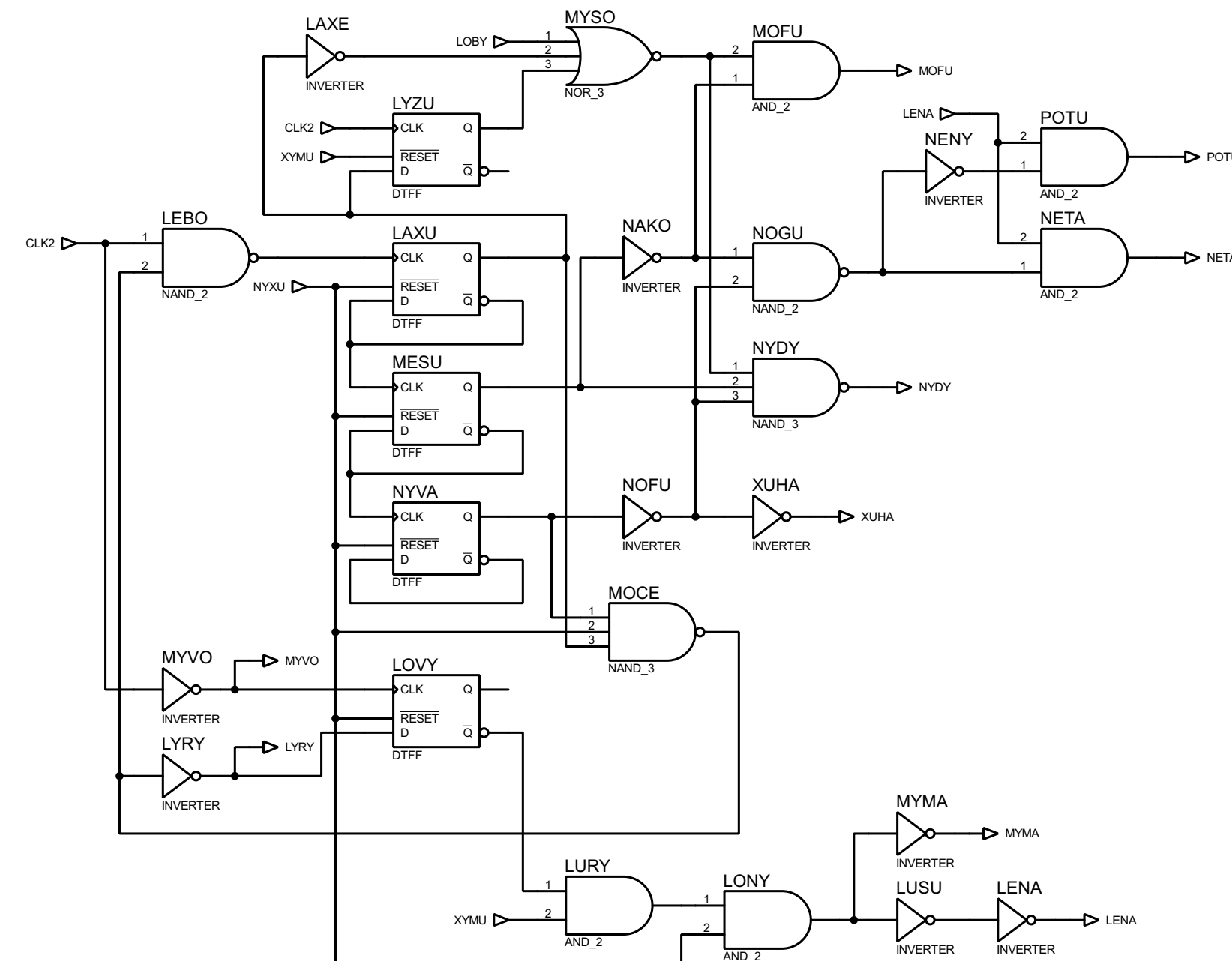
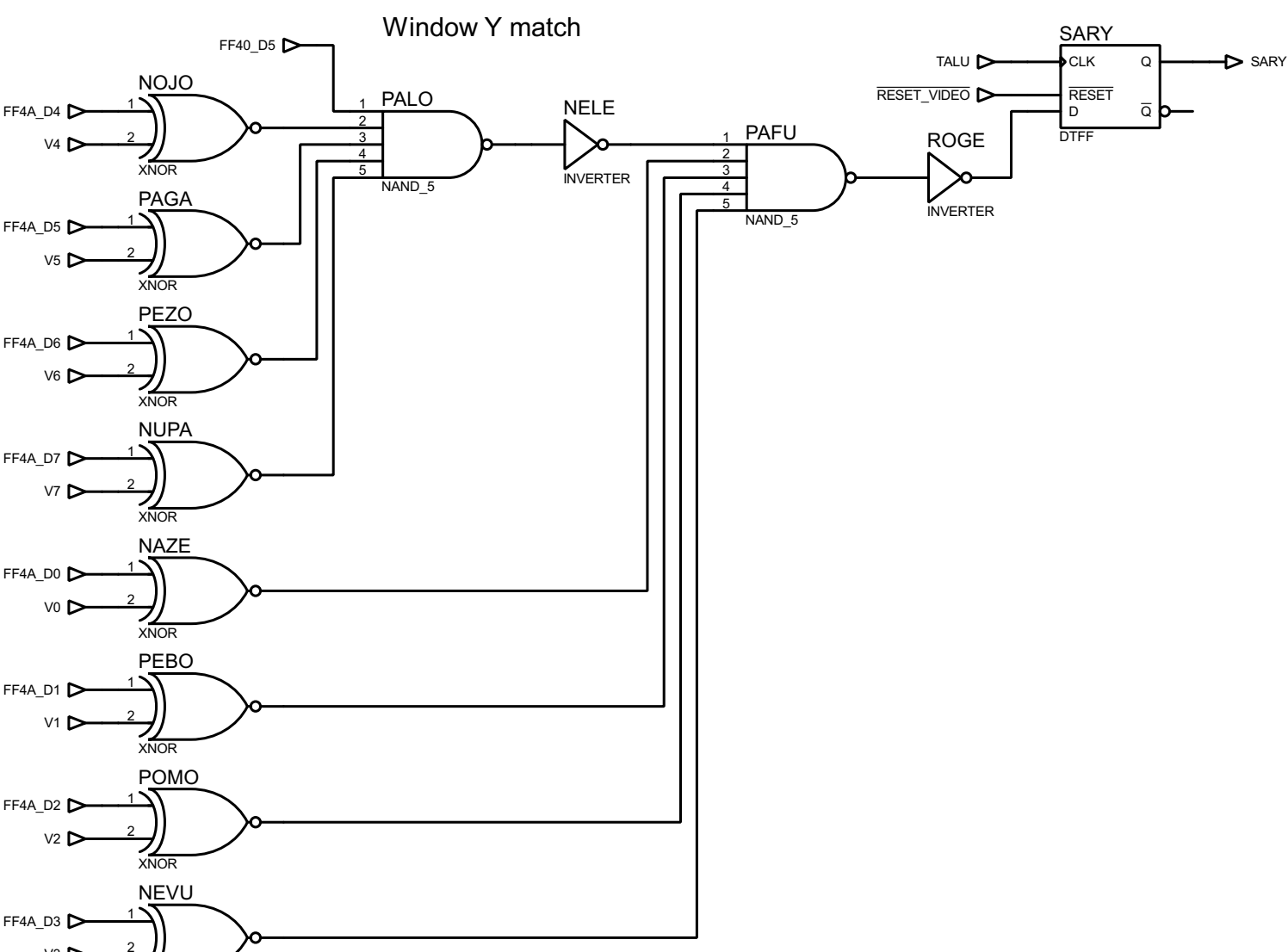
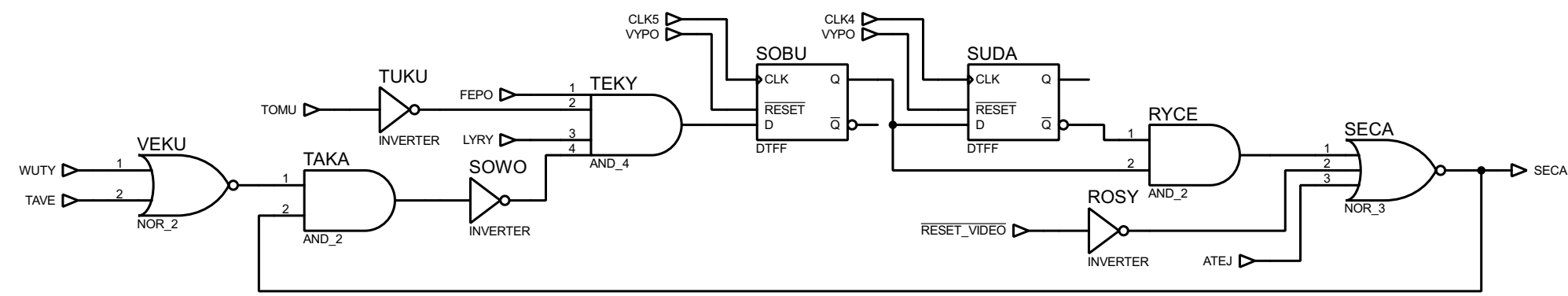


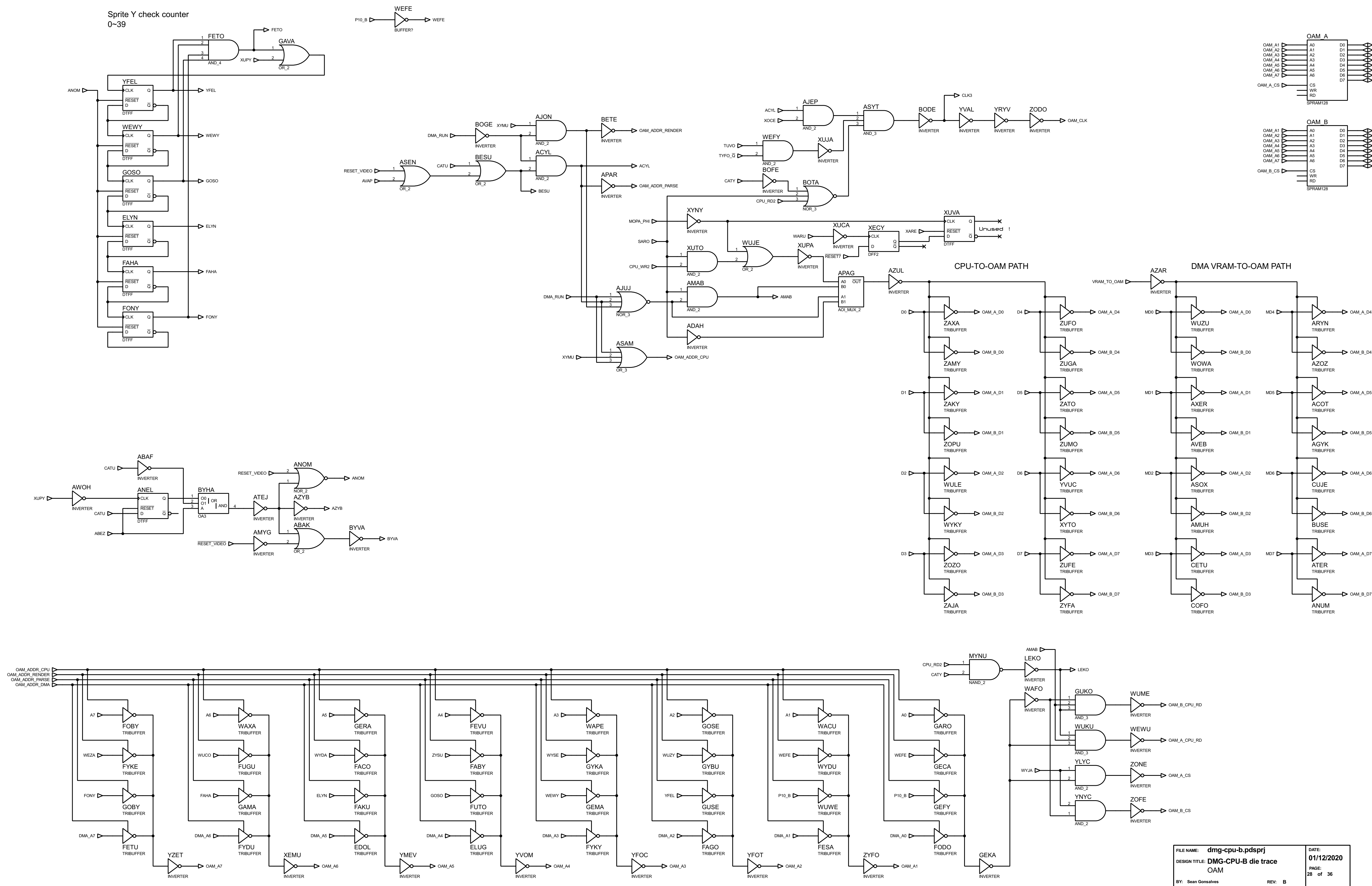


FILE NAME: dmg-cpu-b.pdsprj	DATE: 01/12/2020
DESIGN TITLE: DMG-CPU-B die trace	PAGE: 24 of 36
LCD CONTROL	
BY: Sean Gonsalves	REV: B

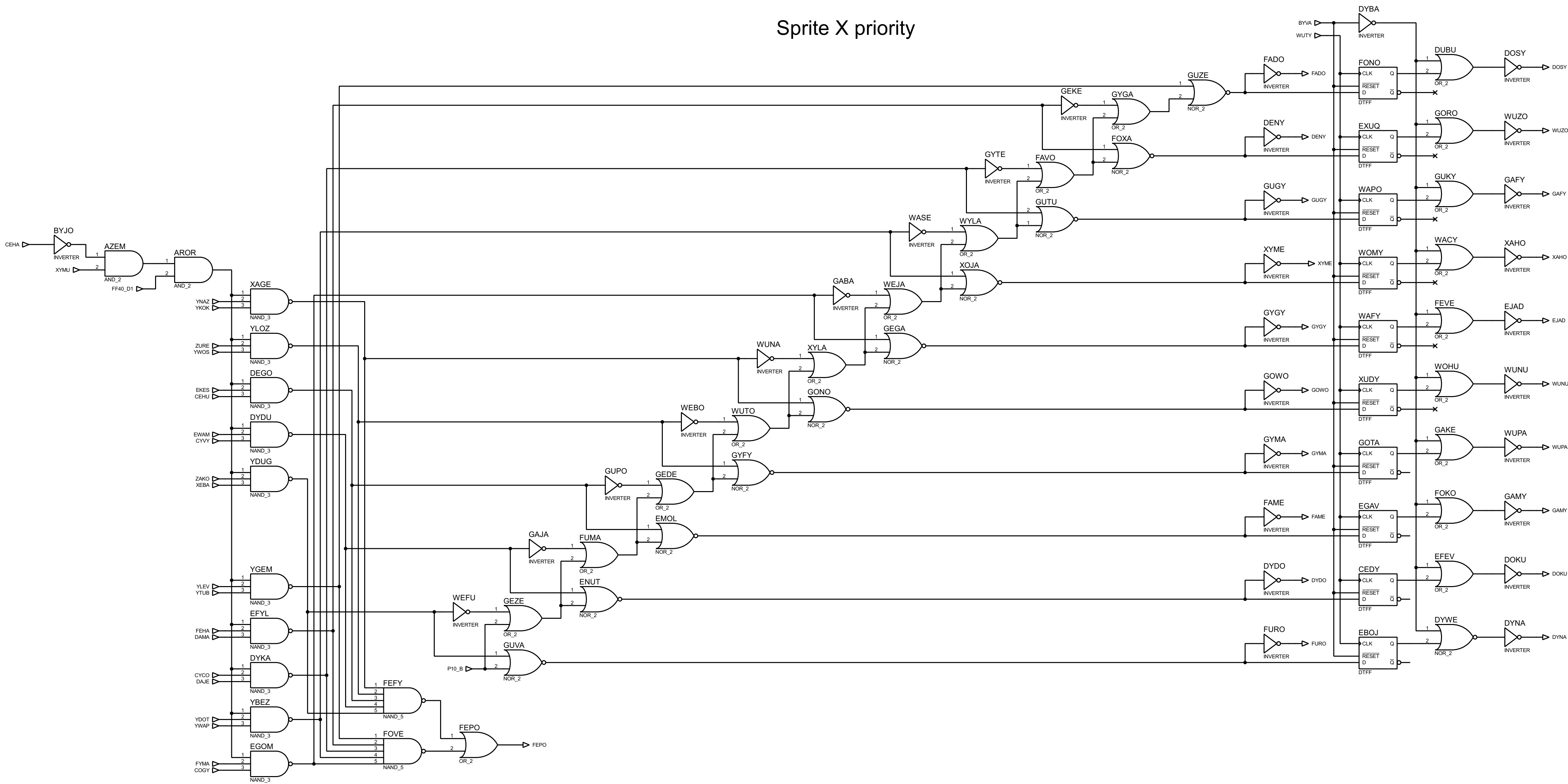




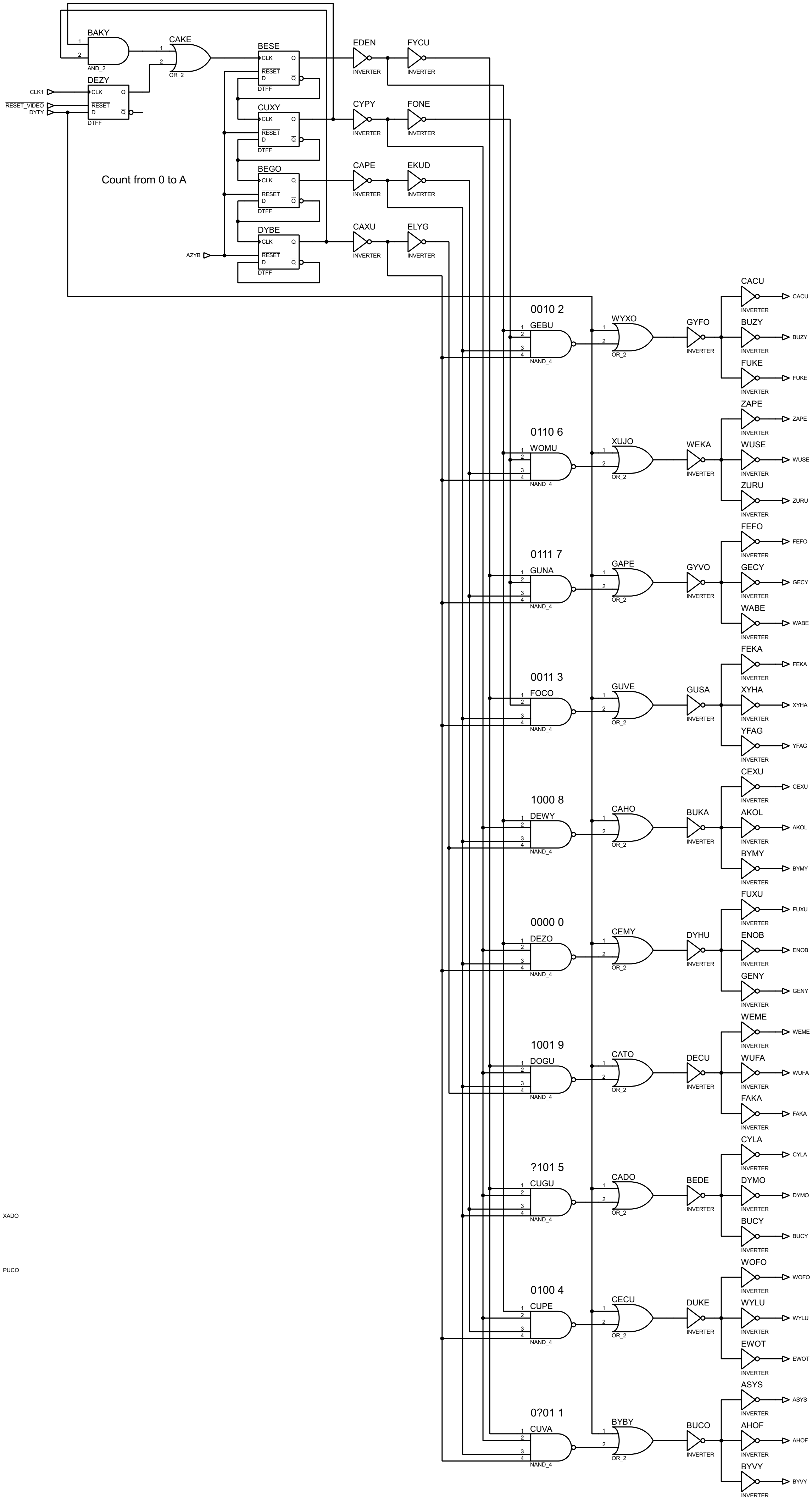
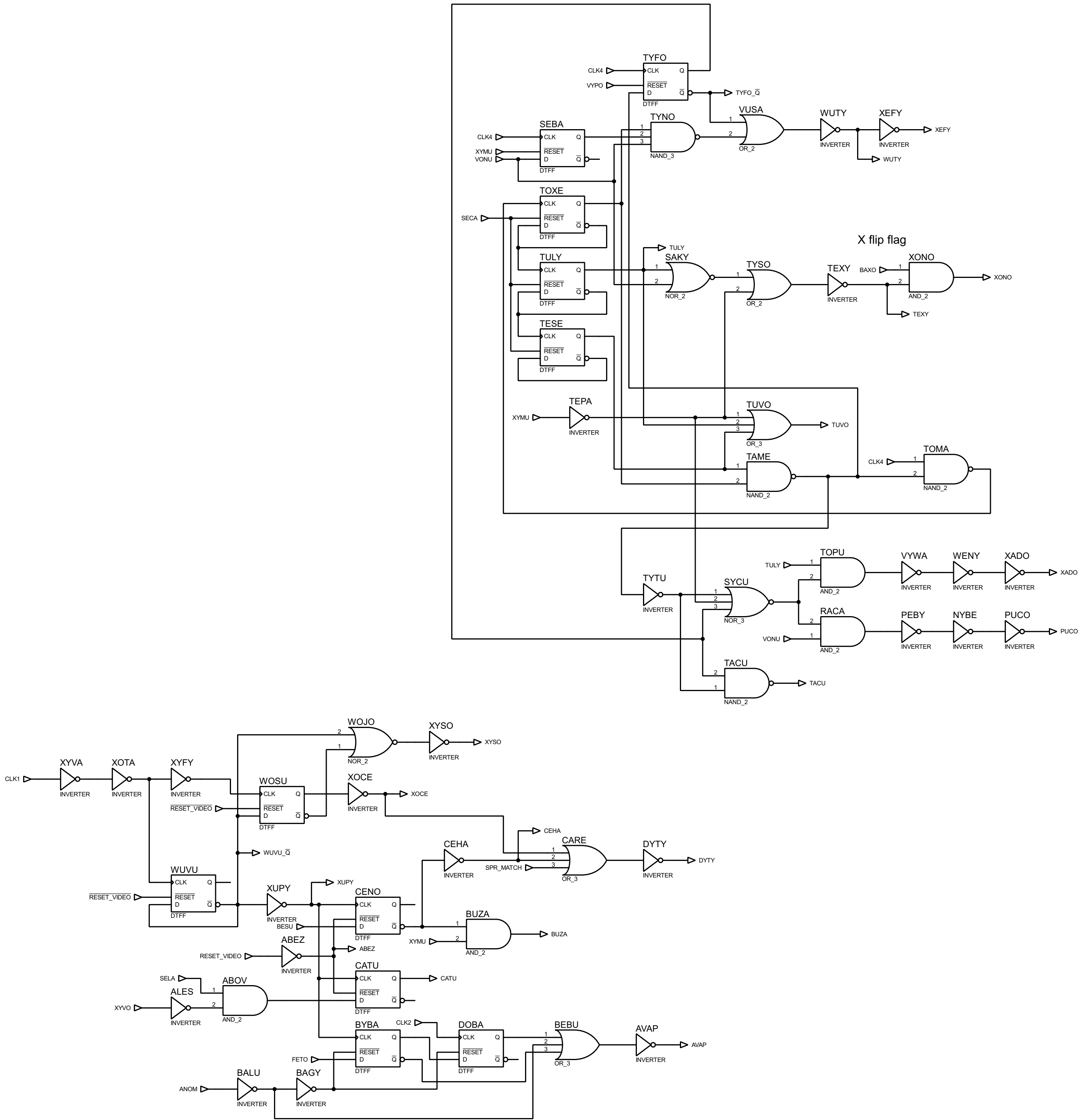
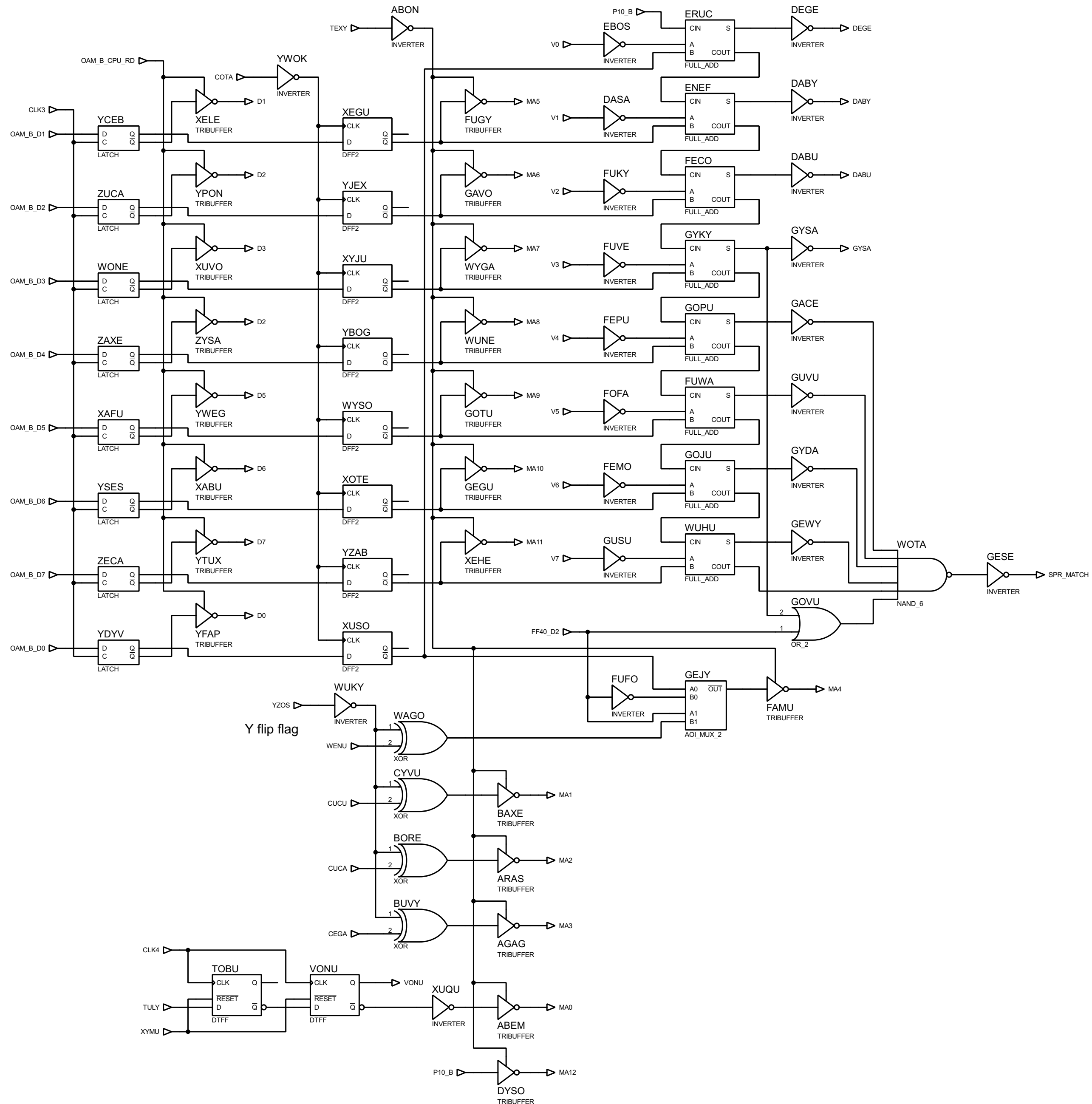




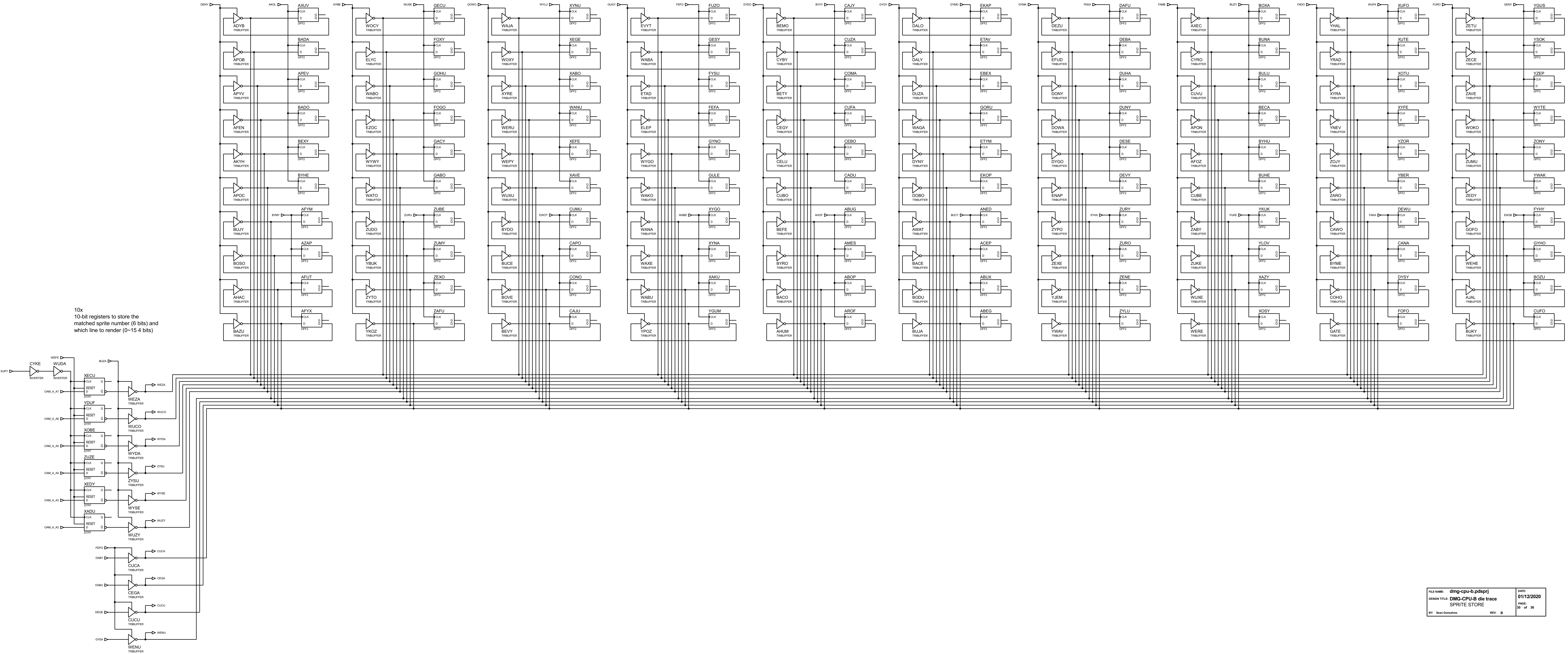
Sprite X priority

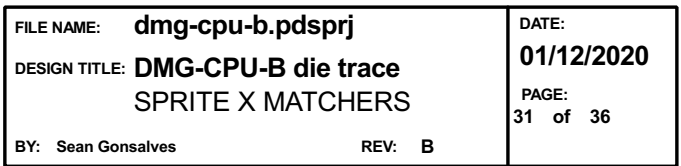


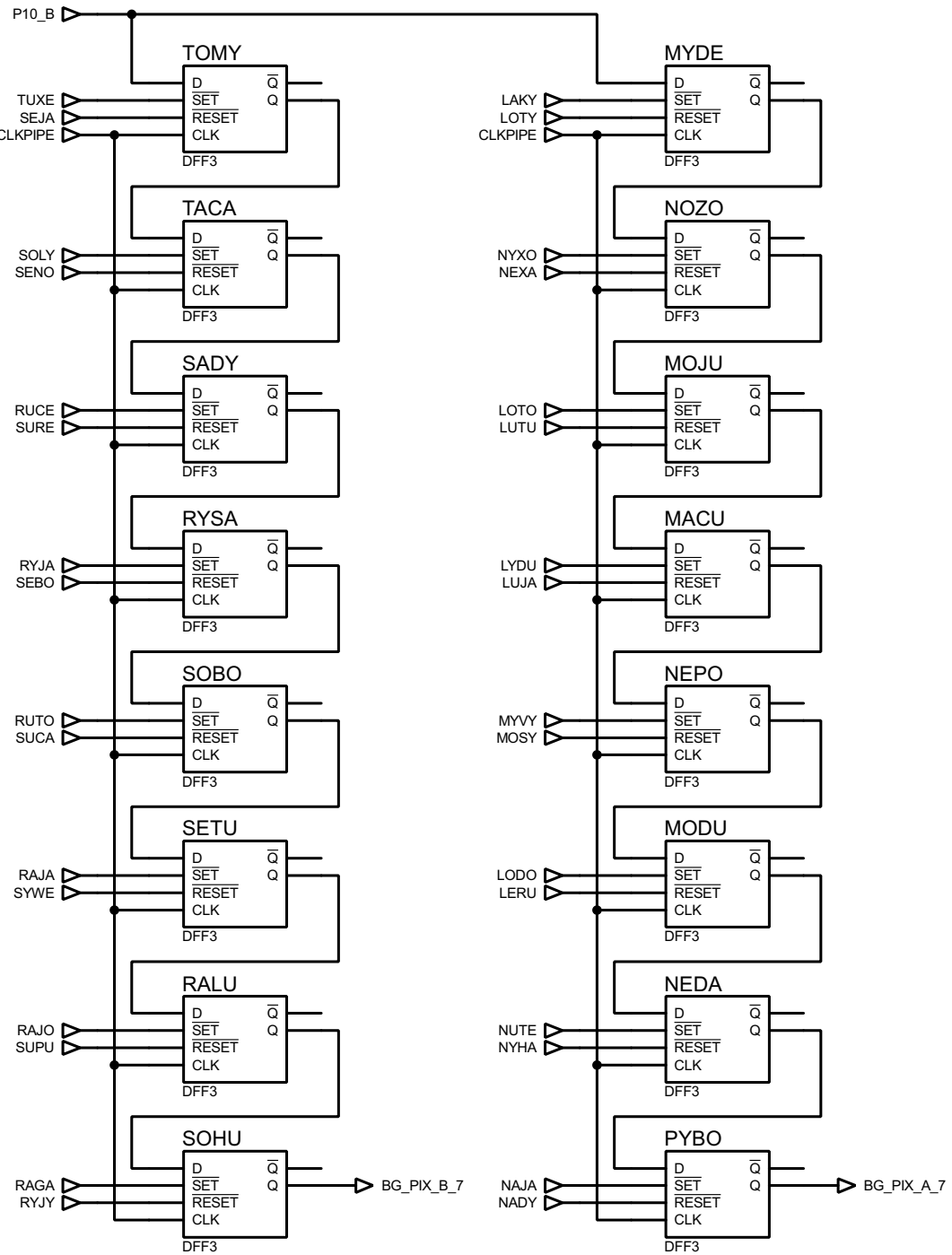
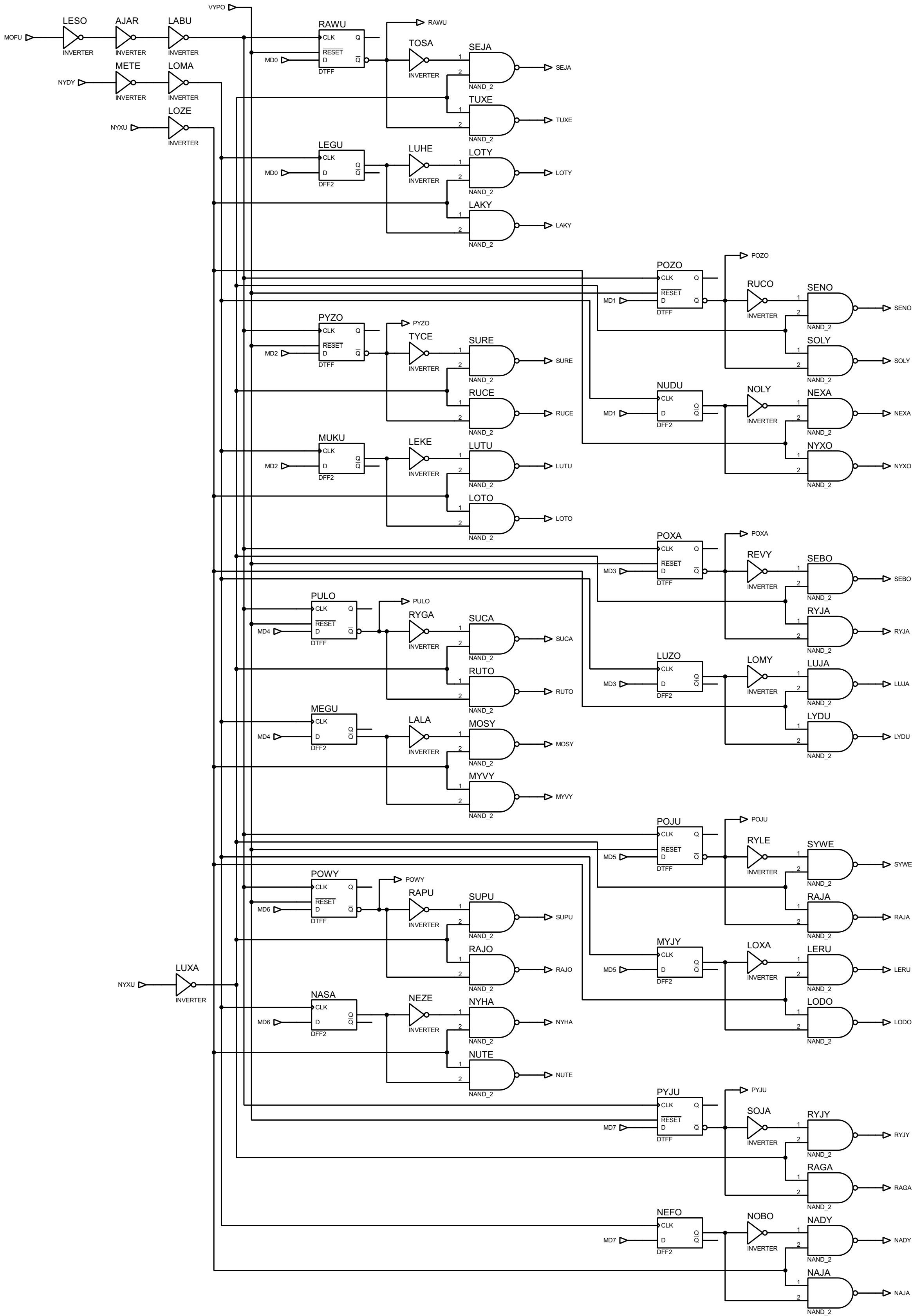
Sprite Y comparator

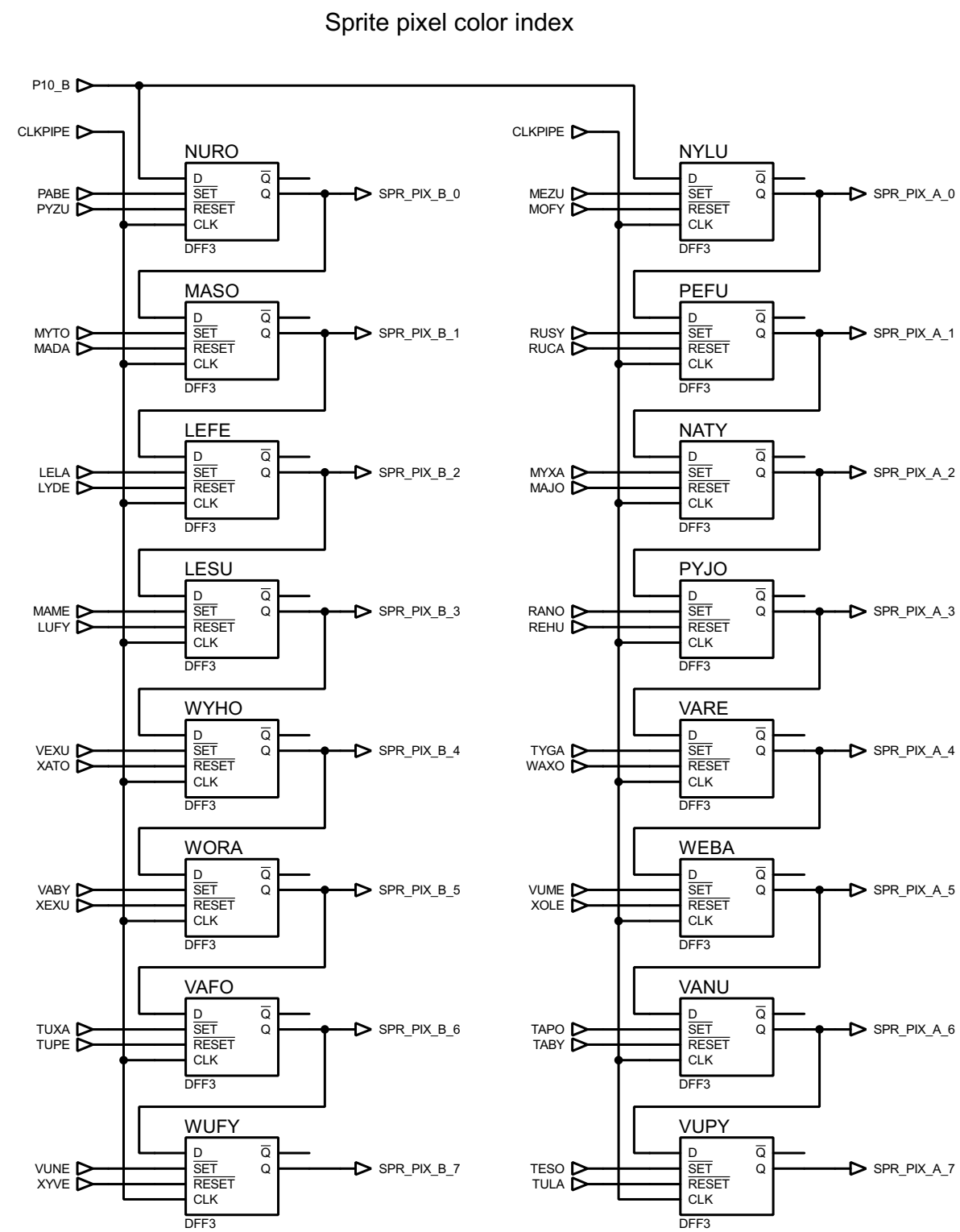
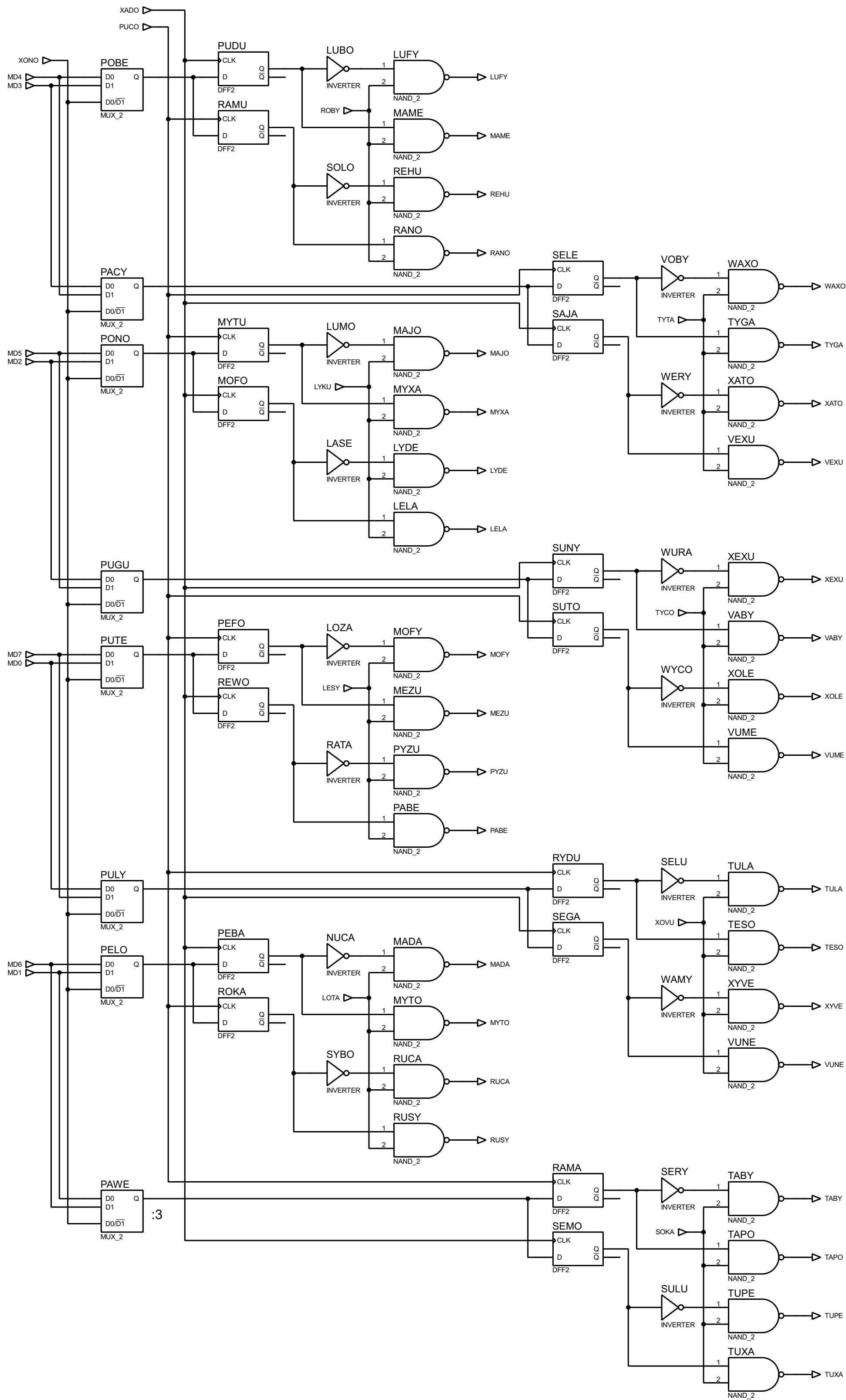


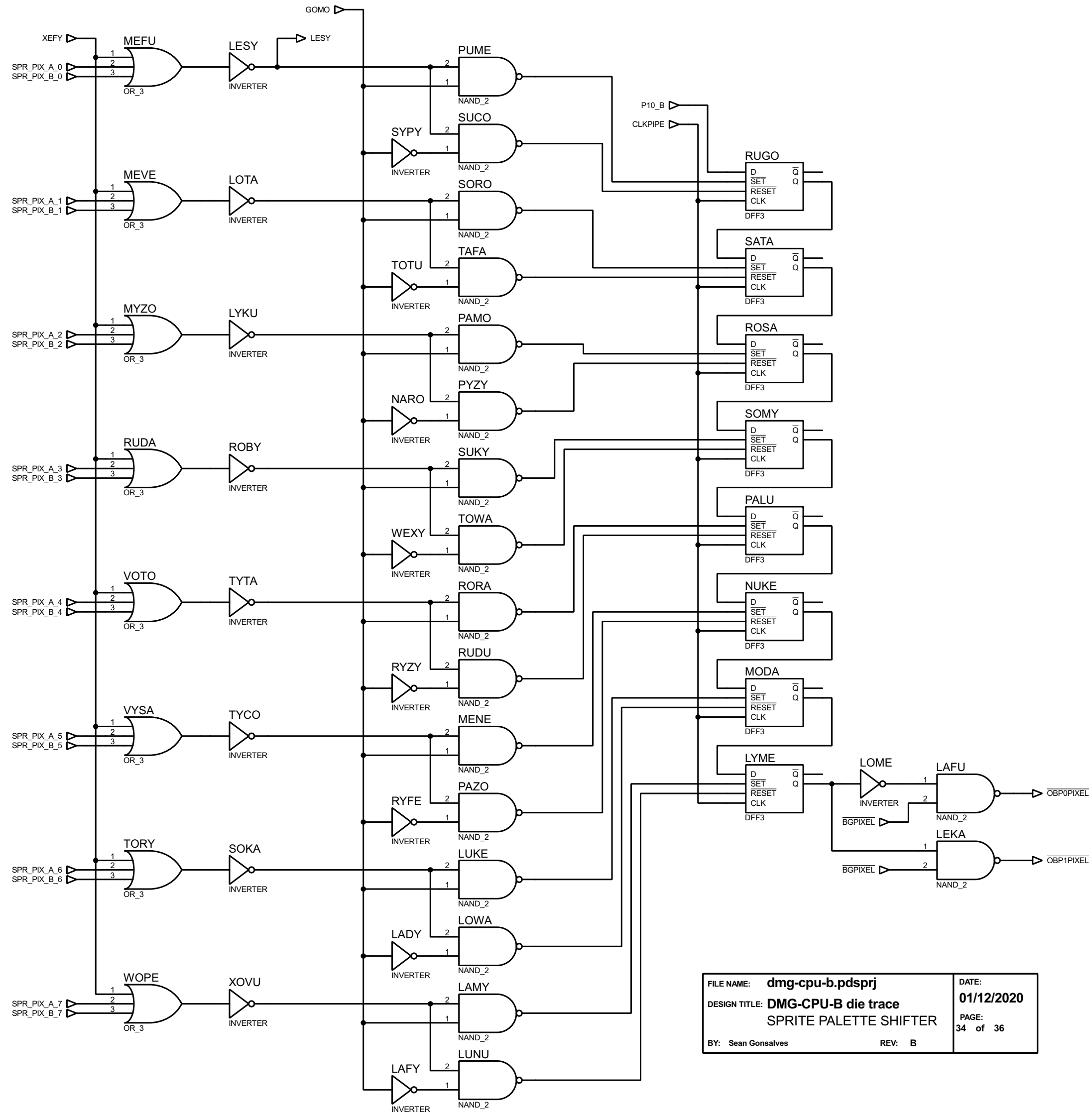
10x
10-bit registers to store the
matched sprite number (6 bits) and
which line to render (0-15 4 bits)



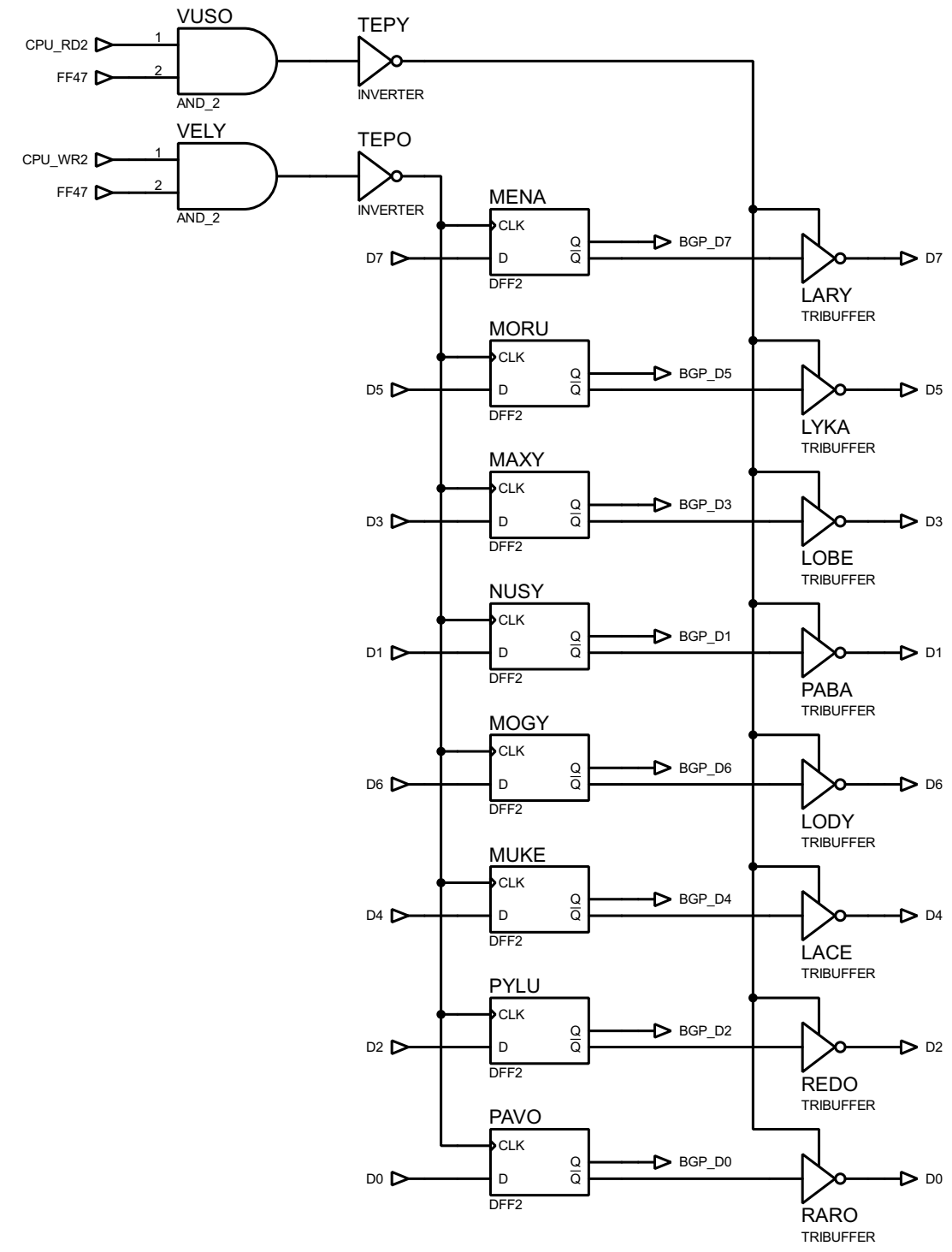




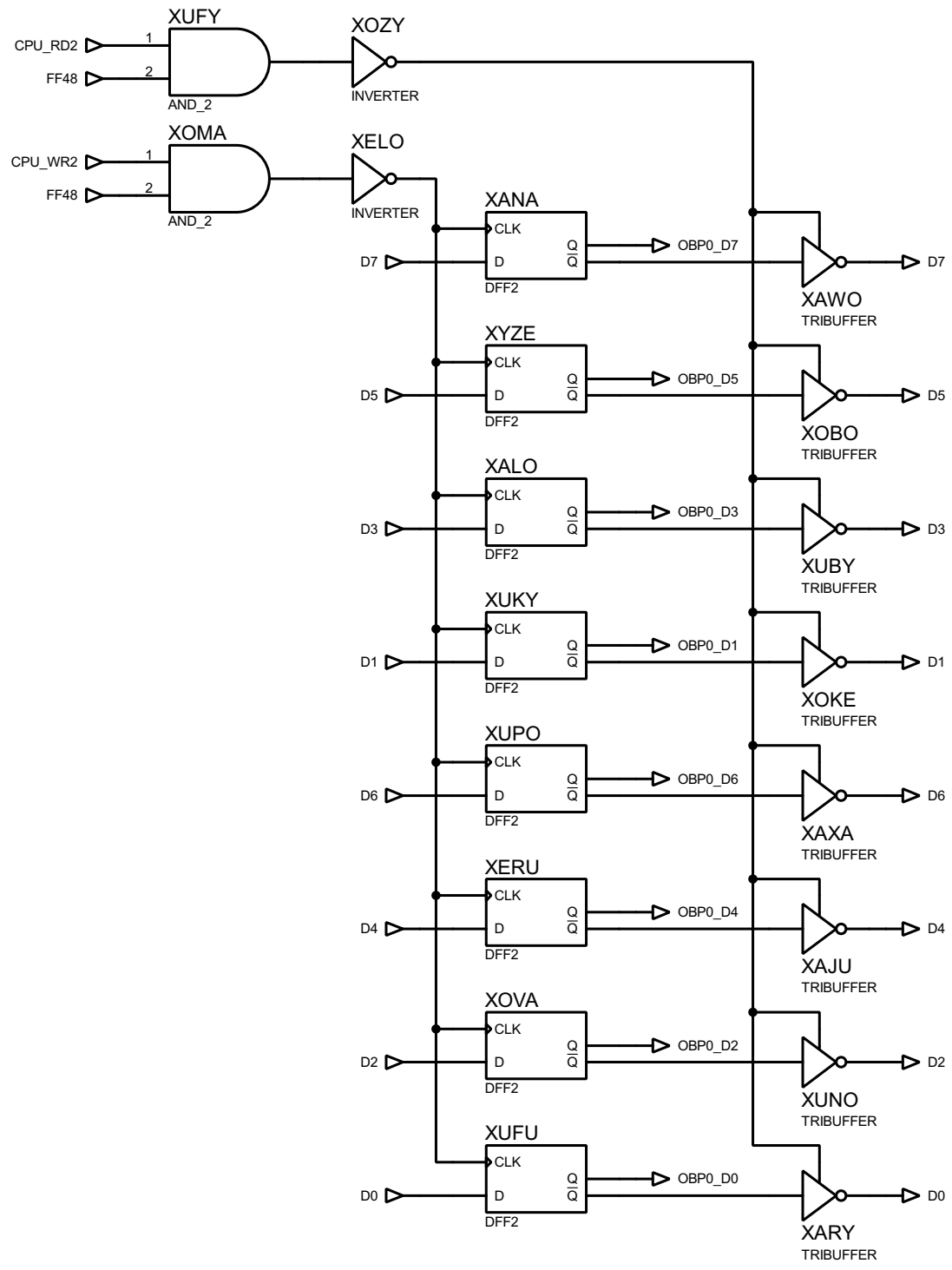




FF47 BGP



FF48 OBP0



FF49 OBP1

