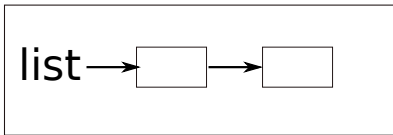


# Memory



BUS

l->next = list

CPU

l->next = list

CPU